



SB201-A6

**Storage Barebone
User's Manual**

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Document Release History

Release Date	Version	Update Content
April, 2023	1	User's Manual release to public.
May, 2023	1.1	Update rear panel (PCIe slot FHHL).
June, 2024	1.2	Update cable connection to BP.



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Preface

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Changes

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Warning

1. A shielded-type power cord is required in order to meet FCC emission limits and also to prevent interference to the nearby radio and television reception. It is essential that only the supplied power cord be used.
2. Use only shielded cables to connect I/O devices to this equipment.
3. You are cautioned that changes or modifications not expressly approved by the party responsible for compliance could void your authority to operate the equipment.

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Instruction Symbols

Special attention should be given to the instruction symbols below.



NOTE

This symbol indicates that there is an explanatory or supplementary instruction.



CAUTION

This symbol denotes possible hardware impairment. Upmost precaution must be taken to prevent serious hardware damage.



WARNING

This symbol serves as a warning alert for potential body injury. The user may suffer possible injury from disregard or lack of attention.

Safety Instructions

Before you commence, please attentively read the following important discretions below. All cautions and warnings on the equipment or in the manuals should be circumspectly noted and reviewed.

Always ground yourself to prevent static electricity.

請全程接地，以防止靜電。

请全程接地，以防止静电。

Всегда заземляйте себя, чтобы избежать статического электричества.

Aard jezelf altijd om statische elektriciteit te voorkomen.

- Firmly ground yourself at all times when installing or assembling the internal components of the server. Most of electronic components in the server are highly sensitive to electrical static discharge.
- Use a solid grounding wrist strap and distinctively place all electronic components in static-shielded devices to prevent static. Grounding wrist straps can be purchased in any electronic supply store.
- Confirm that the power source is turned off and then disconnect the power cords from your system before performing any type of installation or manual servicing. A sudden surge of power could severely damage the sensitive electronic components.
- Do not precipitously open the system's top cover. If you must open the cover for maintenance purposes, only a trained technician should be allowed to proceed this action. Integrated circuits on computer boards are highly sensitive to static electricity. Before operating a board or integrated circuit, touch an unpainted portion of the system unit chassis for a couple of seconds to discharge any static electricity on your body.

Place the server in a stable environment.

請將伺服器放置在穩定的環境中。

请将伺服器放置在稳定的环境中。

Поместите сервер в стабильную среду.

Plaats de server in een stabiele omgeving.

- Place this equipment on a stable surface when installing. A small mild drop or fall could cause fatal injury to both the equipment and the person handling the equipment.
- Please keep this equipment away from humidity to prevent vast rust and disintegration.
- Carefully and accurately mount the equipment into the rack. Uneven mechanical loading may lead to hazardous consequences.
- This equipment is to be installed for operation in an environment with maximum ambient temperature below 35°C.
- Review the environment before performing any installation or servicing. Keep the equipment away from hazardous and uneven grounds.
- This server must be installed only in Restricted Access Locations.

Handle equipment with care.

請謹慎操作設備。

请谨慎操作设备。

Обращайтесь с оборудованием осторожно.

Behandel de apparatuur voorzichtig.

- Do not cover the openings of the system. The openings on the system are for air convection, which intentionally protect the equipment from overheating.
- Never pour any liquid into ventilation openings of the system. This could cause catastrophic fire or electrical shock.

- Ensure that the voltage of the power source is within the specification on the label when connecting the equipment to the power outlet. The current load and output power of loads must be within the specification.
- This equipment must be firmly connected to reliable grounding before usage. Pay special attention to power supplied other than direct connections, e.g. using of power strips.
- Place the power cord out of the way of foot traffic. Do not place anything over the power cord. The power cord must be rated for the product, voltage and current marked on the product's electrical ratings label. The voltage and current rating of the cord should be greater than the voltage and current rating marked on the product.

Pay attention to hardware maintenance.

注意硬體維護。

注意硬體维护。

Обратите внимание на обслуживание оборудования.

Besteed aandacht aan hardware-onderhoud.

- If the equipment is not used for a long time, disconnect the equipment from mains to avoid being damaged by transient over-voltage.
- Module and drive bays must not be empty. They must have a dummy cover.
- Never open the equipment without professional assistance. For safety reasons, only qualified service personnel should open the equipment.
- If one of the following situations arise, the equipment should be checked and tested by service personnel:
 1. The power cord or plug is damaged.
 2. Liquid has penetrated the equipment.
 3. The equipment has been exposed to moisture.
 4. The equipment does not work well or will not work according to its user manual.
 5. The equipment has been dropped and/or damaged.
 6. The equipment has obvious signs of breakage.
 7. Please disconnect this equipment from the AC outlet before cleaning. Do not use liquid or detergent for cleaning. The use of a moisture sheet or cloth is recommended for cleaning.



CAUTION

The equipment intended for installation should be placed in Restricted Access Location.



CAUTION

There will be a risk of explosion if battery is replaced by an incorrect type. Dispose of used batteries according to the instructions. After performing any installation or servicing, make sure the enclosure is correct in position before turning on the power.



CAUTION

This unit may have more than one power supply. Disconnect all power sources before maintenance to avoid electric shock.



About This Manual

Thank you for choosing and purchasing SB201-A6 Storage Server Barebone.

This user's manual is prepensely designed for professional technicians to perform dexterous hardware setup, basic system configurations, and quick software startup. This document pellucidly presents a brief overview of the product design, equipment installation, and firmware settings for our product SB201-A6. For the latest version of this user's manual, please refer to the AIC® website: <https://www.aicipc.com/en/productdetail/51377>.

Chapter 1 Product Features

SB201-A6 is a flexible storage barebone that is specifically designed to accommodate diverse corporations and enterprises for managing heavy workloads and multiple applications.

Chapter 2 Hardware Setup

This chapter displays an easy installation guide for assembling the hardware in this product. Utmost caution for proceeding to set up the hardware is highly advised. Most of the components are highly fragile and vulnerable to exterior influence. Do not endanger the device by placing the device in an unstable environment.

Chapter 3 Hardware Settings

This chapter elaborates the overall layout of the system board which may vary according to the system. These descriptions assist professional technicians to configure different settings of the motherboard and confirm the location of each connector and jumper.

Chapter 4 BIOS Configuration Settings

This chapter introduces the key features of BIOS, including the descriptions and option keys for diverse functions. These details provide users to effortlessly navigate and configure the input/output devices.

Chapter 5 Technical Support

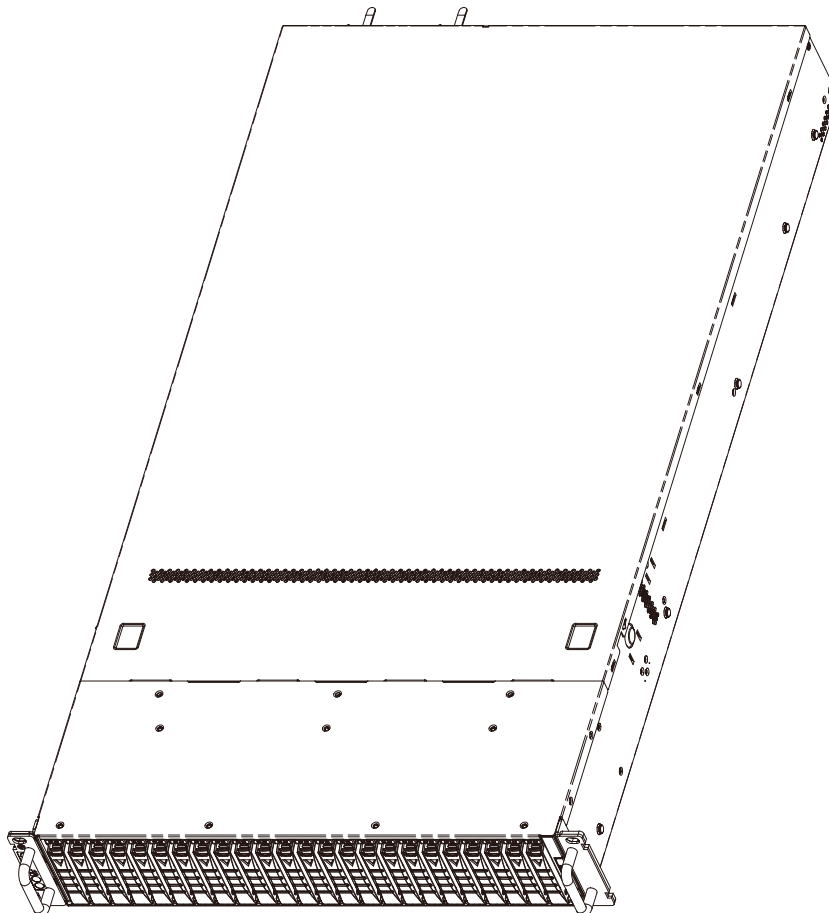
For more information or suggestion, please contact the nearest AIC® corporation representative in your district or visit the AIC® website: <https://www.aicipc.com/en/index>. It is our greatest honor to provide the best service for our customers.

Chapter 1. Product Features

SB201-A6 is a 2U high density storage server that includes motherboard, chassis, power supply, and SSD backplane. For more information about our product, please visit our website at <https://www.aicipc.com/en/index>.

Before removing the subsystem from the shipping carton, visually inspect the physical condition of the shipping carton. Exterior damage to the shipping carton may indicate that the contents of the carton are damaged. If any damage is found, do not remove the components; contact the dealer where the subsystem was purchased for further instructions. Before continuing, first unpack the subsystem and verify that the number of components in the shipping carton is accurate and in good condition.

- 2U general purpose storage server supports 24 hot swap 2.5" universal NVMe U.2 drive bays
- Supports 3rd Gen. Intel® Xeon® Scalable processors (codename Ice Lake)
- Onboard Baseboard Management Controller for system management and IPMI control
- Front-to-back airflow and hot swap redundant fans to provide optimal thermal conditions
- Dedicated BMC management port



1.1 Box Contents

This product contains the components listed below.

Please confirm the number and the condition of the components before installation.

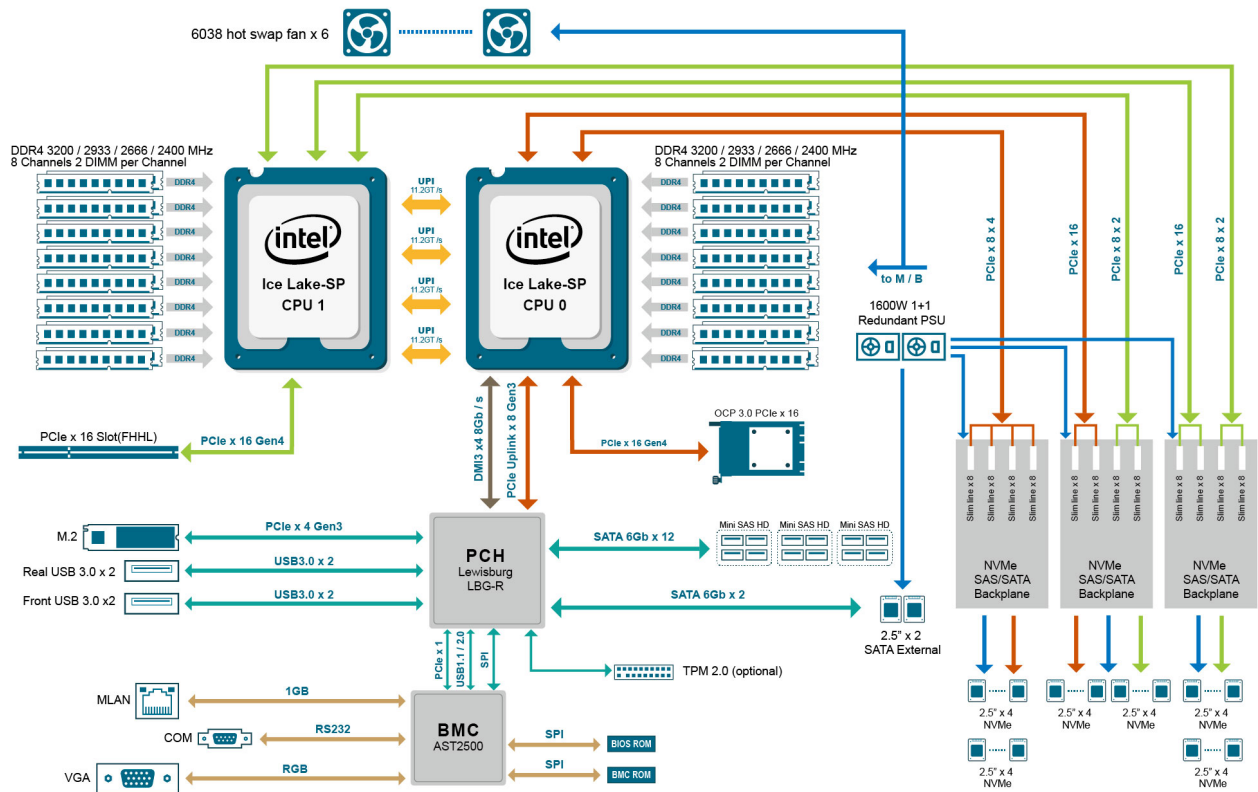
Pre-installed into the system			Number
✓	1600W redundant power supply 80+ Platinum		1+1
✓	External	2.5" hot swap disk drive tray (front)	24
		2.5" disk drive tray (rear)	2
✓	Internal	1 x M.2(NGFF)/ M-Key/ 2280	1
✓	Heat sink		1
✓	Easy swap fans 6 x 60x38mm		6
✓	AIC® motherboard		1
✓	3 x 8-port SAS/SATA/NVMe backplanes with 2 x SFF-8643 + 2 x SFF8654		1
Accessory Item			Number
✓	EPE foam for front board: 563*420*105H		1
✓	EPE foam for rear board: 563*420*105H		1
✓	EPE foam for front tray: 563*345*145H		1
✓	EPE foam for rear tray: 563*345*145H		1
✓	EPE foam for M/B: 560*205*60H		1
✓	EPE pad: 140*100*25T (for rail box)		2
✓	EPE pad: 183*100*45T (for HSK box)		2
✓	Power cord		vary per region
✓	28-inch tool-less slide rail assembly		1

Product features are subject to change without notice.

1.2 Specifications

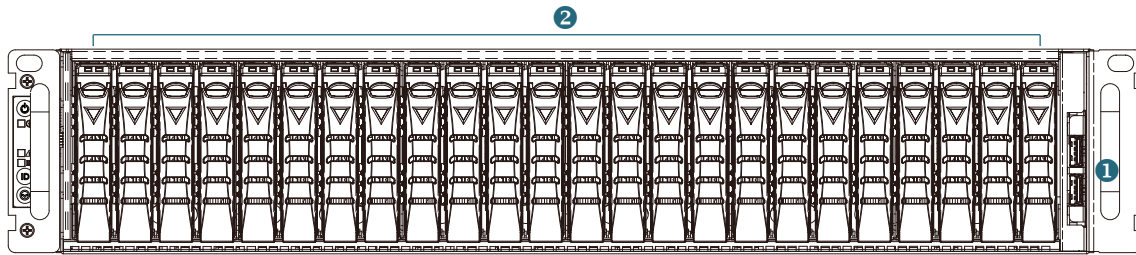
Dimensions (W x D x H)	mm : 438 x 800 x 88			Riser Card (included)	2U2S_RB24_G4 x2	1 x16 + 1 x8 PCIe slots	
	inches : 17.2 x 31.4 x 3.1						
Motherboard	AIC Server Board			System BIOS	BIOS Type	AMI UEFI BIOS	
Processor	Processor Support	3rd Gen Intel® Xeon® Scalable Processors (Ice Lake) <i>*Please contact AIC Technical Support for more info/details about optimized CPUs and specialized system.</i>			BIOS Features	• ACPI • PXE • AC loss recovery • IPMI KCS interface • SMBIOS • Serial console redirection • BIOS Boot Specification • BIOS Recovery Mode	
	UPI Speeds	11.2GT/s					
	Socket Type	1+1 Socket P+ (LGA-4189)					
Chipset Support	Intel® C621A						
System Memory	32 x DDR4 3200Mhz • Total 16+16 DIMM slots (2DPC) • Up to 12TB 3DS ECC RDIMM/LRDIMM • Intel® Optane™ Persistent Memory 200 Series support			On-board Devices	SATA	Intel® Lewisburg PCH on-chip solution • 14 x SATA 6.0 Gb/s (3 Mini-SAS HD, SATA DOM, SATA DOM or M.2) • RAID 0, 1, 5, 10 support by Intel RSTe	
Front Panel	• System power on/off • System reset • System ID						
LEDs	Power LED, ID LED, Fail LED: • Power (Secondary) • Warning B: • Power (Primary) • Warning				IPMI	Aspeed AST2500 Advanced PCIe Graphics & Remote Management Processor • Baseboard Management Controller • Intelligent Platform Interface 2.0 (IPMI 2.0) • iKVM, Media Redirection, IPMI over LAN, Serial over LAN • SMASH Support • HTML5 • Redfish	
Drive Bays	External	2.5" hot swap	24 (Gen4 NVMe, 2.5" SAS/SATA) **Please contact AIC for front 2.5" SAS/SATA device supporting request.				
		2.5"	2 (rear)				
	Internal	M.2	1 x M.2(NGFF)/M-Key/2280 (SATA/PCIe)		Network Controllers	Networking options provided via OCP Mezzanine extension	
Backplane	3 x 8-port SATA/SAS/NVMe with 2 x SFF-8643+ 2 x SFF-8654 (Slimline 8i) connectors on each backplane				Graphics	Aspeed AST2500 Advanced PCIe Graphics & Remote Management Processor • PCIe VGA/2D Controller • 1920x1200@60Hz 32bpp	
Expansion Slots	PCIe 4.0	• 1 x16 FHHL • 1 x16 OCP 3.0			Environmental Specifications	• Storage temperature: -10°C(14°F) ~ 60°C(140°F) • Operating temperature: 0°C(32°F) ~ 35°C(95°F) • Storage operating humidity: 5%~95% non-condensing	
Rear I/O	USB	2 x USB 3.0 Type A in double-stack connector				Gross Weight	(w/ PSU & Rail)
	Serial Port	1 x DB9					lbs : 44
Power Supply	1600W 1+1 redundant power supply 80+ Platinum • AC Input : 100~240V,50/60Hz,7-3.5A			Packaging Dimensions	(W x D x H)	mm : 595 x 1115 x 239	
System Cooling	6 x 60x38mm easy swap fans						inches : 23.4 x 43.9 x 9.4
System Management	• IPMI : Aspeed AST2500 Advanced PCIe Graphics & Remote Management Processor • Baseboard Management Controller • Intelligent Platform Interface 2.0 (IPMI 2.0) • iKVM, Media Redirection, IPMI over LAN, Serial over LAN • SMASH Support			Mounting	Standard	28" tool-less slide rail	

1.3 System Block Diagram



1.4 Features

Front Panel

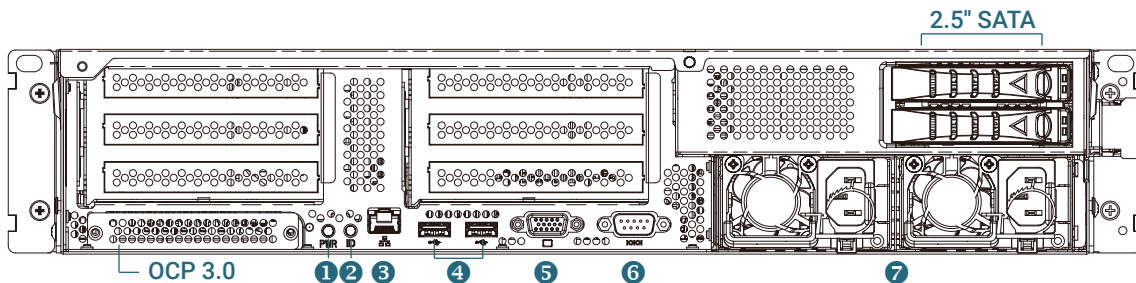


Item	Content	Description
1	USB port	2 USB 3.0 Type A in double-stack connector
2	Drive Tray	24 external 2.5-inch disk drive

LED Indicator

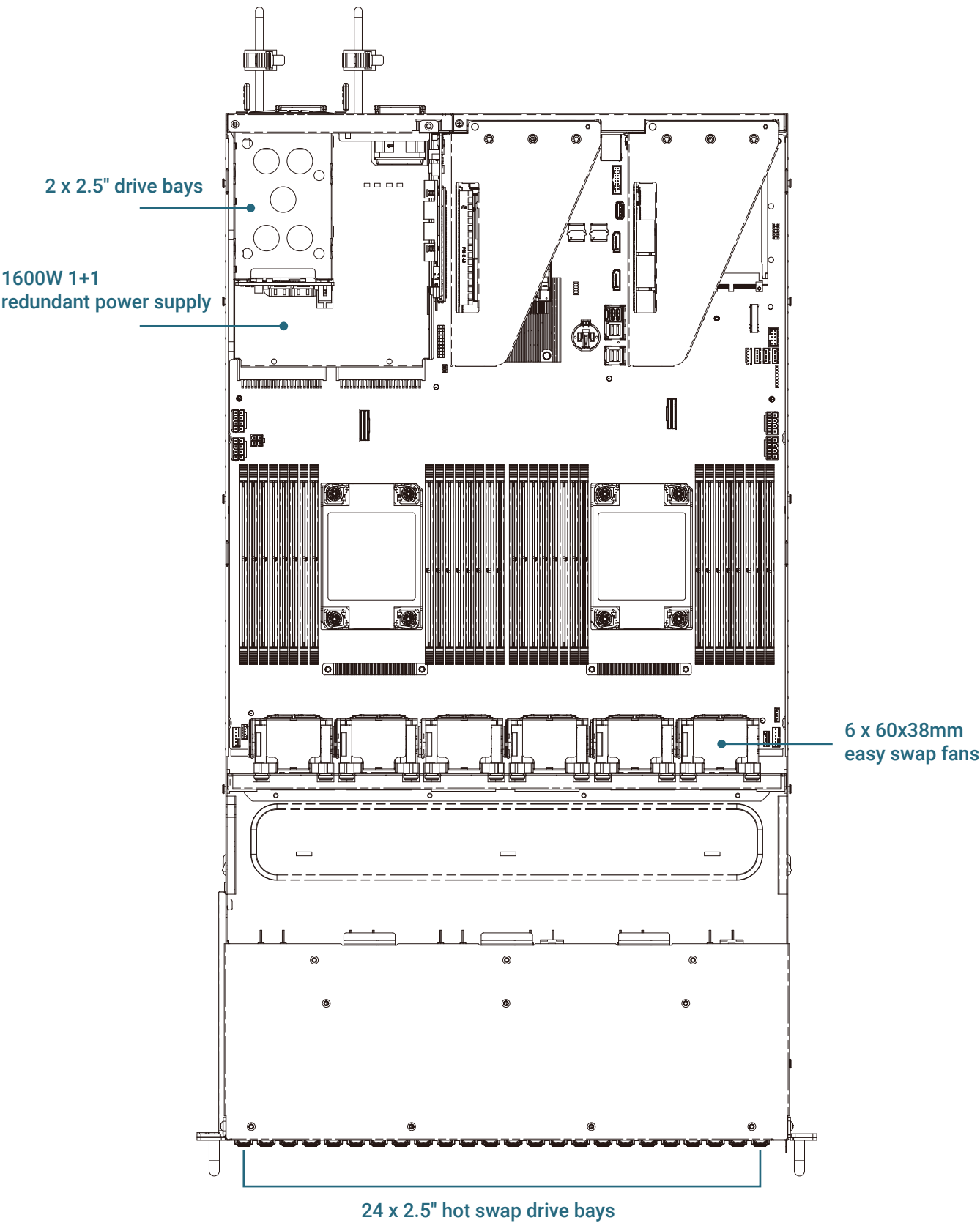
Item	Description	Item	Description
	Power Button		Alert LED
	Power Status LED		ID LED
	System Reset Button		ID Button

Rear Panel



Item	Content	Description
1	Power Switch	PWR_BTN1
2	UID Switch	UID1
3	LAN port	1 GbE RJ45 dedicated to BMC management
4	USB port	2 USB 3.0 Type A in double-stack connector
5	VGA port	1 external DB-15 VGA port
6	Serial port	1 external DB-9 COM port
7	PSU	1600W 1+1 redundant power supply

Top View



Chapter 2. Hardware Setup

This chapter provides the graphic detail and basic instruction for hardware installation. Turn off the system and unplug all peripheral devices before proceeding.

2.1 Central Processing Unit

The serverboard supports dual Xeon scalable processors and Socket P+ (LGA-4189).

2.1.1 Installation

To ensure a safe and easy setup, you need to prepare before installation:

- ☑ a T30 torque screwdriver
- ☑ ESD wrist strap/mat and conductive foam pad
- ☑ Safe and stable environment



CAUTION

The pins of the processor socket are vulnerable and easily susceptible to damage if fingers or any foreign objects are pressed against them. Please keep the socket protective cover on when the processor is not installed.

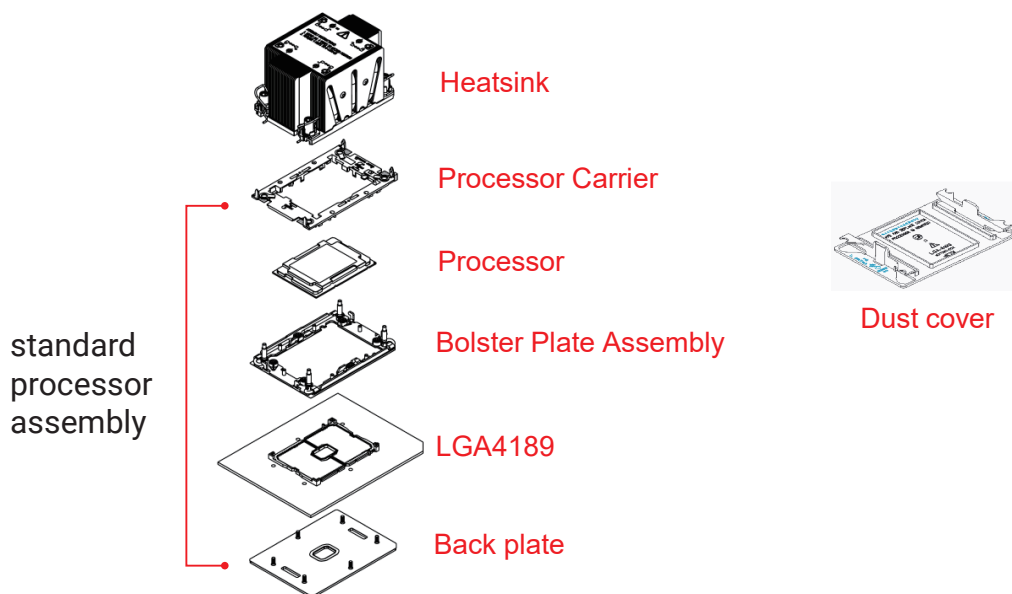


CAUTION

When unpacking a processor, hold the processor only by its edges to avoid touching the contacts.

Standard Processor Assembly:

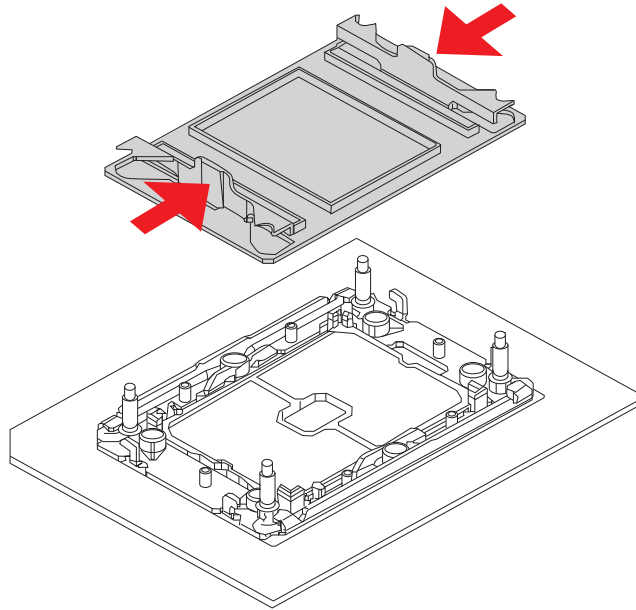
A standard processor assembly is comprised of 5 components: processor carrier, processor, bolster plate assembly, socket and back plate.



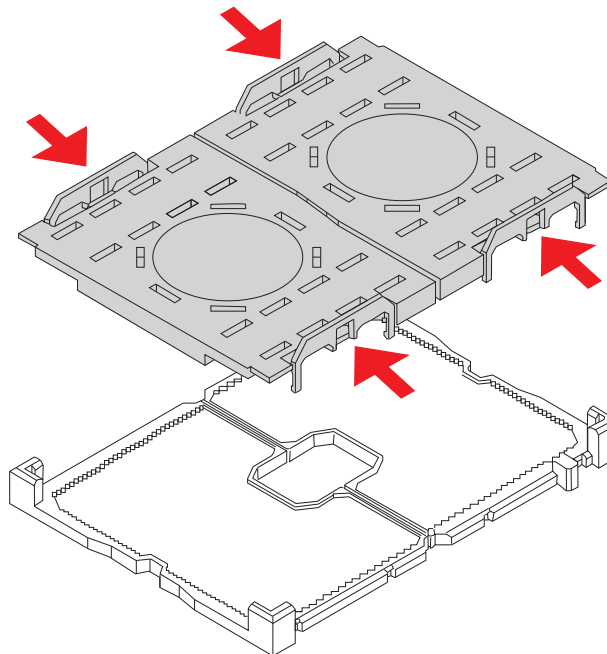
This information is provided for professional technicians only.

Procedure:

- ① Remove the dust cover. Push the tab inward on both sides to remove.



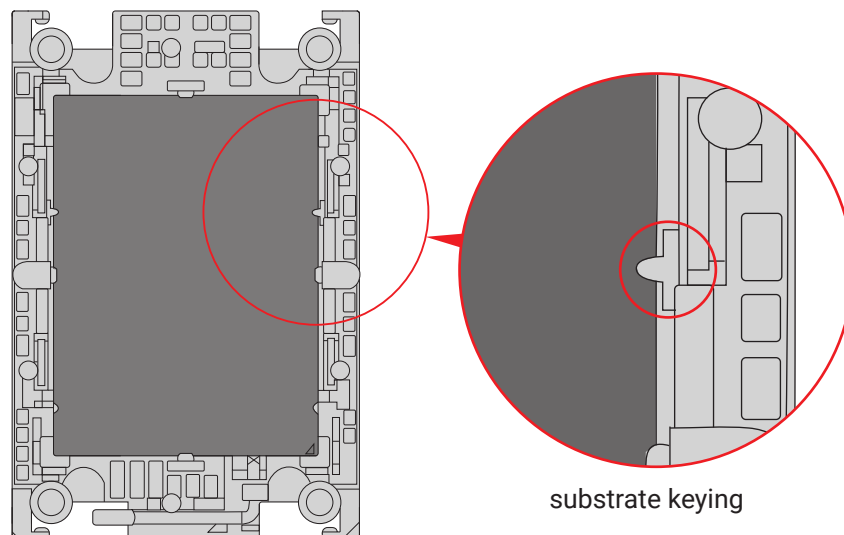
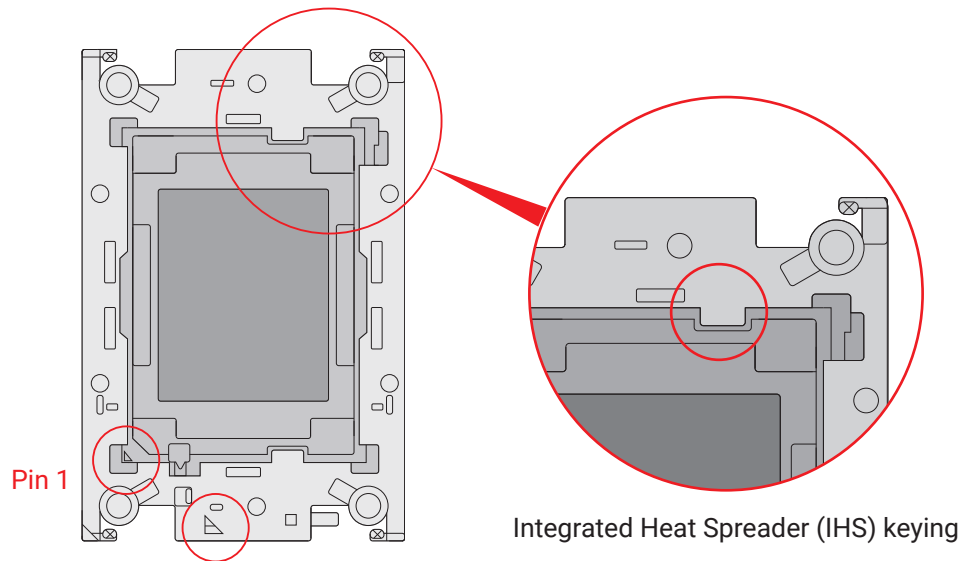
- ② Remove the Pnp cap from the socket. Press the tabs on both sides to remove.



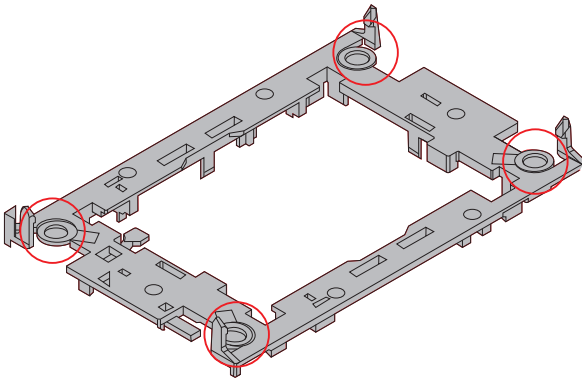
- ③ Insert the CPU into the CPU carrier. Carefully align and insert on side of the CPU and then the other.

NOTE

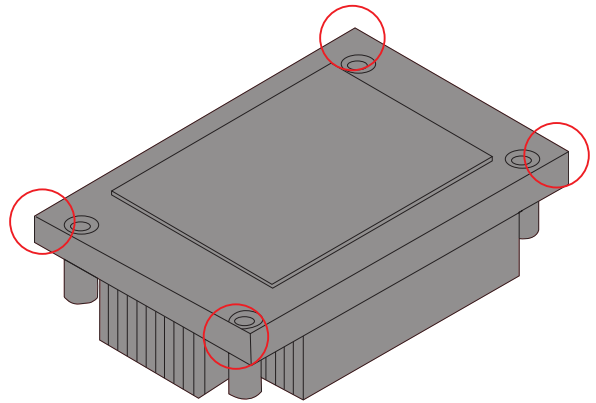
Must ensure to match the direction and pin of the CPU with the carrier. Refer to the placement of pin 1.



- ④ Attach the heat sink onto the CPU carrier. Hook the corners of the CPU carrier to the back side of the heat sink.



CPU carrier

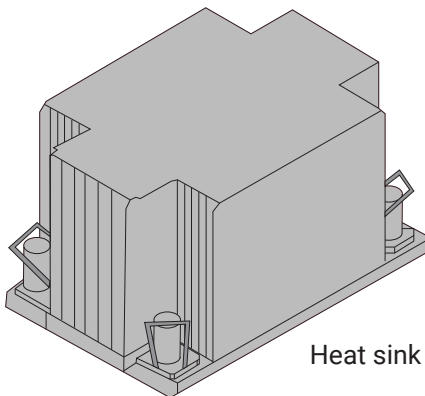


Heat sink back side

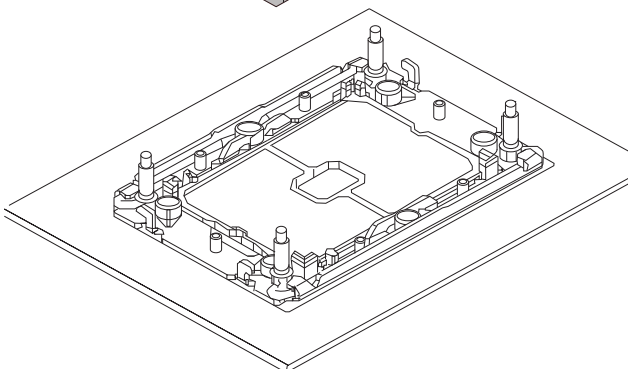
- ⑤ Install the assembled heat sink and CPU carrier onto the CPU socket. Please use a T-30 torque driver tighten the nuts in the four corners of the heat sink labeled in the order 1 → 2 → 3 → 4.

CAUTION

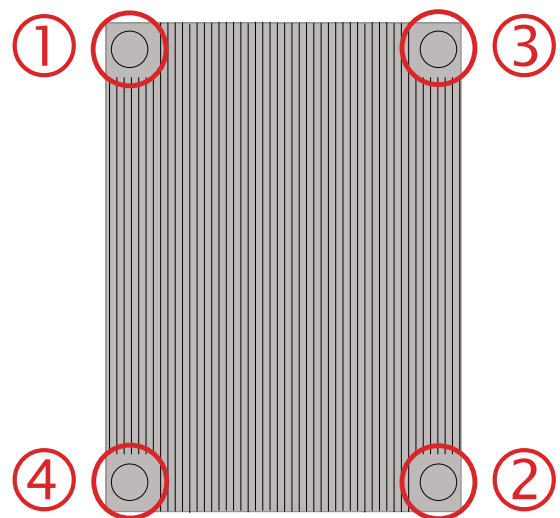
Failure to tighten the heat sink screws in the specified order may cause damage to the processor socket assembly. Heat sink screws is recommended to be tightened to 8 in-lbs torque, but can be tightened to 12 in-lbs torque according to the indicated order on the top of the heatsink label.



Heat sink with carrier

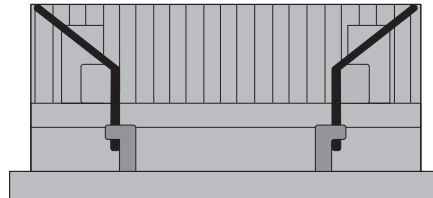
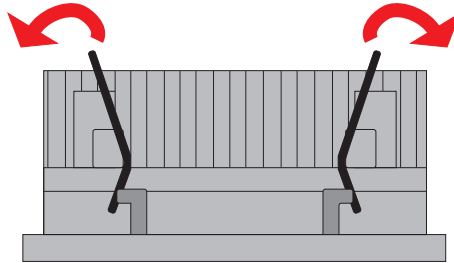


CPU socket



Screw tightening order

- ⑥ Press the rotating wire located on the four corners of the heat sink to latch position to secure the heat sink.



Latched position

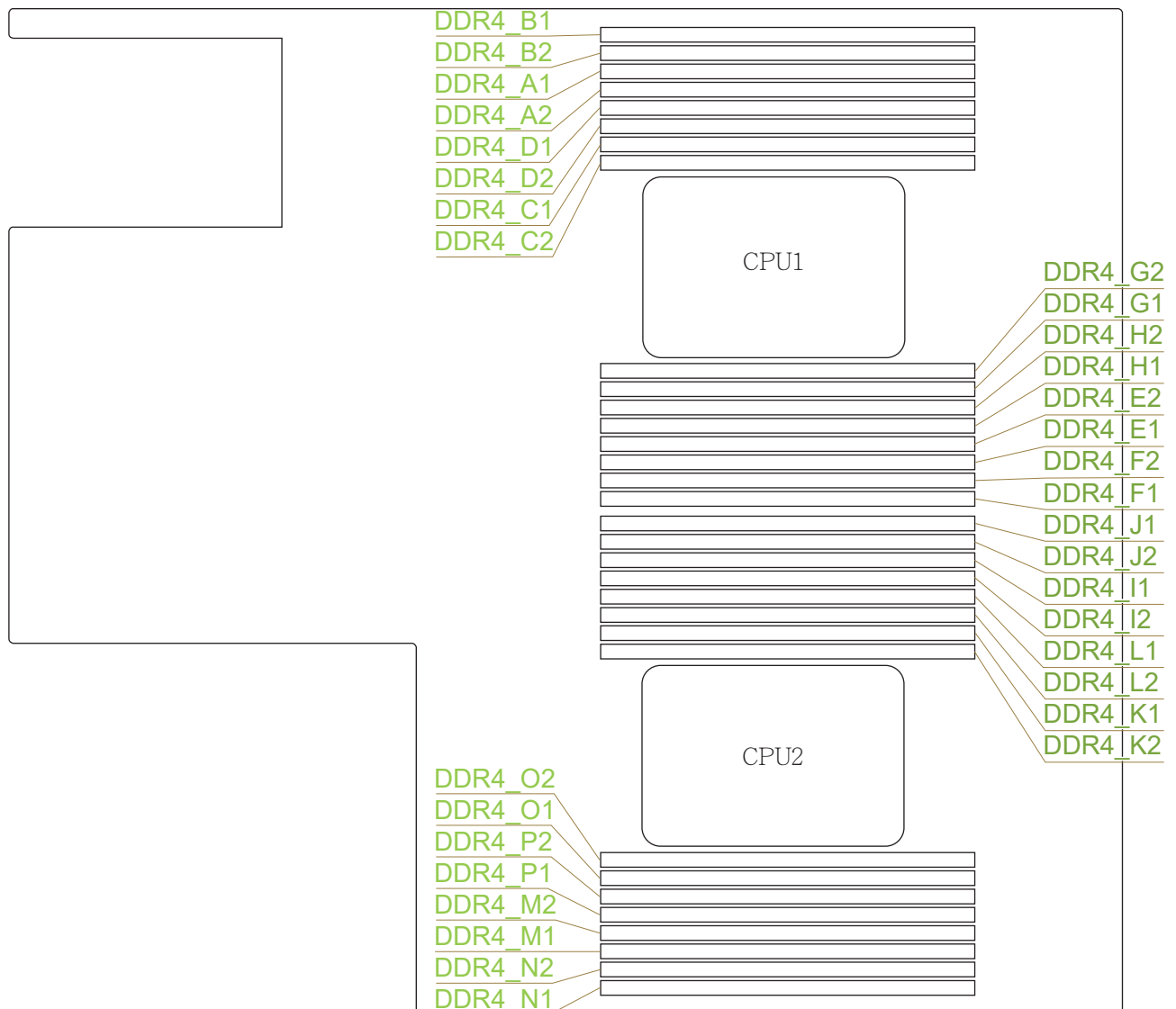
2.2 System Memory

2.2.1 Placement

The DIMMs are displayed on the serverboard as DDR4_B1/ DDR4_B2/DDR4_A1/DDR4_A2/DDR4_D1/ DDR4_D2/DDR4_C1/DDR4_C2/DDR4_G2/DDR4_G1/DDR4_H2/DDR4_H1/DDR4_E2/DDR4_E1/DDR4_F2/DDR4_F1/DDR4_J1/DDR4_J2/DDR4_I1/DDR4_I2/DDR4_L1/DDR4_L2/DDR4_K1/DDR4_K2.

To ensure satisfactory performance, you need to:

- ☑ Verify the DIMM type:
This product supports DDR4 (RDIMM/RDIMM-3DS/LRDIMM/LRDIMM-3DS).
- ☑ Verify if all of the DIMMs installed are of the same DIMM type to avoid memory failure and loss of performance speed.
- ☑ Verify if the DIMMs are populated in parallel slots across channels to activate DUAL Channel Memory Technology.



2.2.2 DIMM Population

1 CPU Configuration

Placement		DIMM Number						
		1	2	4	6	8	12	16
CPU1	A1		#	#	#	#	#	#
	B1	#			#	#	#	#
	C1			#	#	#	#	#
	D1					#		#
	E1		#	#	#	#	#	#
	F1				#	#	#	#
	G1			#	#	#	#	#
	H1					#		#
	A2						#	#
	B2						#	#
	C2						#	#
	D2							#
	E2						#	#
	F2						#	#
	G2						#	#
	H2							#



The symbol # indicates that the DIMM slot is populated.

2 CPU Configurations

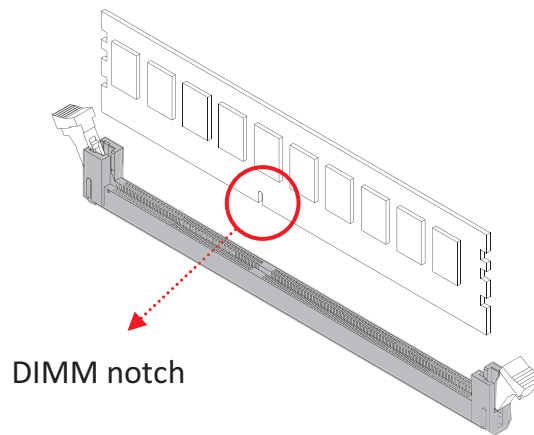
Placement		DIMM Number						
		1	2	4	6	8	12	16
CPU1	A1		#	#	#	#	#	#
	B1	#			#	#	#	#
	C1			#	#	#	#	#
	D1					#		#
	E1		#	#	#	#	#	#
	F1				#	#	#	#
	G1			#	#	#	#	#
	H1					#		#
	A2						#	#
	B2						#	#
	C2						#	#
	D2							#
	E2						#	#
	F2						#	#
	G2						#	#
	H2							#
Placement		1	2	4	6	8	12	16
CPU2	I1		#	#	#	#	#	#
	J1	#			#	#	#	#
	K1			#	#	#	#	#
	L1					#		#
	M1		#	#	#	#	#	#
	N1				#	#	#	#
	O1			#	#	#	#	#
	P1					#		#
	I2						#	#
	J2						#	#
	K2						#	#
	L2							#
	M2						#	#
	N2						#	#
	O2						#	#
	P2							#

2.2.3 Installation

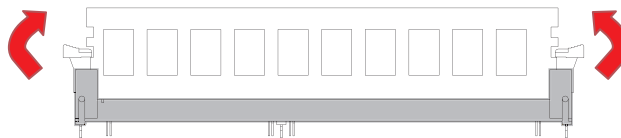
Step 1 Unlock the DIMM socket by pressing the retaining clips outward.



Step 2 Insert the memory module into the slot. Make sure that the DIMM notch is accurately positioned.

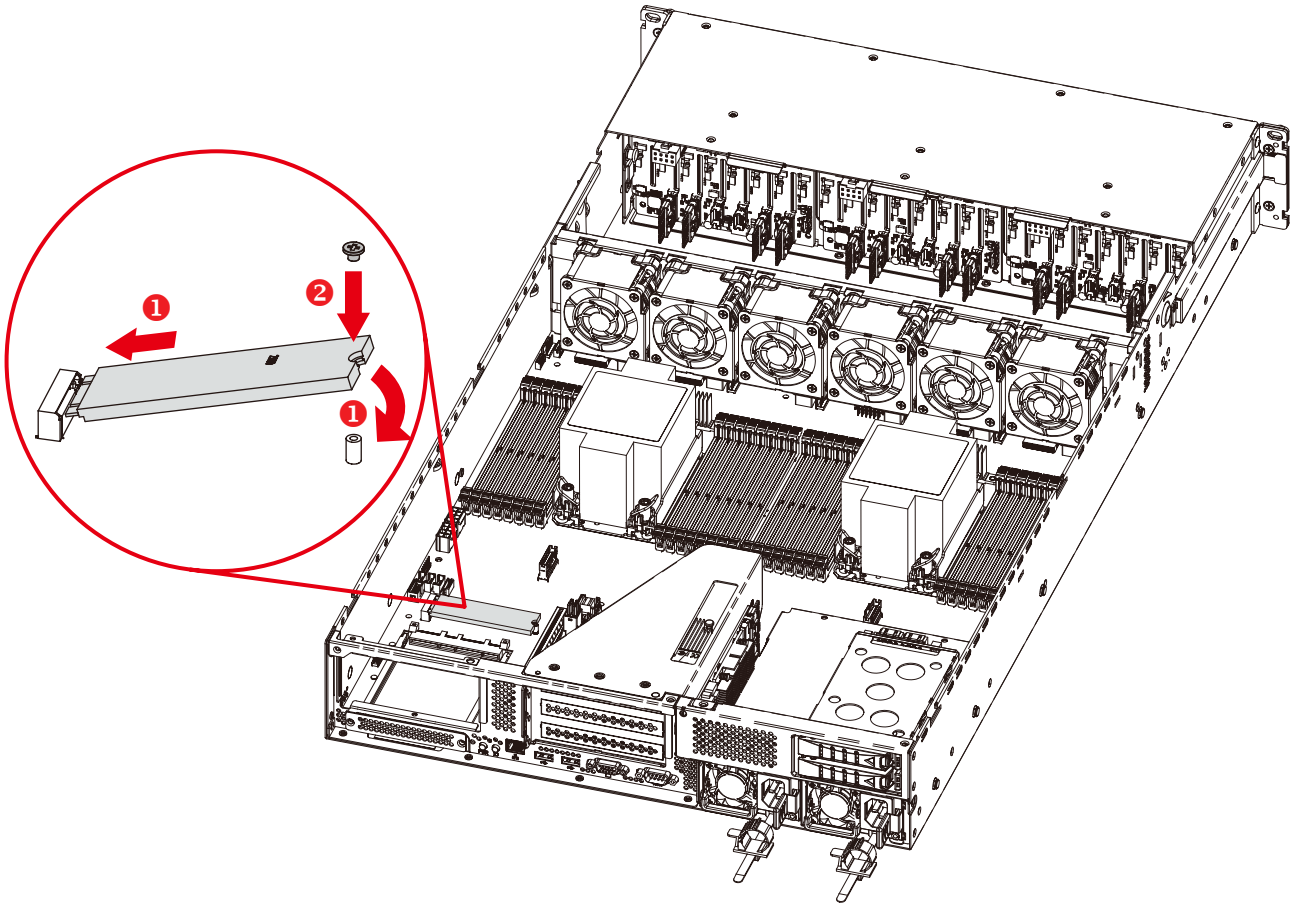


Step 3 Close the retaining clips to complete installation.



2.3 M.2 SSD (NGFF) Card

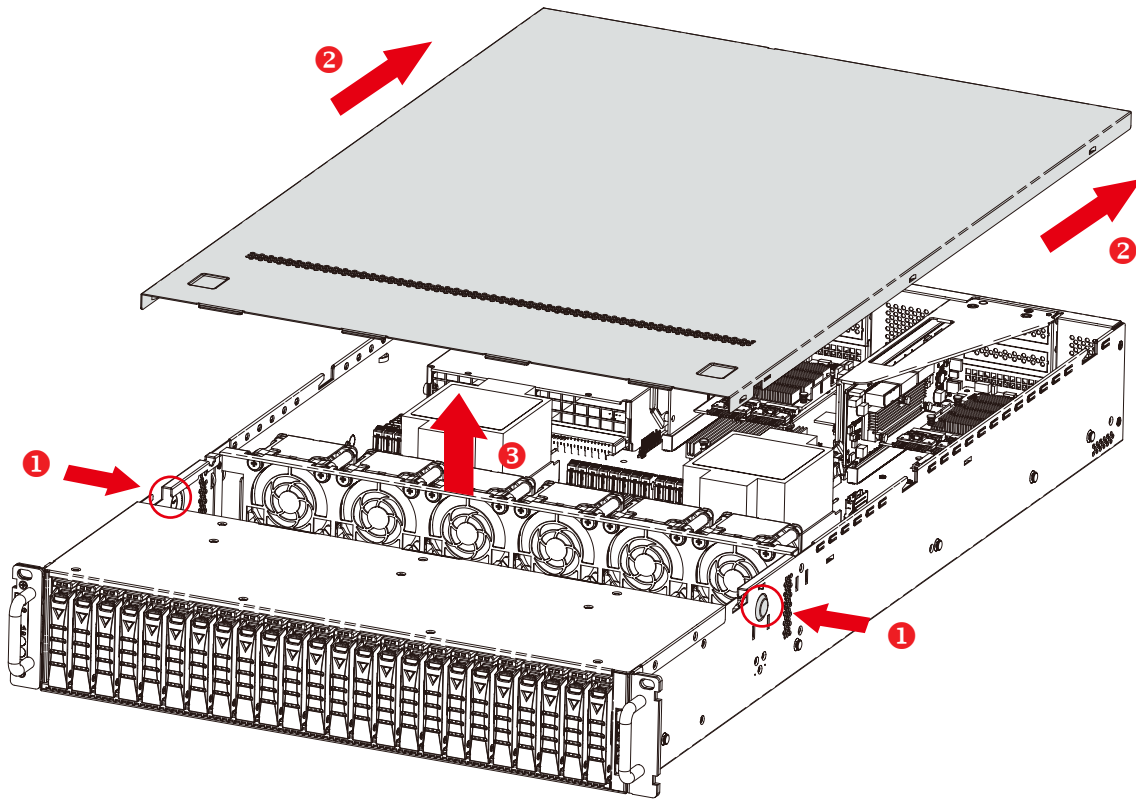
- ① Align and insert the M.2 card into the socket. Ensure the size of your M.2 card match the corresponding standoff on the serverboard.
- ② Fasten the screw to complete setup.
- ③ Installation complete.



This information is provided for professional technicians only.

2.4 Top Cover

- ① Press the button on the both side of the chassis.
- ② Slide the top cover towards the rear of the system barebone.
- ③ Lift the top cover upward to remove.

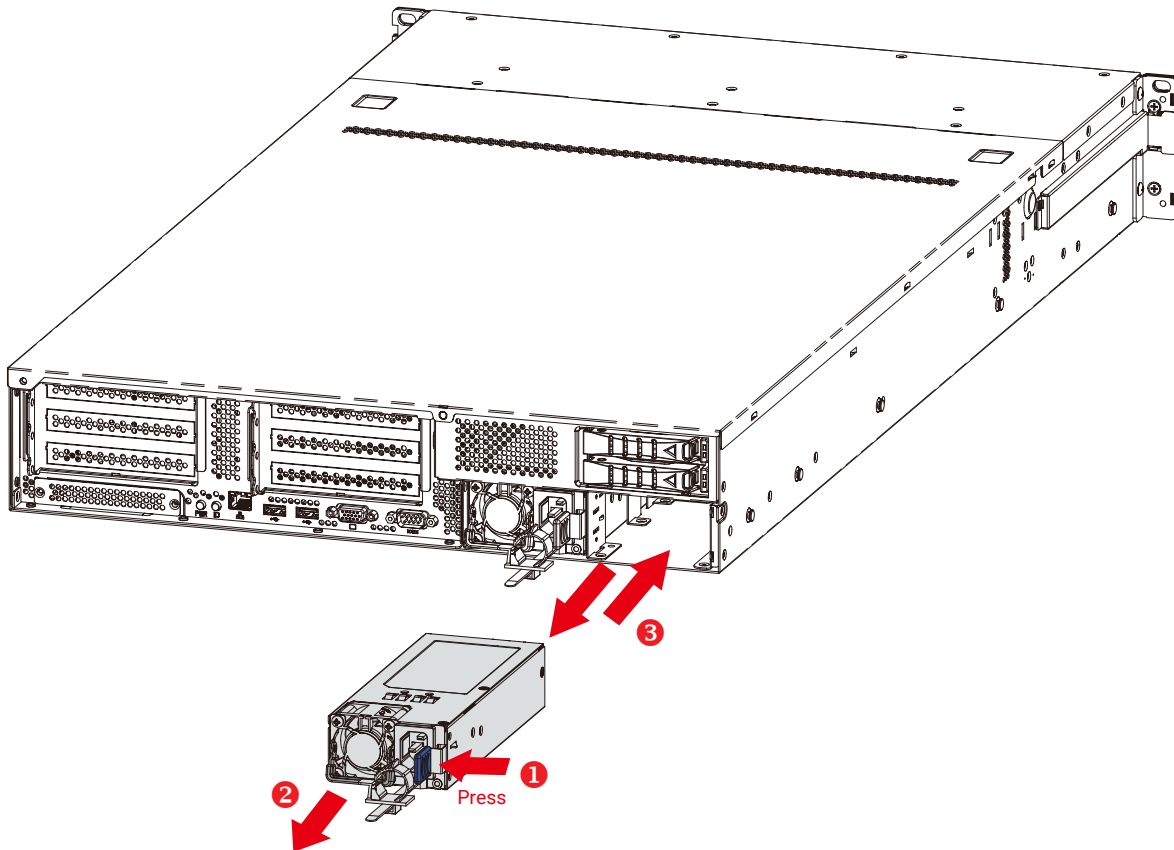


This information is provided for professional technicians only.

2.5 Power Supply Unit

2.5.1 Installation

- ① Press the latch to release the module.
- ② Pull the handle to remove the module out of the chassis.
- ③ Push the replaced power supply unit into the chassis. Ensure that the module is hooked into the cage.



2.5.2 LED Indicator

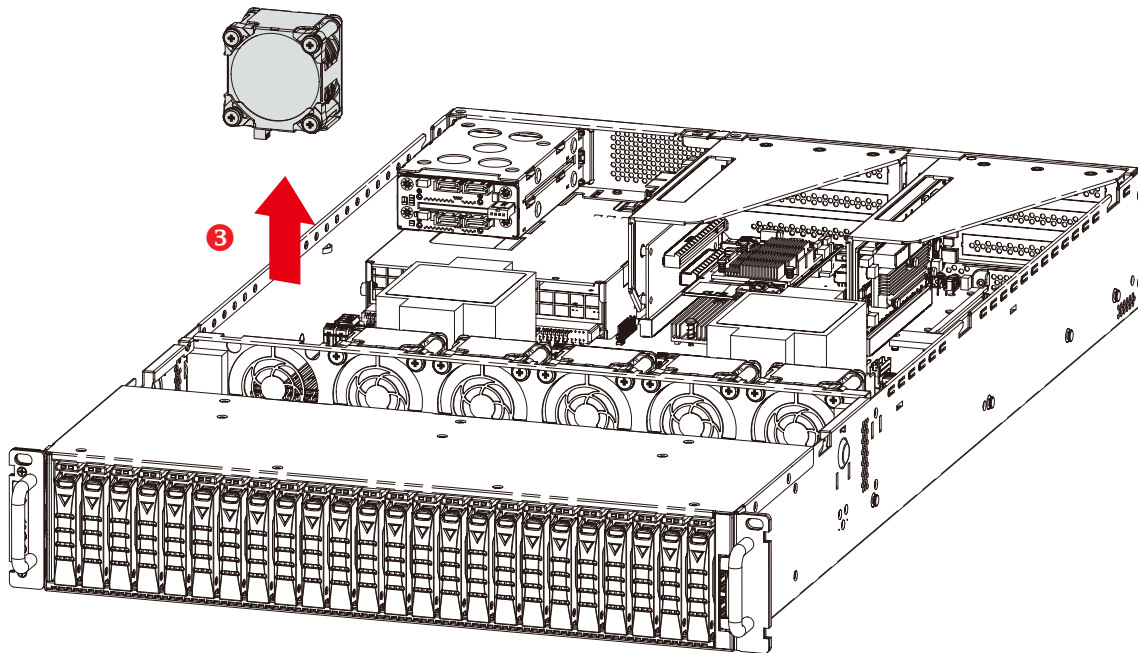
Indicator Color	Behavior
Green	Output on and Ok.
Green (blinking, 1Hz)	Only 12Vsb (PS off) or PSU is in cold redundant state
Amber	Power supply critical event causing a shutdown; AC cord unplugged or AC power lost, failure, OCP, OVP, fan fail.
Amber (blinking, 1Hz)	Power supply warning events where the power supply continues to operate high temp, high power, high current, slow fan.



This information is provided for professional technicians only.

2.6 Fan

- ① Remove the top cover from the chassis. Please refer to [Section 2.4](#).
- ② Unplug the cables and connectors from the fan.
- ③ Pull the fan module from the node. Check to carefully dislodge the rubber connectors from the attached bracket.
- ④ Insert the replaced fan into the node. Verify to align the rubber connectors with the appropriate slot in the bracket.



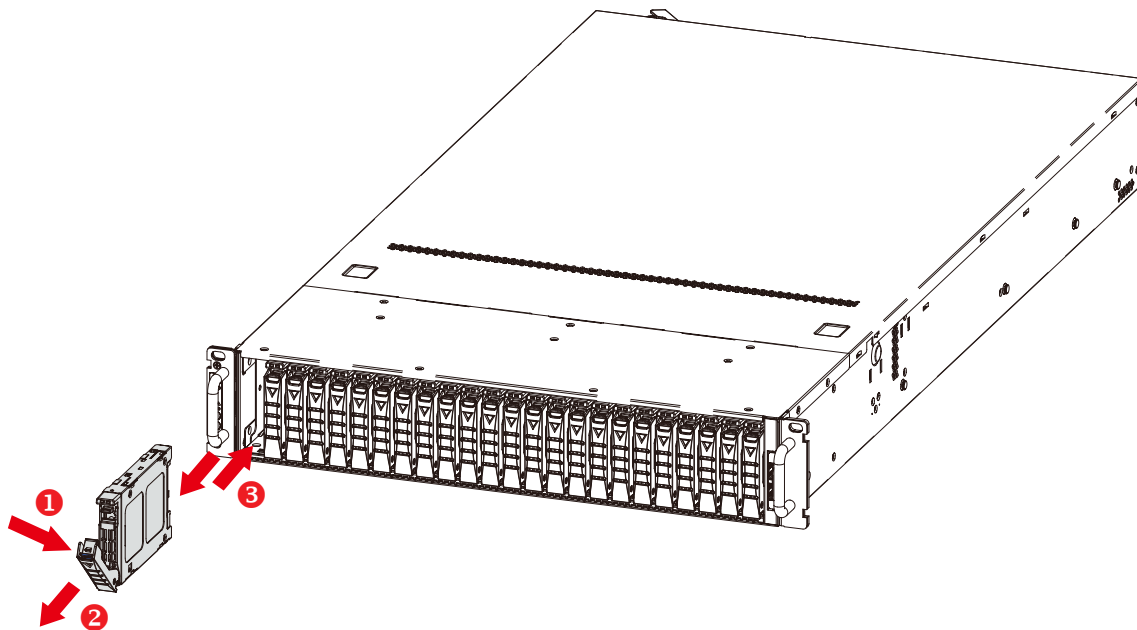
This information is provided for professional technicians only.

2.7 Disk Drive

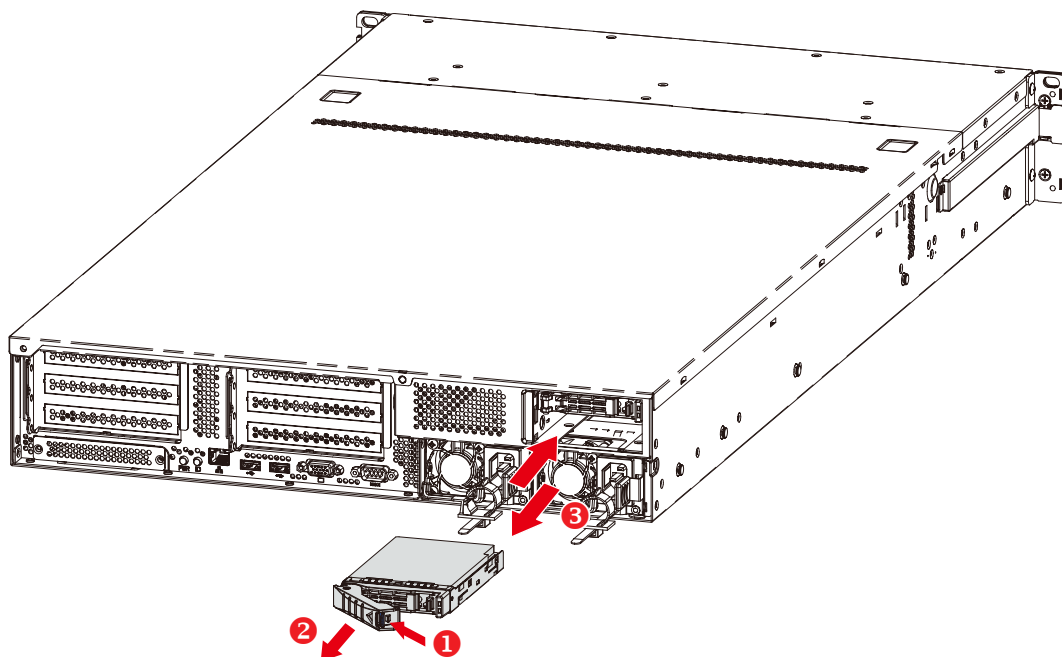
2.7.1 Drive: 2.5-inch

- ① Press the release button on the tray lever to loosen the lever.
- ② Pull the tray lever outward completely.
- ③ Pull the tray out of the system.

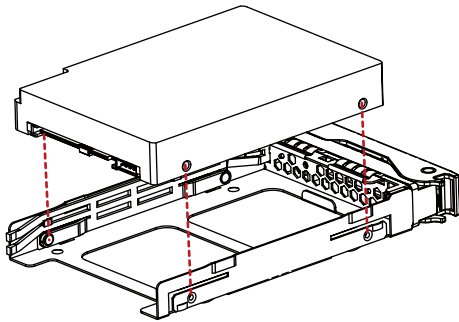
Front



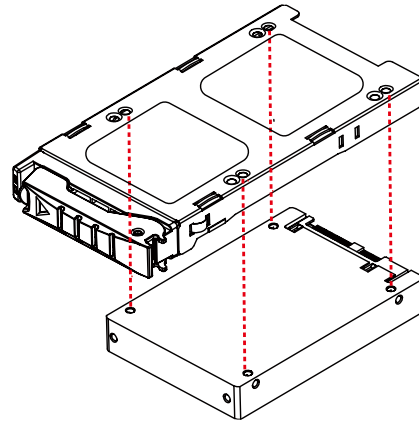
Rear



- ④ Insert the disk drive into the tray. Ensure that the dimples on the tray match the disk drive. For additional assurance, fasten the screws * 4 on the tray to secure the disk drive.



dimple placement



screw placement

- ⑤ Push the tray with the installed disk drive into the end of the drive slot in the chassis.
⑥ Close the tray handle.

2.7.2 LED Indicator

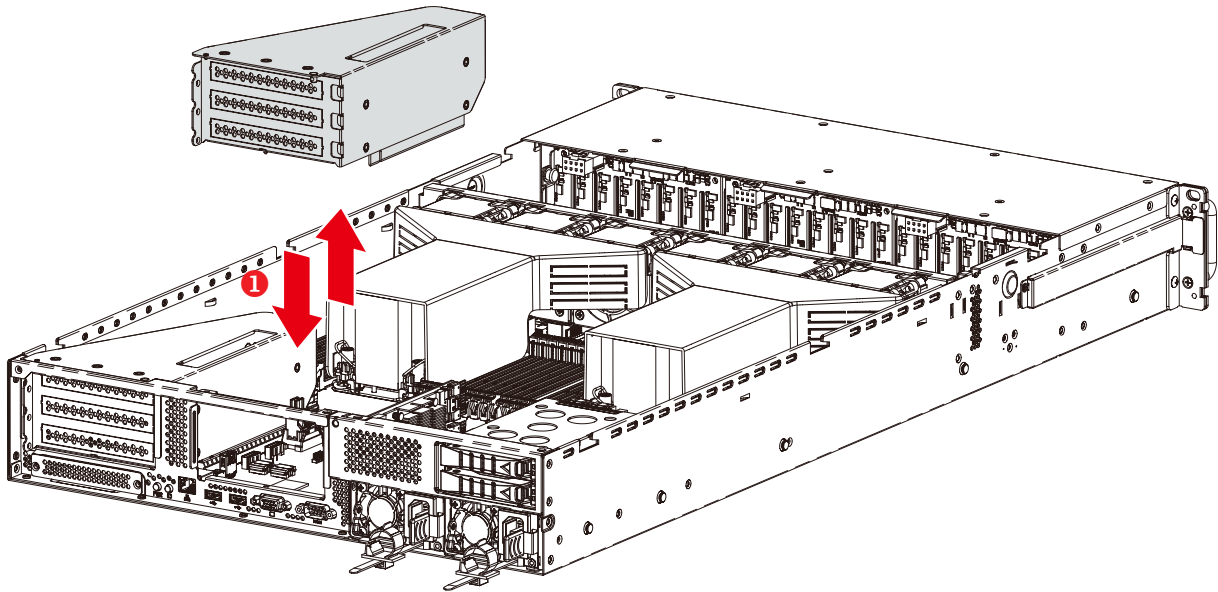
Indicator Color	Behavior
Blue LED (solid)	SATA/SAS/NVMe drive is in idle state.
Blue LED (blinking)	SATA/SAS/NVMe is active.
Red LED (solid)	SATA/SAS/NVMe is not detected.
Red and Green LED (blinking, 1Hz)	SATA/SAS/NVMe is in locate status.



This information is provided for professional technicians only.

2.8 Riser Card

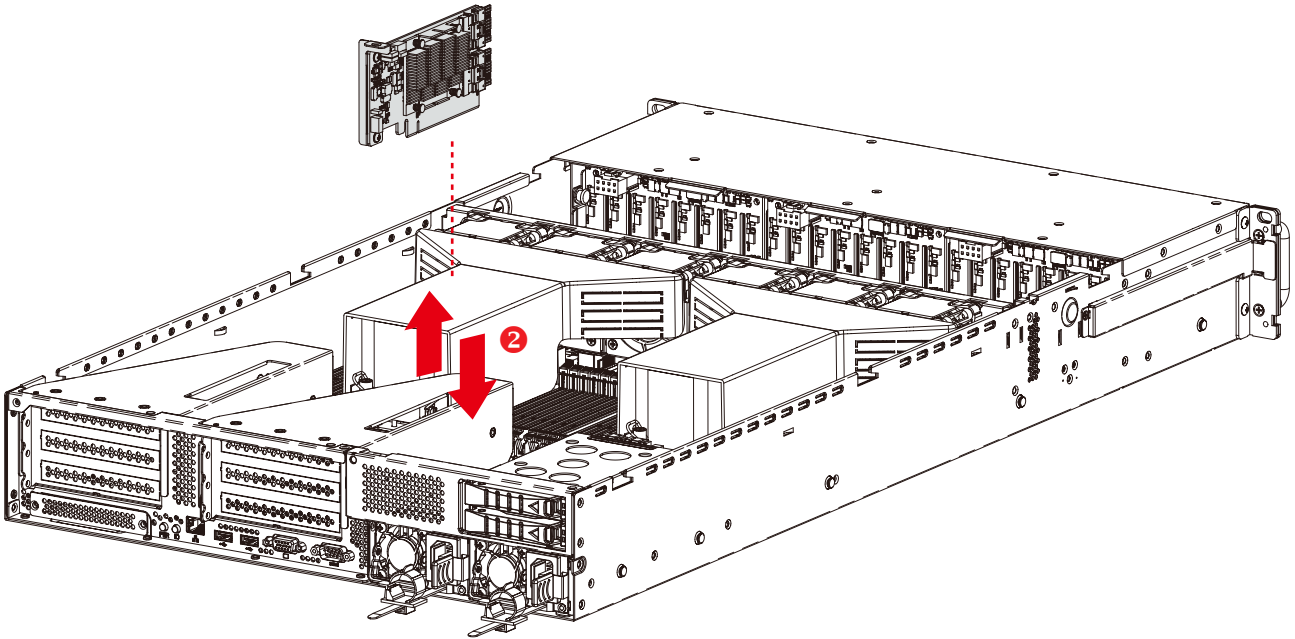
- ① Pull upward to remove the riser card from the system.
- ② Insert the PCIe card into the card slot.
- ③ Insert the replaced riser card into the appropriate card slot. Ensure that the card is properly aligned.



This information is provided for professional technicians only.

2.9 Retimer Card

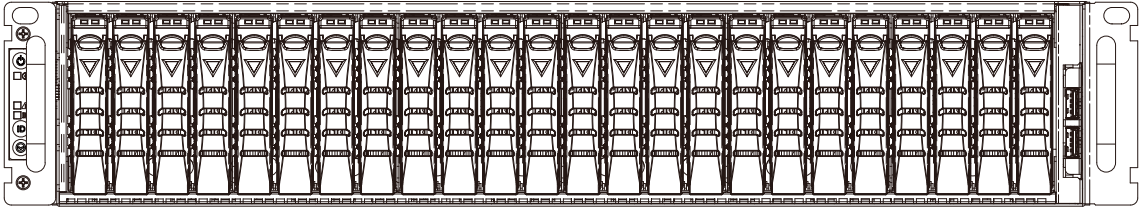
- ① Remove the top cover from the chassis. Please refer to [Section 2.4 Top Cover](#).
- ② Align and insert the retimer card into the appropriate card slot.
- ③ Ensure that the card is properly aligned and fastens the screw to complete setup.



This information is provided for professional technicians only.

2.10 Drive Slot Map

Front Panel



1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

2.11 Slide Rail

NOTE



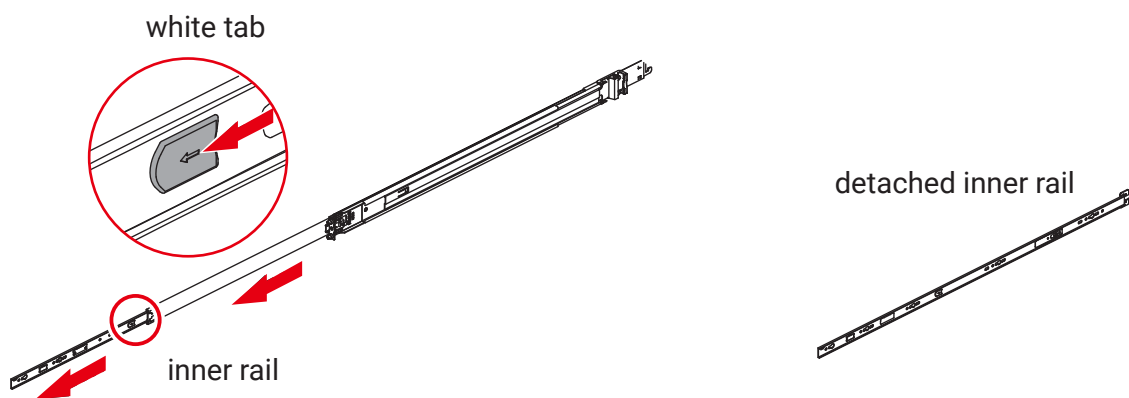
This section provides a basic instruction for mounting the slide rail onto the system. Tool-less rails vary per order. The rail in this manual may not exactly match the rail for your system. Please refer to the specifications or quick installation guide that came with your purchased product.

CAUTION

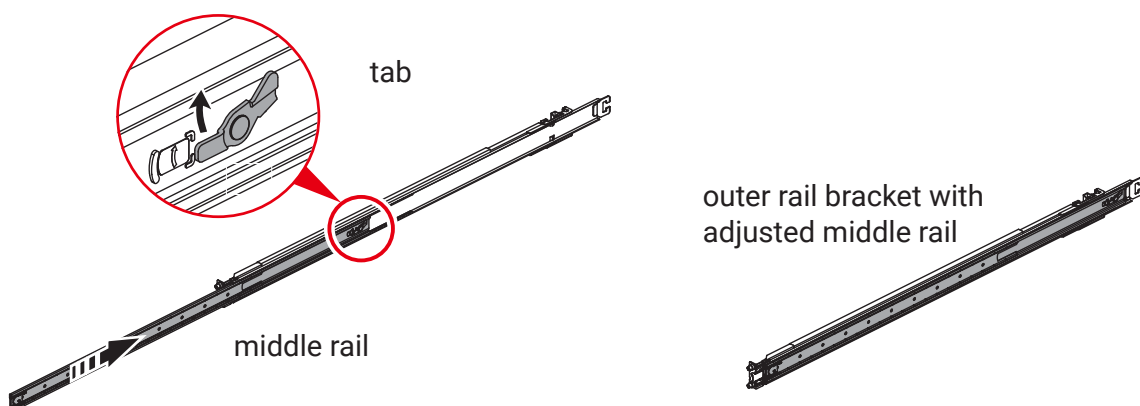


The rack may tilt and fall due to incorrect installation or placed on uneven grounds. The rack must be placed in a flat surface before you begin to slide the system barebone in for servicing.

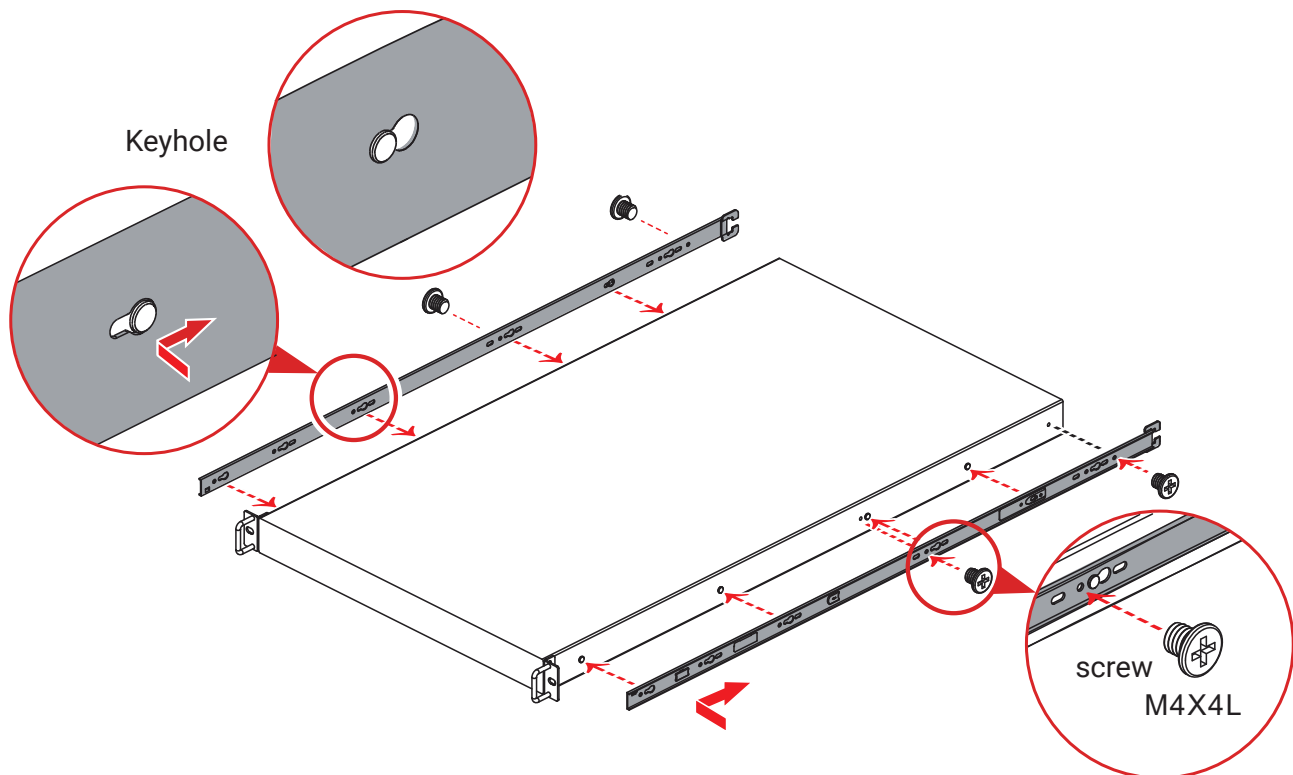
1. Pull the inner rail out of the slide rail until it clicks.
2. Detach the inner rail completely from the slide rail by pulling the white tab forward.



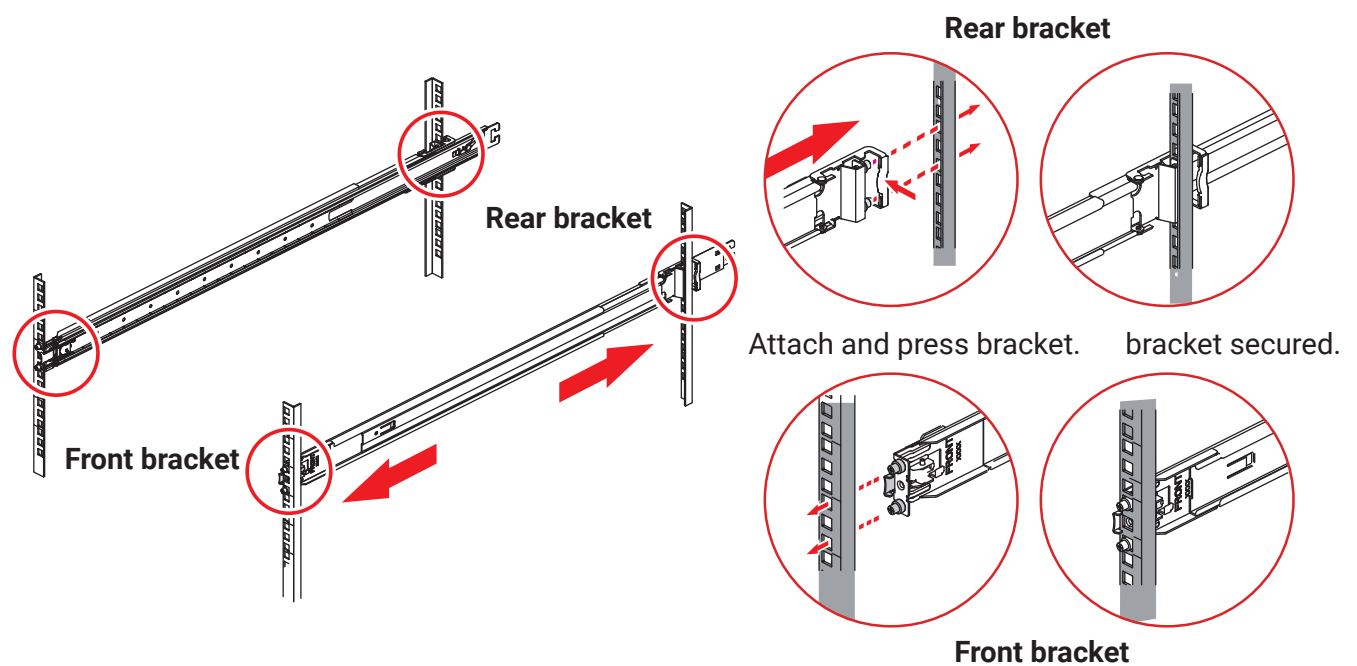
3. After the inner rail is dislodged, adjust the middle rail back to its original position by pushing the tab on the middle rail.



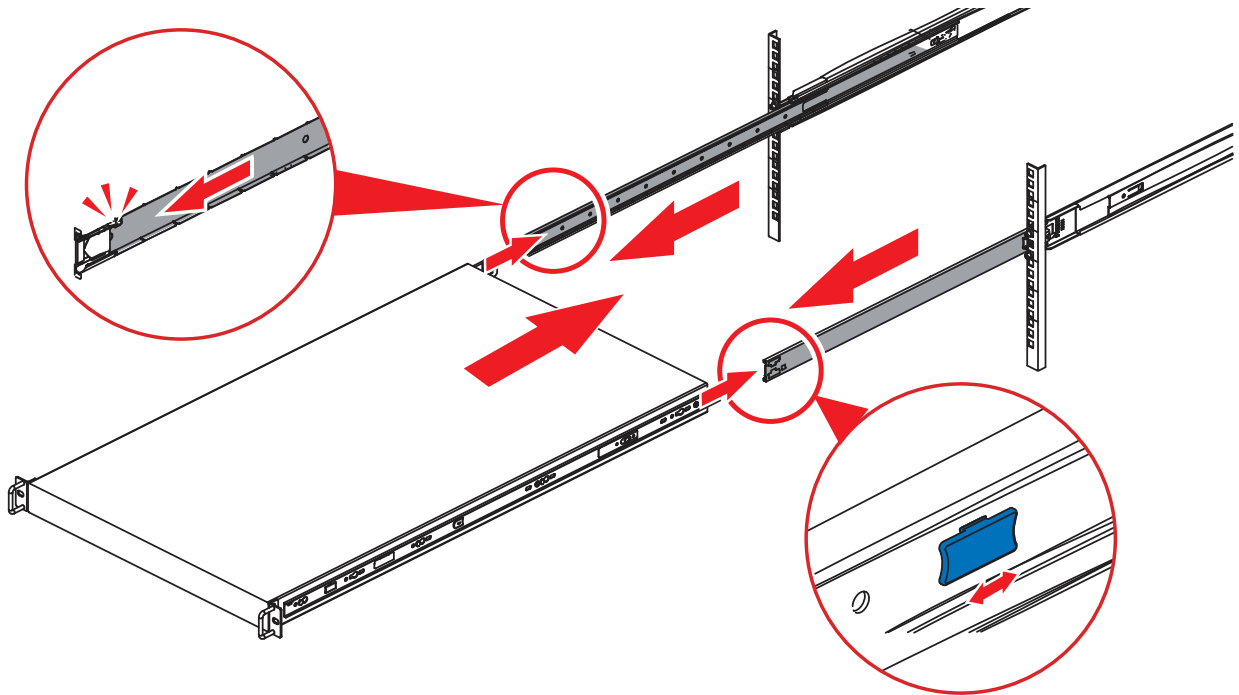
4. Install the inner rail onto the system barebone. Lock the keyholes and secure the screws on sides of the system.



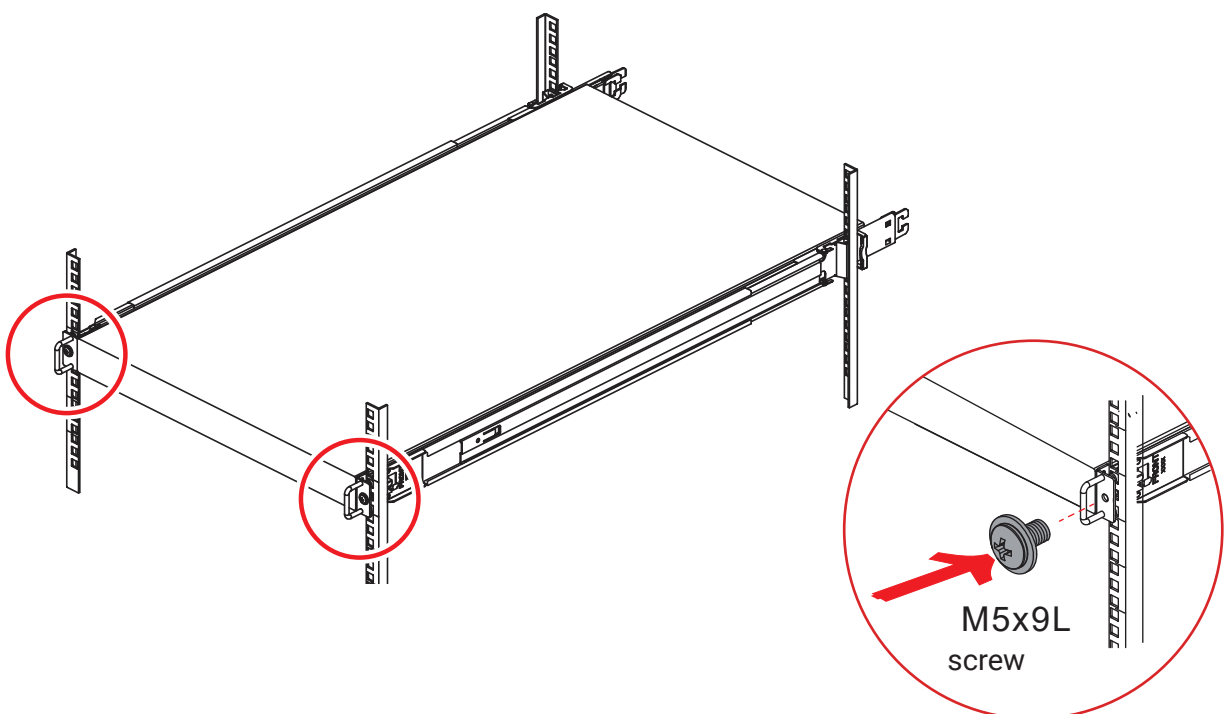
5. Continue installing the outer rail bracket to the mounting frame. Attach the outer rail assembling to the frame and press the bracket to form a rack on both ends. Repeat to fully mount the bracket assembly on the other side.



6. Insert the chassis to complete the installation.
 - a. Pull the middle rail fully extended in lock position, ensure ball bearing retainer is located at the front of the middle rail.
 - b. Insert the chassis into middle-outer rails.
 - c. When hit a stop, please pull/push the blue release tab on the inner rails.



7. Tighten and secure the chassis with the shipping screws.

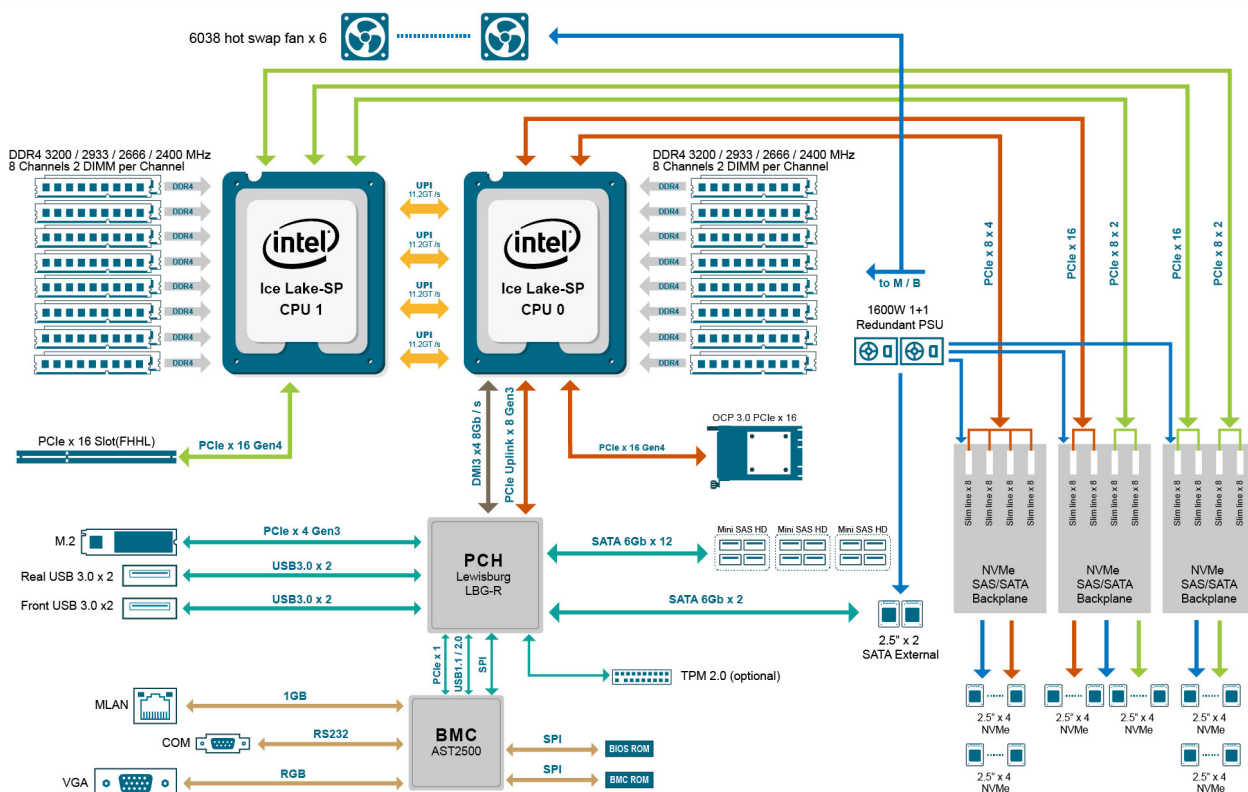


Chapter 3. Hardware Specifications

This section provides illustrations that display the internal jumpers, connectors and system LED indicators on the A6 motherboard. The motherboard layout and essential connectors are listed below for your reference.

3.1 Motherboard Settings

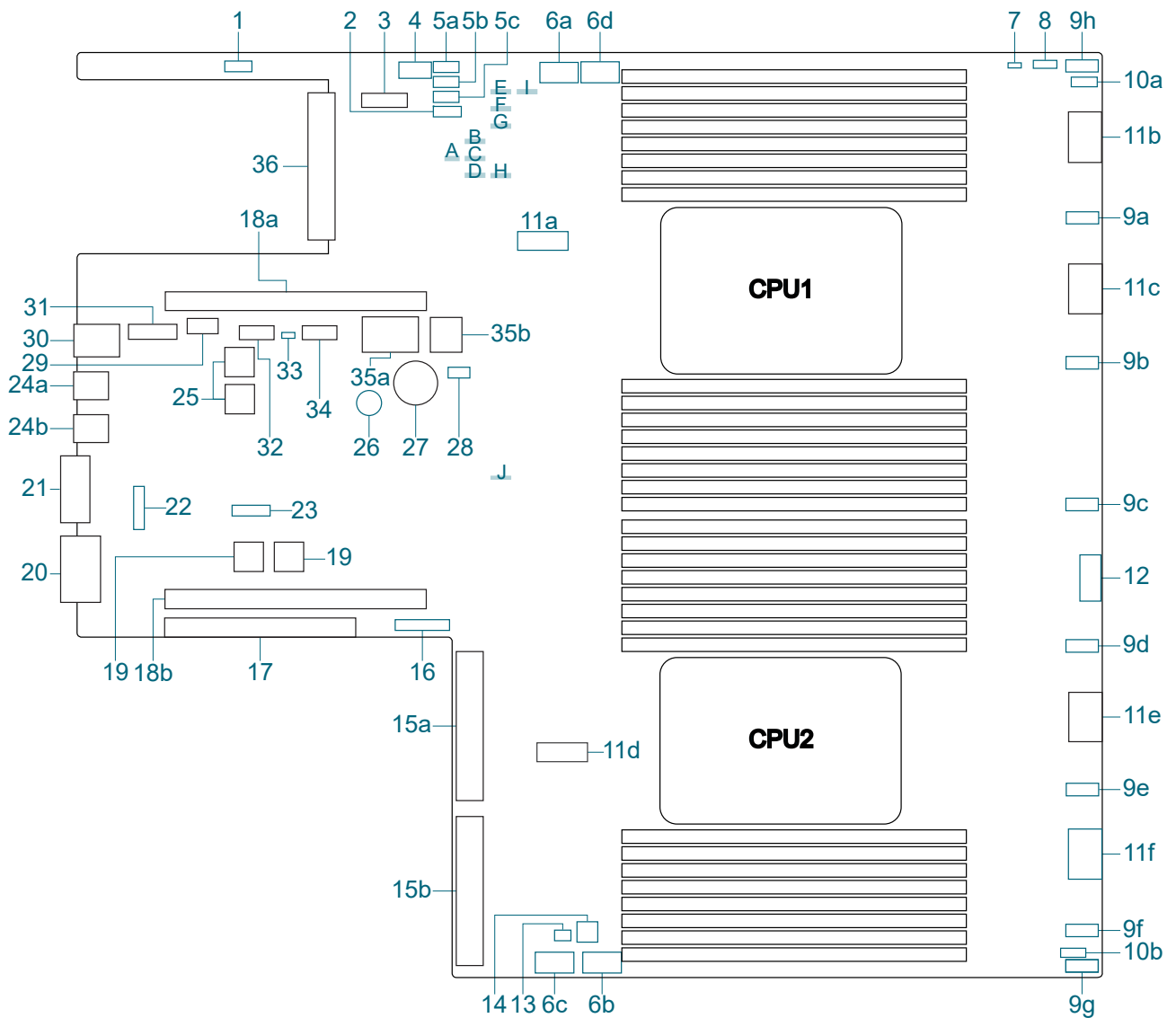
3.1.1 Block Diagram



3.1.2 Content List

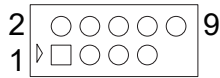
Item Placement			Item Placement		
1	System Panel Header	PANEL1	19	BIOS ROM Socket	BIOS_ROM
2	Intelligent Platform Management Bus Header	IPMB1	20	Serial Port	COM1
3	M.2 Socket	M2_1	21	VGA Port	VGA1
4	USB 2.0 Header	USB_1_2	22	Front VGA Header	FRNT_VGA1
5a	BMC SMBus Header	BMC_SMB1	23	TPM-SPI Header	TPM_BIOS_PH1
5b		BMC_SMB2			
5c		BMC_SMB3			
6a	GPU Power Connector	GPU_PWR1	24a 24b	USB 3.2 Gen1 Port	USB3_1 USB3_2
6b		GPU_PWR2			
6c		GPU_PWR3			
6d		GPU_PWR4			
7	PWM Configuration Header	PWM_CFG1	25	BMC ROM Socket	BMC_ROM
8	Virtual RAID On CPU Header	RAID_1	26	Buzzer	BUZZER
9a	System Fan Connector	FAN1	27	Battery Socket	BAT1
9b		FAN2			
9c		FAN3			
9d		FAN4			
9e		FAN5			
9f		FAN6			
9g		FAN7			
9h		FAN8			
10a	Backplane PCI Express Hot-Plug Connector	CPU1_HSBP1	28	SATA SGPIO Connector	SSATA_SGPIO1
10b		CPU2_HSBP1			
11a	Slimline NVMe Connector	SLIM1	29	Vertical Type A USB 3.2 Gen1 Connector	USB3_3_5
11b		SLIM2			
11c		SLIM3			
11d		SLIM4			
11e		SLIM5			
11f		SLIM6			
12	HDD Backplane Power Connector	HSBP_PWR1	30	LAN port	IPMI_LAN1
13	PDB Power Connector	CRPS_IN1	31	USB 3.2 Gen1 Header	USB3_3_4
14	SATA Power Connector	HDD_PWR1	32	SATA3 Connector	SSATA1
15a	PSU Connector	PSU_CON1	33	SATA DOM Power Connector	SSATAPWR1
15b		PSU_CON2			
16	Auxiliary Panel Header	AUX_PANEL1	34	SATA3 DOM Connector	SSATA0
17	PCIe 4.0 x16 slot	PCIE3	35a 35b	Mini SAS HD Connector	SATA_0_7 SSATA_2_5
18a	PCIe 4.0 x24 slot	PCIE1	36	OCP 3.0 Gen4 x16 Mezzanine Card Slot	OCP3
18b		PCIE2			
Header/Jumper			Header/Jumper		
A	BIOS Swap Override Header	ESPI_MODE1	F	Password Reset Jumper	PASSWORD_CLEAR
B	PMBUS Mode Jumper	PMBUS_SEL_CLK1	G	BIOS Recovery	BIOS_RECOVERY1
C	PMBUS Mode Jumper	PMBUS_SEL_DAT1	H	ME Recovery	ME_RECOVERY1
D	PMBUS Mode Jumper	PMBUS_SEL_ALT1	I	ESPI/LPC Selection Jumper	ESPI_LPC
E	QAT Mode Jumper	QAT_SEL	J	ESPI Flash Sharing Jumper	ESPI_SHARE

3.1.3 Placement



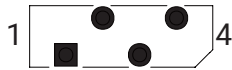
3.1.4 Connector and Jumper Definition

1 System Panel Header (PANEL1)



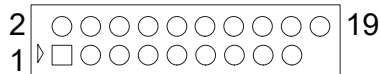
HDLED+	1	2	PLED+
HDLED-	3	4	PLED-
GND	5	6	PWRBTN#
RESET#	7	8	GND
GND	9	10	

2 Intelligent Platform Management Bus Header (IPMB1)



1	IPMB_SDA
2	GND
3	IPMB_SCL
4	NC

4 USB 2.0 Header (USB_1_2)



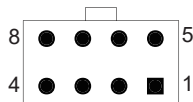
Dummy	1	2	IntA_PA_D+
IntA_PB_D+	3	4	IntA_PA_D-
IntA_PB_D-	5	6	GND
GND	7	8	IntA_PA_SSTX+
IntA_PB_SSTX+	9	10	IntA_PA_SSTX-
IntA_PB_SSTX	11	12	GND
GND	13	14	IntA_PA_SSRX+
IntA_PB_SSRX+	15	16	IntA_PA_SSRX-
IntA_PB_SSRX	17	18	Vbus
Vbus	19		

5a ~ 5c BMC SMBus Header (BMC_SMB1/BMC_SMB2/BMC_SMB3)



1	BMC_SMBDATA
2	GND
3	BMC_SMBCLK
4	Power
5	BMC_SMB_PRESENT_1_N

6a ~ 6d GPU Power Connector (GPU_PWR1/GPU_PWR2/GPU_PWR3/GPU_PWR4)



GND	1	5	+12V
GND	2	6	+12V
GND	3	7	+12V
GND	4	8	+12V

7 PWM Configuration Header (PWM_CFG1)



1	GND
2	SMB_DATA_VSB
3	SMB_CLK_VSB

8 Virtual RAID On CPU Header (RAID_1)



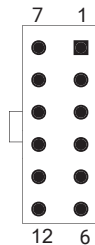
1	VROC RAID KEY
2	GND
3	+3VSB
4	GND

9a ~ 9h System fan Connectors (FAN1/FAN2/FAN3/FAN4/FAN5/FAN6/FAN7/FAN8)

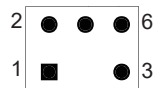
1	GND
2	12V (FAN_VOLTAGE)
3	FAN_SPEED_SENSOR1
4	FAN_SPEED_CONTROL
5	FAN_SPEED_SENSOR2
6	NC

10a ~ 10b Backplane PCI Express Hot-Plug Connector (CPU1_HSBP1/CPU2_HSBP1)

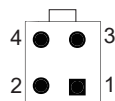
1	+3V
2	CPU_HP_SCL
3	CPU_HP_SDA
4	P0_HP_ALERT_L
5	GND

12 HDD Backplane Power Connector (HSBP_PWR1)

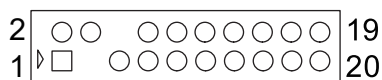
+12V	7	1	GND
+12V	8	2	GND
+12V	9	3	GND
+12V	10	4	GND
+12V	11	5	GND
+12V	12	6	GND

13 PDB Power Connector (CRPS_IN1)

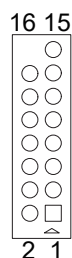
PS_ON#	1	2	PSU1_PWROK
No key	3	4	N/A
+12VSB	5	6	GND

14 SATA Power Connector (HDD_PWR1)

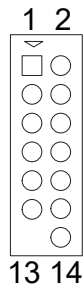
GND	1	2	GND
+12V	3	4	+5V

16 Auxiliary Panel Header (AUX_PANEL1)

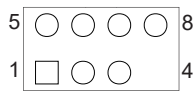
+5VSB	1	2	SMB_Alert
No key	3	4	SMB_CLK
CASEOPEN#	5	6	No key
GND	7	8	GND
LOCATORLED1+	9	10	SMB_DATA
LOCATORLED1-	11	12	+3VSB
LOCATORBTN#	13	14	LAN1_LINK
GND	15	16	LED_PWR
System Fault LED-	17	18	LED_PWR
System Fault LED+	19	20	LAN2_LINK

22 Front VGA Header (FRNT_VGA1)

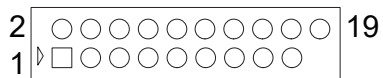
Red	1	2	Green
Blue	3	4	
GND	5	6	GND
GND	7	8	VGA_SELECT
+5V	9	10	GND
	11	12	DDC _ D AT A
H_SYNC	13	14	V_SY N C
DDC _ C L K	15	16	No Key

23 TPM-SPI Header (TPM_BIOS_PH1)

SPI_DQ2	1	2	SPI_DQ3
SPI_CS0	3	4	+3.3V
SPI_MISO	5	6	TPM_Present
RSMRST#	7	8	CLK
GND	9	10	SPI_MOSI
SPI_TPM_CS#	11	12	RST#
No Key	13	14	TPM_PIRQ

28 SATA SGPIO Connector (SSATA_SGPIO1)

+3.3V	1	2	
SDATAOUT	3	4	SDATAIN
GND	5	6	SLOAD
No Key	7	8	SCLOCK

31 USB 3.2 Gen1 Header (USB3_3_4)

Dummy	1	2	IntA_PA_D+
IntA_PB_D+	3	4	IntA_PA_D-
IntA_PB_D-	5	6	GND
GND	7	8	IntA_PA_SSTX+
IntA_PB_SSTX+	9	10	IntA_PA_SSTX-
IntA_PB_SSTX	11	12	GND
GND	13	14	IntA_PA_SSRX+
IntA_PB_SSRX+	15	16	IntA_PA_SSRX-
IntA_PB_SSRX	17	18	Vbus
Vbus	19		

A BIOS Swap Override Header (ESPI_MODE1)

ESPI_MODE1	Setting	
Short	Enable Override	Default
Open	Disable Override	

B C D PMBUS Mode Jumper (PMBUS_SEL_CLK1, PMBUS SEL_DAT1 and PMBUS_SEL_ALT1)

PMBUS_SEL_CLK1 PMBUS_SEL_DAT1 PMBUS_SEL_ALT1	Setting	
Pin 1-2	PMBus connected to BMC	Default
Pin 2-3	PMBus connected to PCH	

E QAT Mode Jumper (QAT_SEL)

QAT_SEL	Setting	
Pin 1-2	QAT Asymmetric	Default
Pin 2-3	QAT Symmetric	

F Password Reset Jumper (PASSWORD_CLEAR)

PASSWORD_CLEAR	Setting	
Pin 1-2	Normal Mode	Default
Pin 2-3	Password Clear	

G BIOS Recovery (BIOS_RECOVERY1)

BIOS_RECOVERY1	Setting	
Pin 1-2	Normal Mode	Default
Pin 2-3	Recover BIOS	

H ME Recovery (ME_RECOVERY1)

ME_RECOVERY1	Setting	
Pin 1-2	Normal Mode	Default
Pin 2-3	ME Recovery Mode	

I ESPI/LPC Selection Jumper (ESPI_LPC)

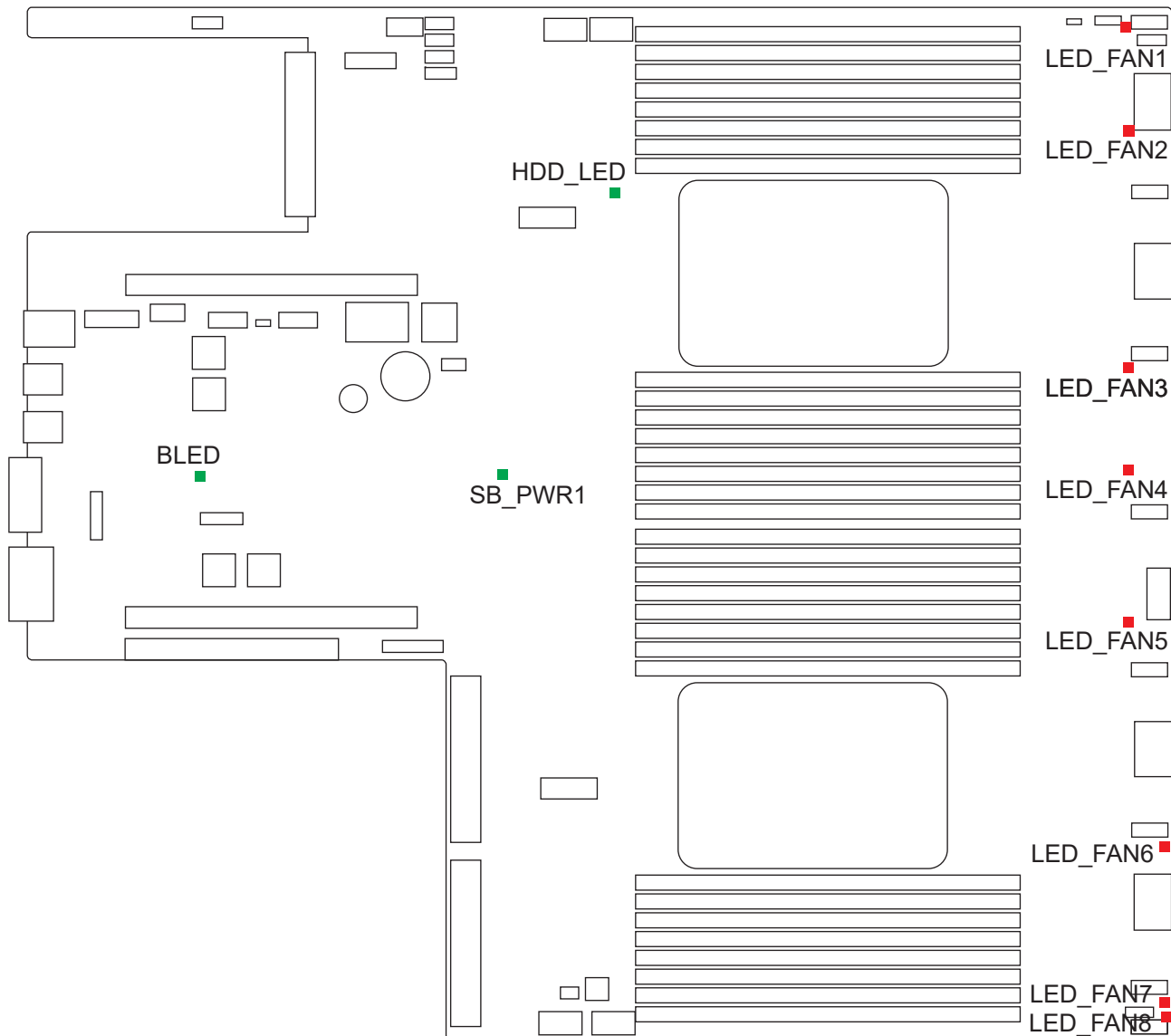
ESPI_LPC	Setting	
Pin 1-2	ESPI	Default
Pin 2-3	LPC	

J ESPI Flash Sharing Jumper (ESPI_SHARE)

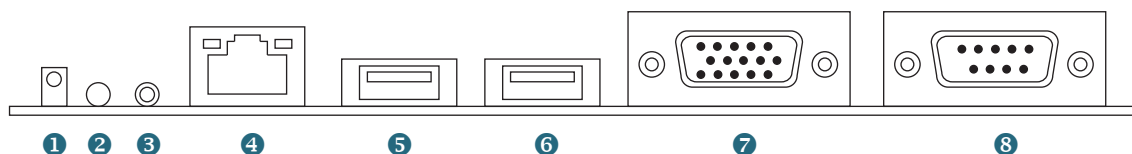
ESPI_SHARE	Setting	
Pin 1-2	Master ESPI Flash Sharing	Default
Pin 2-3	Slave ESPI Flash Sharing	

3.1.5 LED Indicator

Item		Color	Behavior
Fan LED	LED_FAN1~ LED_FAN8	Red	Failure to activate fan.
BMC Heartbeat LED	BLED	Green	BMC is active.
		Off	BMC is not detected.
Standby Power LED	SB_PWR1	Green	STB PWR is ready.
		Off	STB PWR is not detected.
HDD LED	HDD_LED	Green	HDD is active.
		Off	HDD is not detected.

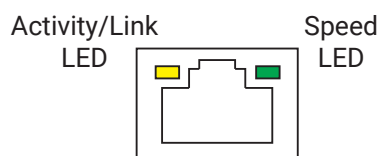


3.1.6 Input and Output Panel



Item		Placement	Item		Placement
1	System Fault LED	SYS_FAULT_LED1	5	USB 3.2 Gen1 Port	USB3_1
2	Power Button	PWR_BTN1	6	USB 3.2 Gen1 Port	USB3_2
3	UID Switch	U1D1	7	VGA port	VGA1
4	LAN RJ45 Port	IPMI_LAN	8	Serial Port	COM1

LAN port LED Indicator

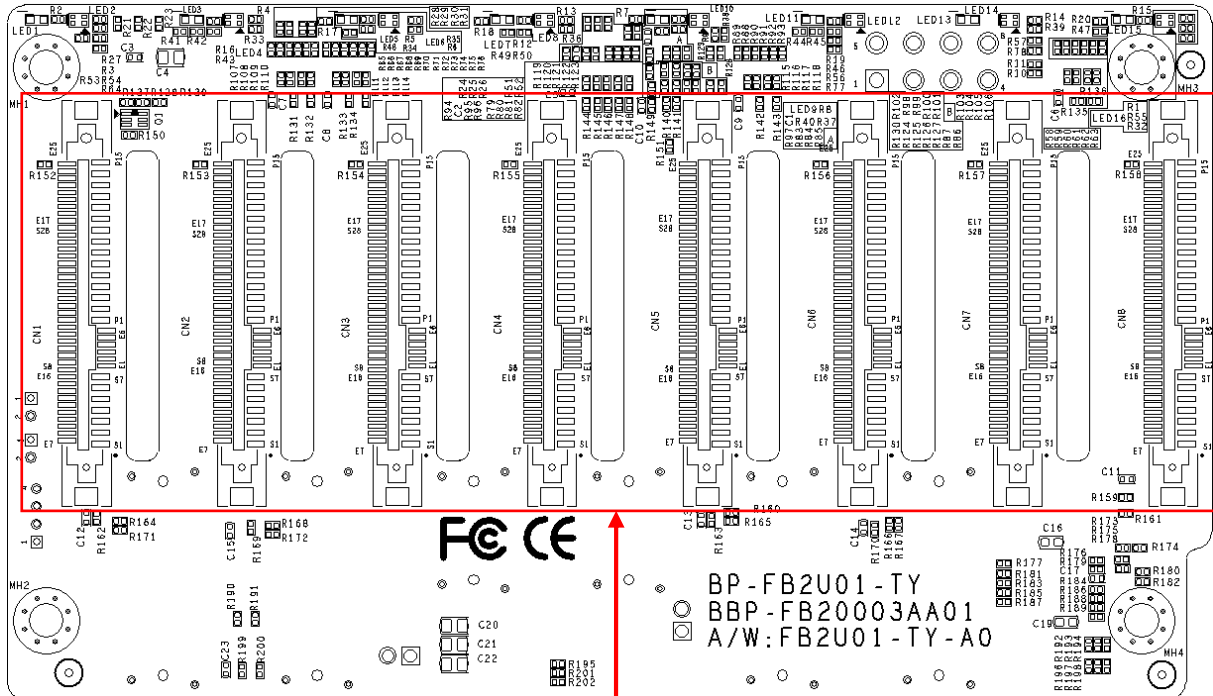


Item	Color	Behavior
Activity/Link LED	Off	No link.
	Yellow (blinking)	Data activity.
	On	Link.
Speed LED	Off	10M bps connection or no link.
	Off	100M bps connection.
	Green	1G bps connection.

3.2 Drive Backplane: 8 Bay

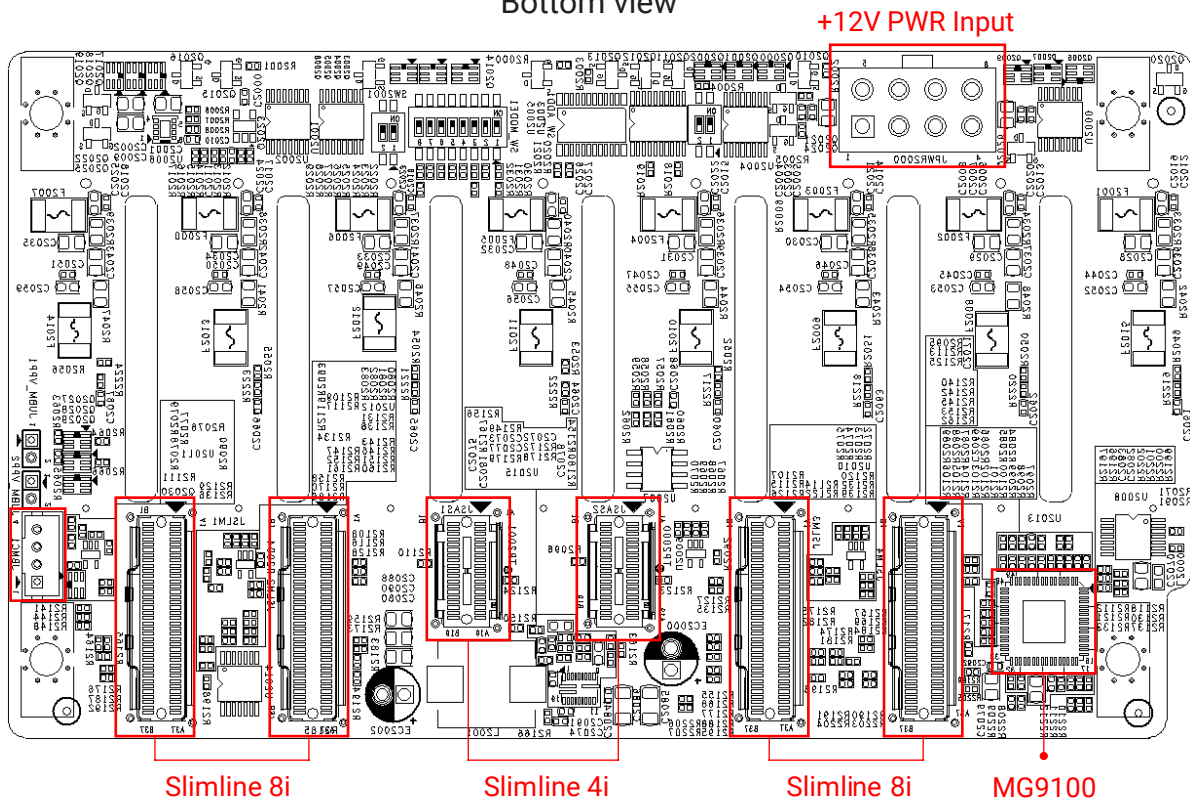
3.2.1 Placement

Top view



8BAYU.2/SAS Receptacle Connector

Bottom view



External I2C

Slimline 8i

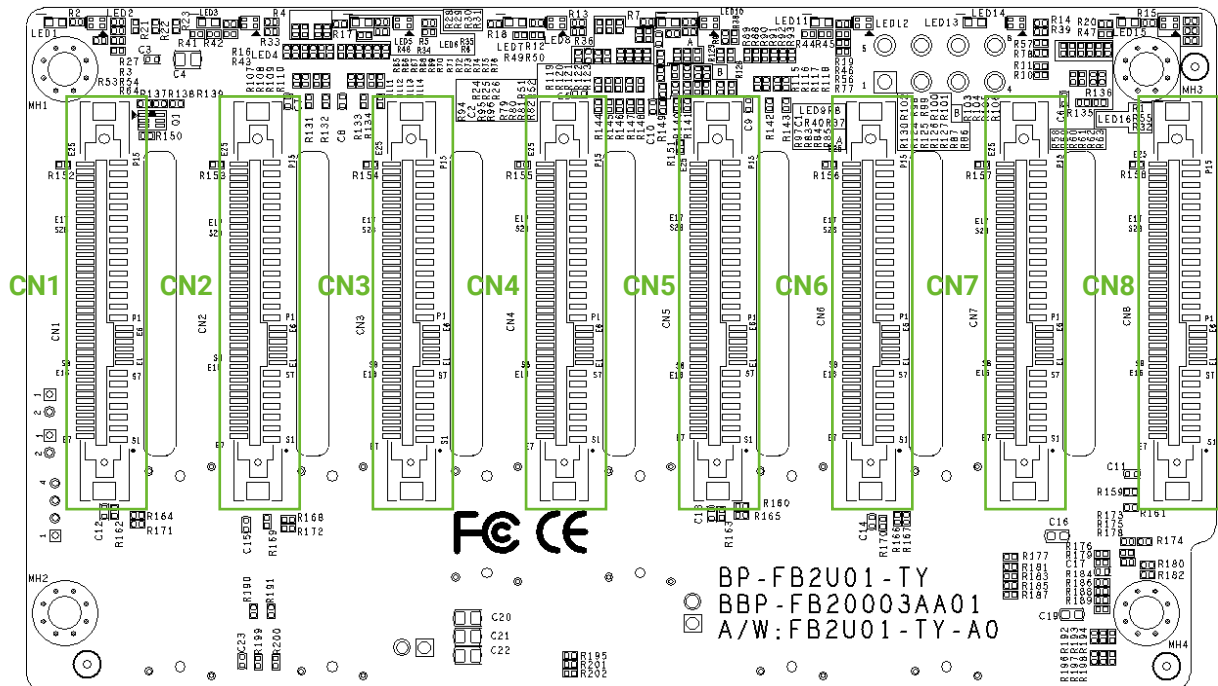
Slimline 4i

Slimline 8i

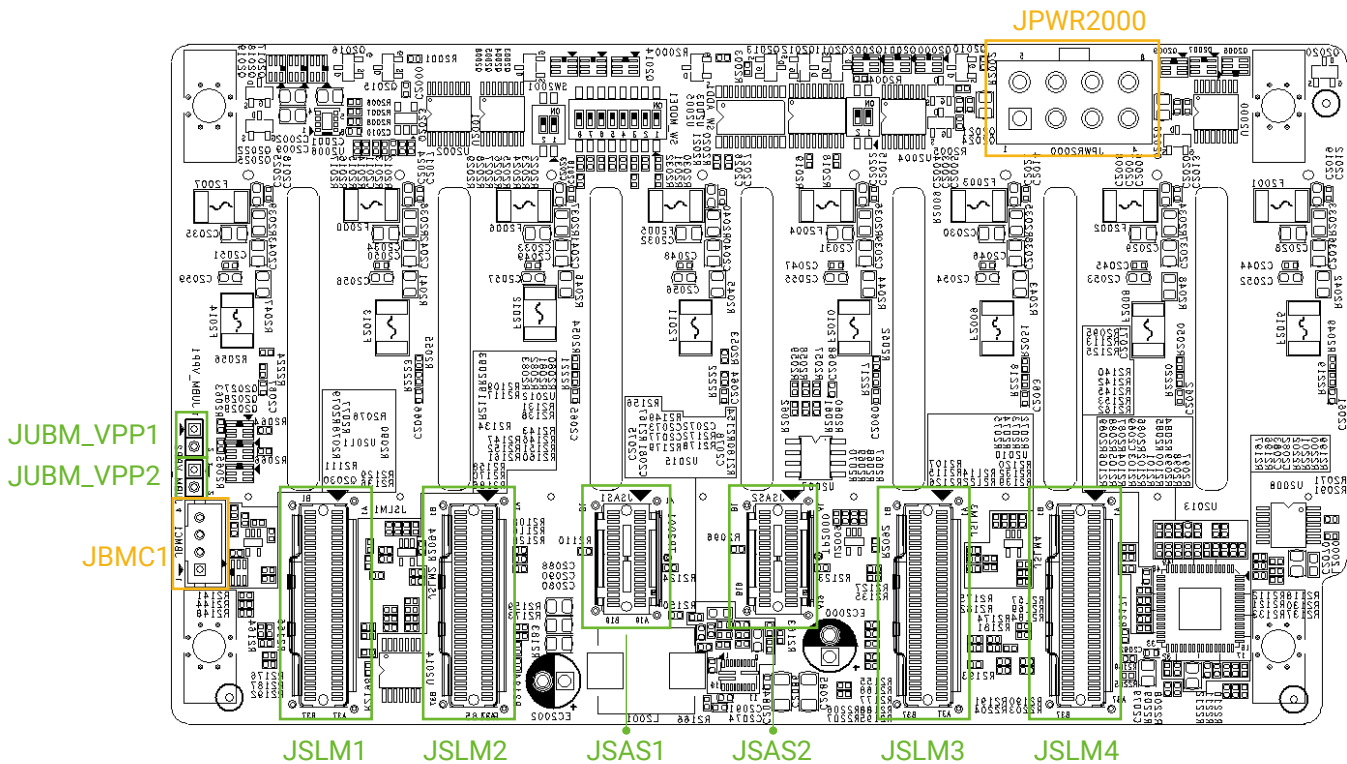
MG9100

3.2.2 Connector

Top view

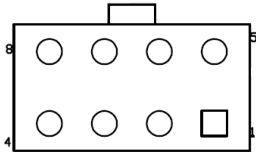


Bottom view



Power Supply (JPWR 2000)

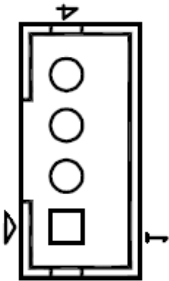
1x4 power connector, PH5.5 / +12V power in, 7A per pin



+12V	5	1	GND
+12V	6	2	GND
+12V	7	3	GND
+12V	8	4	GND

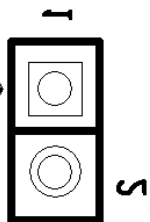
External I2C (JBMC1)

2x4 pin box header, PH2.0



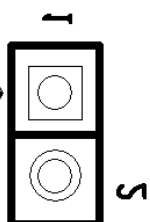
1	SMB_ALARM_N
2	GND
3	BP_BMC_SDA
4	BP_BMC_SCL

JUMB_VPP1



Open	VPP Operation(default)
Short	UBM Operation

JUBM_VPP2



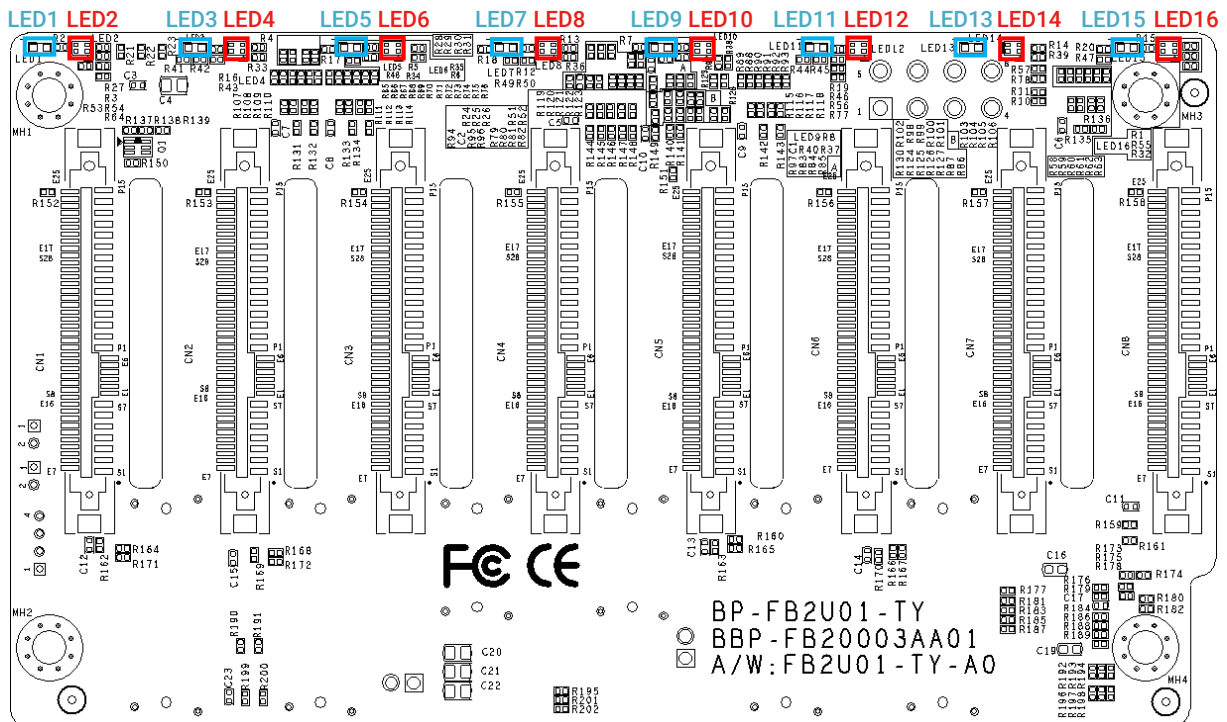
Open	VPP Operation(default)
Short	UBM Operation

- Device Mapping

JSLM1	CN1	NVMe HDD1
	CN2	NVMe HDD2
JSLM2	CN3	NVMe HDD3
	CN4	NVMe HDD4
JSLM3	CN5	NVMe HDD5
	CN6	NVMe HDD6
JSLM4	CN7	NVMe HDD7
	CN8	NVMe HDD8
JSAS1	CN1	SAS HDD1
	CN2	SAS HDD2
	CN3	SAS HDD3
	CN4	SAS HDD4
JSAS2	CN5	SAS HDD5
	CN6	SAS HDD6
	CN7	SAS HDD7
	CN8	SAS HDD8

3.2.3 LED Indicator

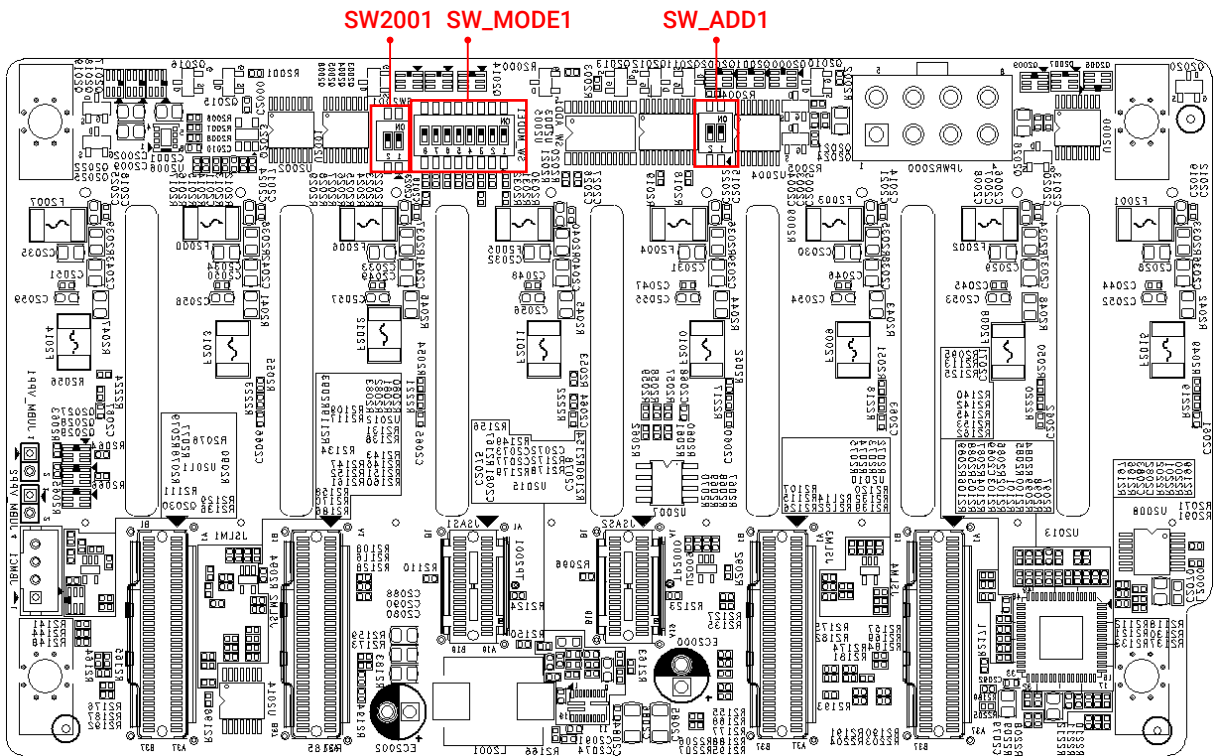
Top view

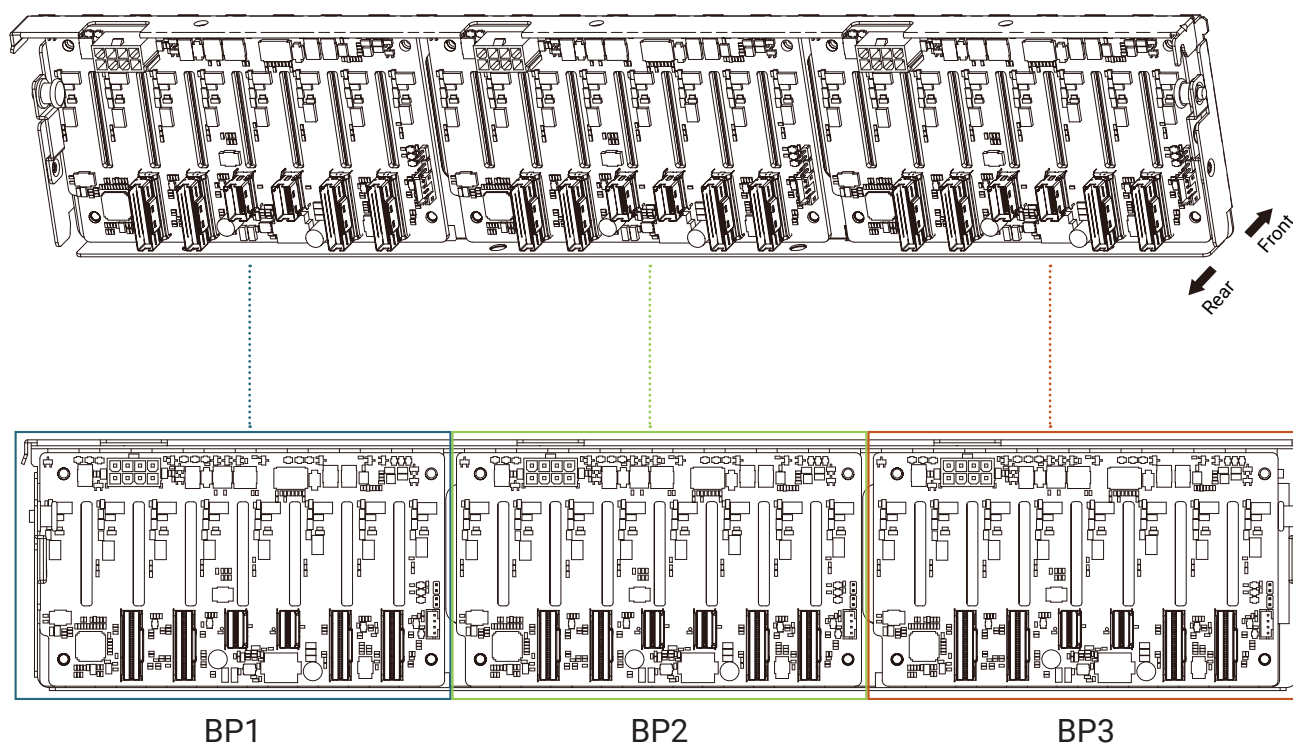


Indicator		Color	Behavior
HDD1-8 Activity LED	LED	Blue (On)	HDD present
	1		
	3		
	5	Blue (Blinking)	HDD activity is detected or External control.
	7		
	9		
	11	Off	HDD is not connected.
	13		
	15		
HDD1-8 Fail LED	LED	Yellow (On)	HDD Fault
	2		
	4		
	6	Yellow (Blinking)	HDD Rebuild
	8		
	10		
	12	Off	Normal
	14		
	16		
HDD1-8 Locate LED	LED	GEN (On)	HDD Locate
	2		
	4		
	6		
	8	Off	Normal
	10		
	12		
	14		
	16		

3.2.4 DIP-Switch

Bottom view





There are three 8-bays backplanes used in SB201-A6, please refer to the following table for default value.

SW2001



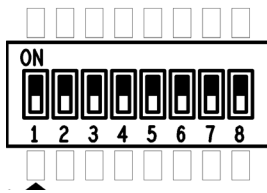
Pin1	Pin2	9100 BMC Address	SGPIO Configurations	Default
off	off	0xC6h	Conf#F	BP 3
on	off	0xC4h	Conf#E	BP 2
off	on	0xC2h	Conf#D	BP 1
on	on	0xC0h	Conf#C	

SW_ADD1



Pin1	Pin2	PCA9548 Address	LM75 Address	Default
off	off	0xE6h	0x96h	BP 3
on	off	0xE4h	0x94h	BP 2
off	on	0xE2h	0x92h	BP 1
on	on	0xE0h	0x90h	

SW502



Vendor ID & Strapping Configuration Settings								
Vendor	Pin1	Pin2	Pin3	Pin4	Pin5	Pin6	Pin7	Pin8
UBM only	on	off	off	on	off	off	off	off
Avago	Avago SHP ID				on	on	on	off
AMD	SHP0 ID			SHP1 ID			off	on
Intel (Default)	VPP0 ID		VPP1 ID		on	on	on	on

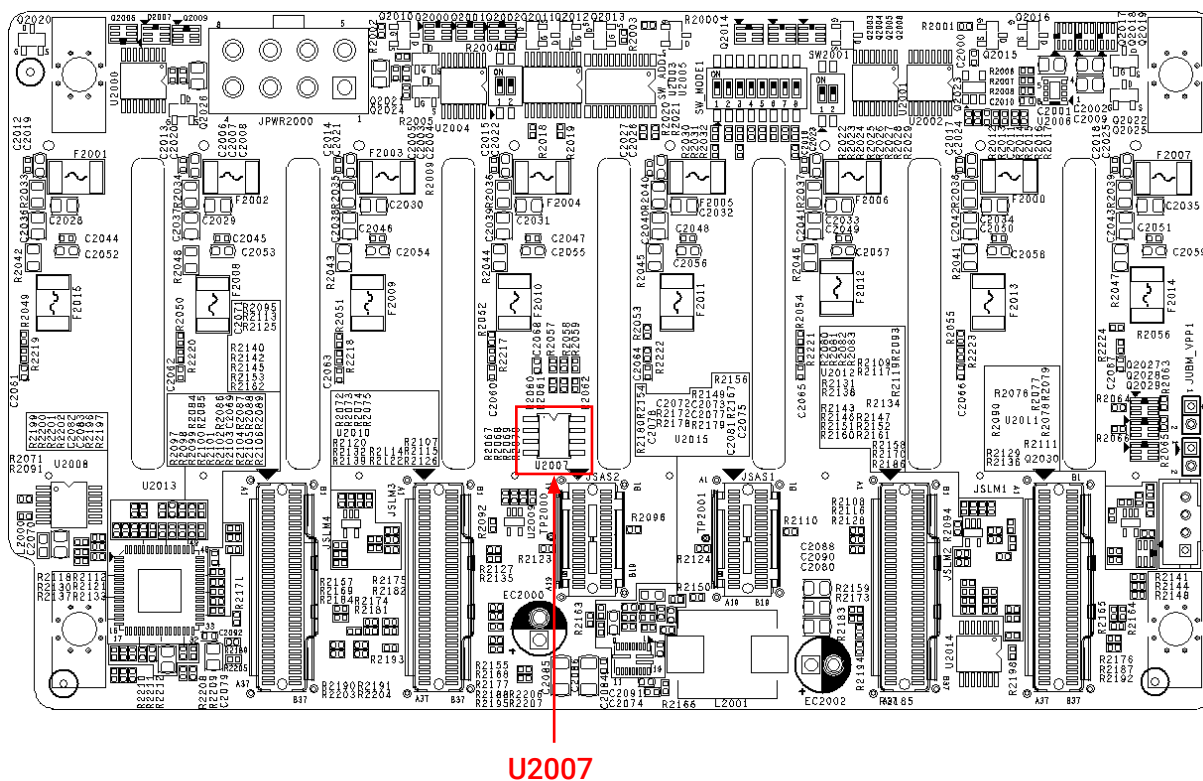
SHP ID Configuration Settings for Avago SHP Mode					
Pin1	Pin2	Pin3	Pin4	SHP0 SMBUS Address	SHP1 SMBUS Address
off	off	off	off	0x40h/0x42h/0x44h/0x46h (Default)	0x48h/0x4Ah/0x4Ch/0x4Eh (Default)
on	off	off	off	0x50h/0x52h/0x54h/0x56h	0x58h/0x5Ah/0x5Ch/0x5Eh
off	on	off	on	0x60h/0x62h/0x64h/0x66h	0x68h/0x6Ah/0x6Ch/0x6Eh
on	on	off	on	0x70h/0x72h/0x74h/0x76h	-

SHP0 ID & SHP1 ID Configuration Settings for AMD Mode							
Pin1	Pin2	Pin3	SHP0 SMBUS Address	Pin4	Pin5	Pin6	SHP1 SMBUS Address
off	off	off	0x50h/0x52h (Default)	off	off	off	0x50h/0x52h (Default)
on	off	off	0x54h/0x56h	on	off	off	0x54h/0x56h
off	on	off	0x58h/0x5Ah	off	on	off	0x58h/0x5Ah
on	on	off	0x5Ch/0x5Eh	on	on	off	0x5Ch/0x5Eh
off	off	on	0x60h/0x62h	off	off	on	0x60h/0x62h
on	off	on	0x64h/0x66h	on	off	on	0x64h/0x66h
off	on	on	0x68h/0x6Ah	off	on	on	0x68h/0x6Ah
on	on	on	0x6Ch/0x6Eh	on	on	on	0x6Ch/0x6Eh

VPP0 ID & VPP1 ID Configuration Settings for INTEL Mode					
Pin1	Pin2	VPP0 SMBUS Address	Pin3	Pin4	VPP1 SMBUS Address
off	off	0x40h/0x42h (Default)	off	off	0x40h/0x42h (Default)
off	on	0x44h/0x46h	off	on	0x44h/0x46h
on	off	0x48h/0x4Ah	on	off	0x48h/0x4Ah
on	on	0x4Ch/0x4Eh	on	on	0x4Ch/0x4Eh

3.2.5 Thermal Sensor location

Bottom view

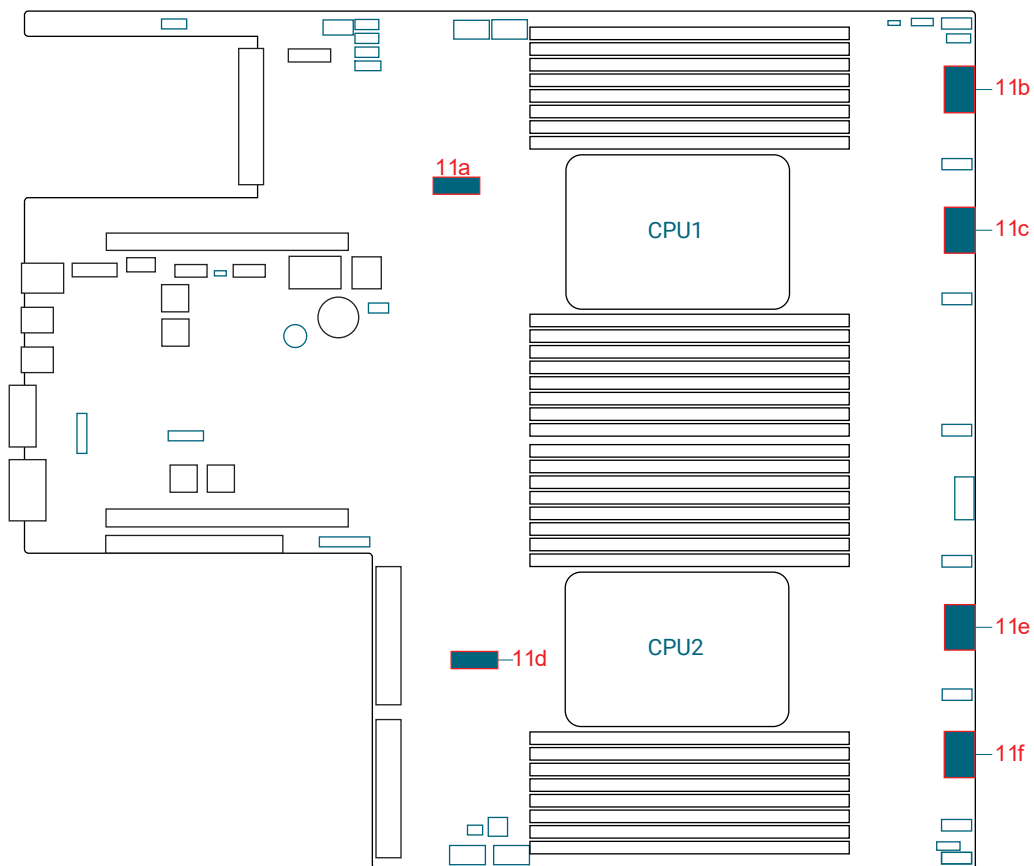


Location	Value
U2007	TI LM75

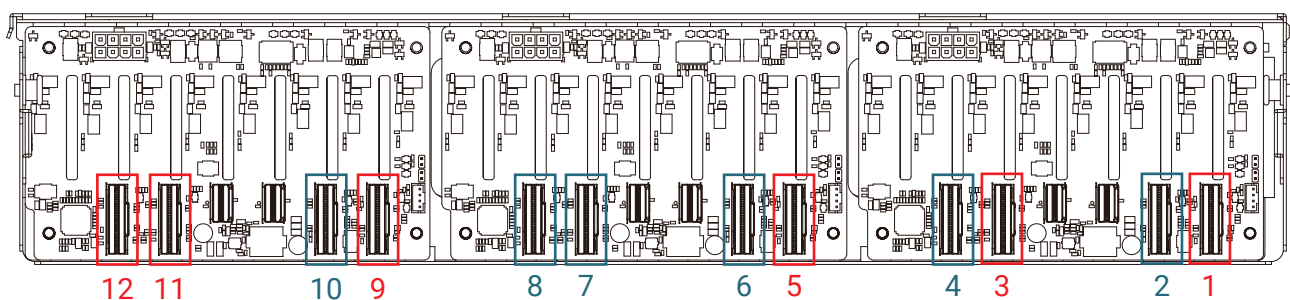
3.2.6 Cable connection

Motherboard to tri-mode backplane.

MB	BP
SLIM1 (11a)	11
SLIM2 (11b)	12
SLIM3 (11c)	9
SLIM4 (11d)	5
SLIM5 (11e)	3
SLIM6 (11f)	1



Motherboard

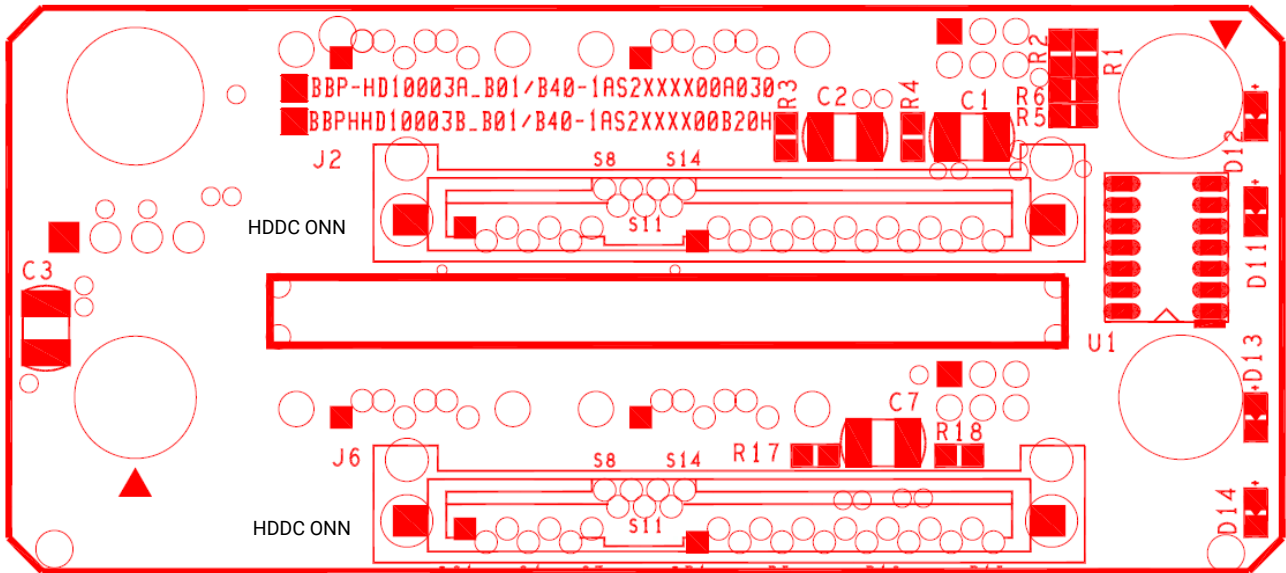


Backplane

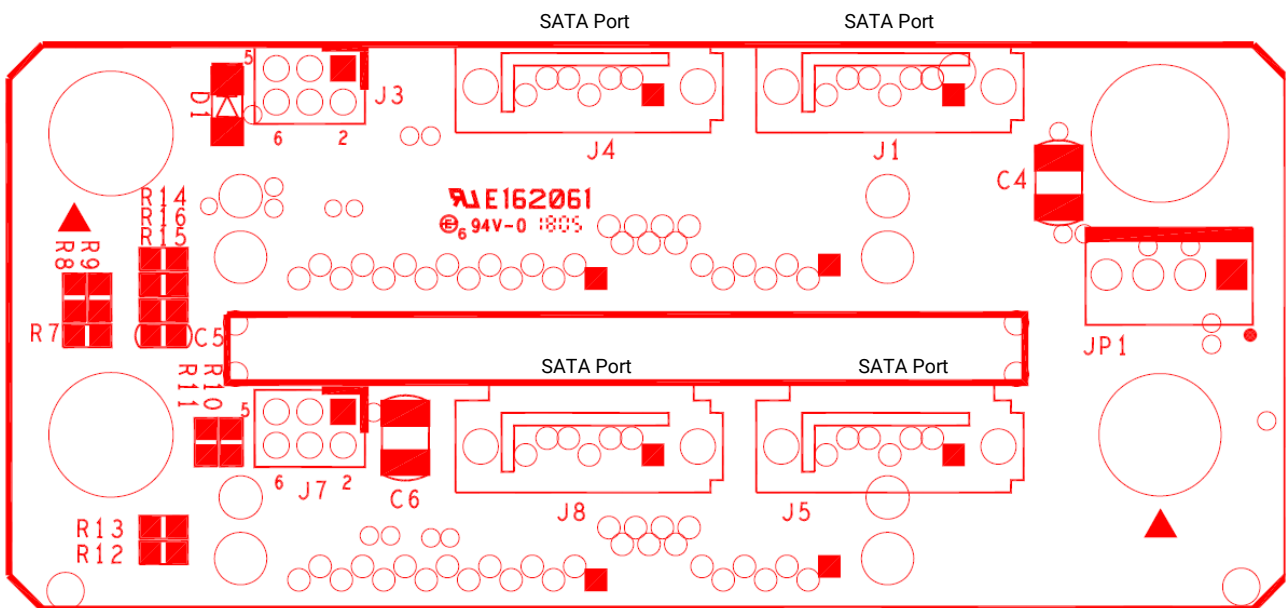
3.3 Drive Backplane: 2 Bay

3.3.1 Placement

Top view



Bottom view



3.3.2 Connector

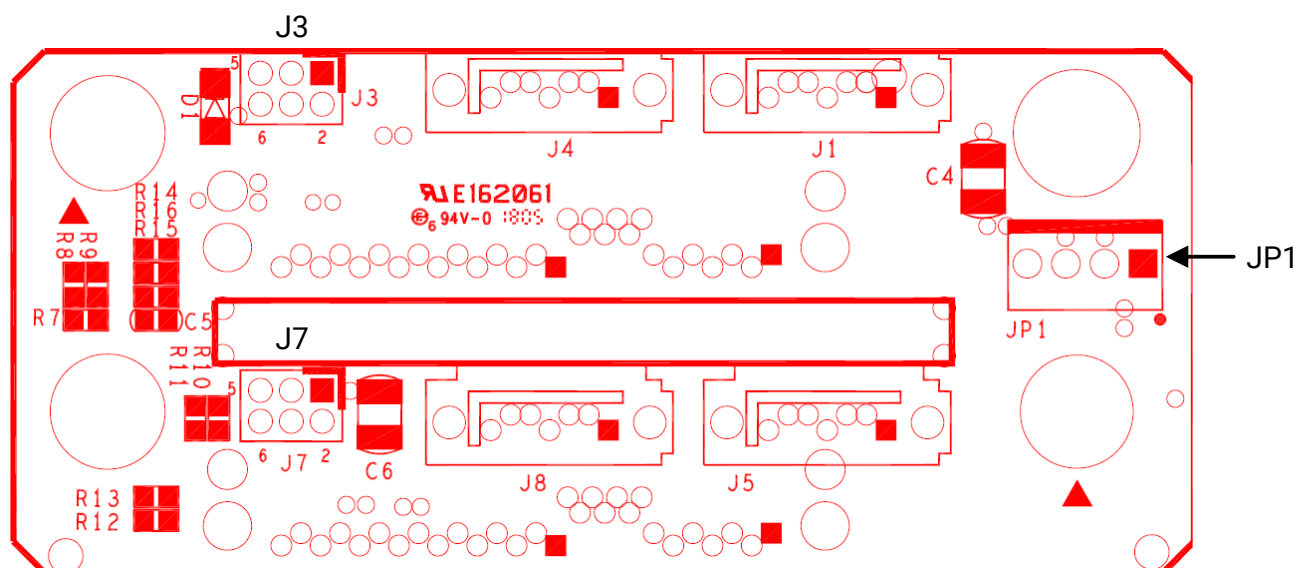
External connectors

Function	Description	Comments
SAS (J2,J6)	SFF-8482 Receptacle	SAS 6G / SATA 6G
SATA(J1,J4,J5,J8)	SATA CONN	SATA 6G

Internal connectors

Function	Description	Comments
BP Power (JP1)	1x4 pin wafer	+12V, +5V input
LED I/O (J3,J7)	2x3 pin header	LED mode option

Bottom view

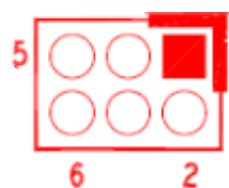


Power (JP1)



1	12V
2	GND
3	GND
4	5V

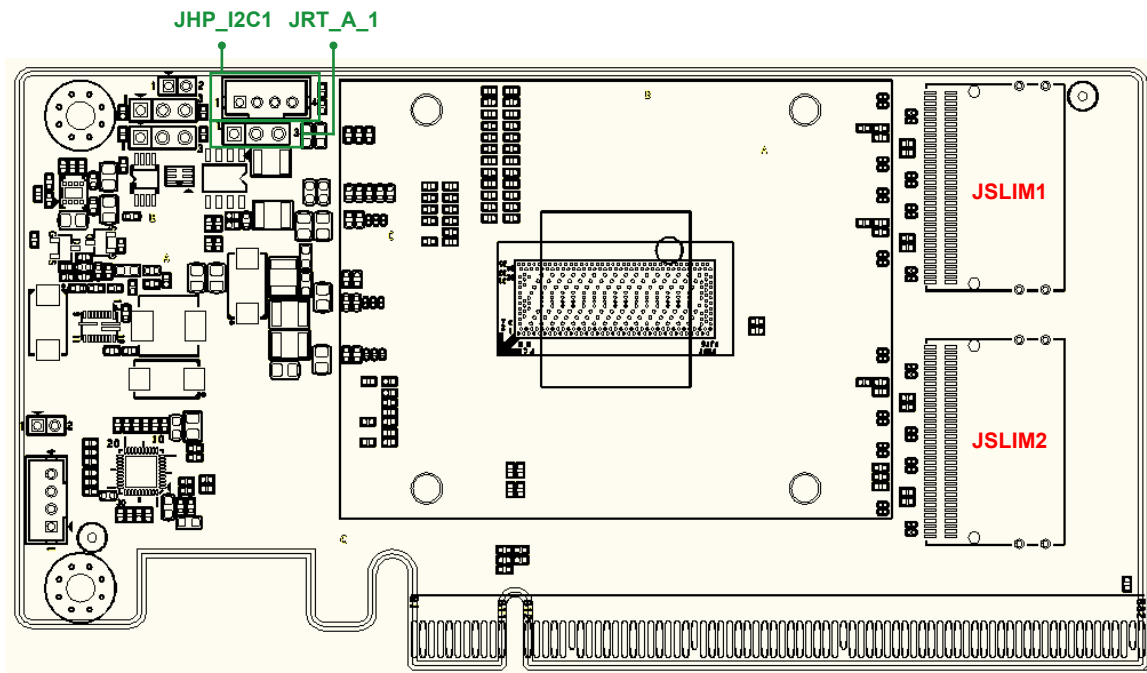
LED I/O (J3,J7)



LED signal output	Pin	Description	From External input control	Pin	Description
	1	Fail LED		2	Fail LED0
	3	4V3		4	GND
	5	ACC LED0		6	ACC LED0

3.4 Retimer Card (two x4x4)

3.4.1 Placement



Internal connectors

Function	Description	Comments
Retimer DownStream PCIe 4 (JSLIM1,JSLIM2)	SFF-8654 right angle	PCIe Gen4 x4x4
Hot Plug I2C(JHP-I2C1)	1x4pin box-header	External Hot-plug I2C box header
Retimer / EEPROM debug port (JRT_A_1)	1x3pin header	I2C

3.4.2 Connector

External Hot plug I2C (JHP_I2C1)



1	HP_I2C_SDA
2	GND
3	HP_I2C_SCL
4	HP_INT_R#

External Retimer EEPROM debug port (JRT_A_1)

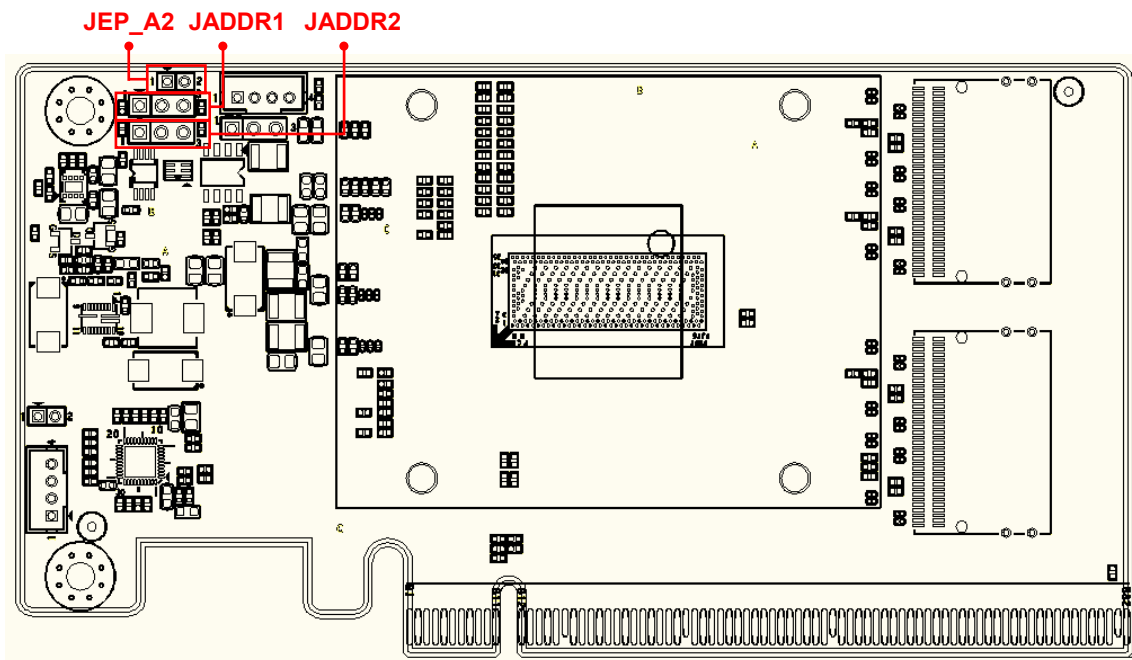


1	RTMR_EE_SDA
2	GND
3	RTMR_EE_SCL

3.4.3 Jumper

EEPROM Address Jumper Setting	JEP_A2	Retimer Address Jumper Setting	JADDR1	JADDR2
0xA0	1-2	0x20	2-3	2-3
0xA8	Open			
0xA2	1-2	0x24	Open	2-3
0xAA	Open			
0xA2	1-2	0x26	1-2	2-3
0xAA	Open			
0xA4	1-2	0x38	2-3	1-2
0xAC	Open			
0xA6	1-2	0x3C	Open	1-2
0xAE	Open			
0xA6	1-2	0x3E	1-2	1-2
0xAE	Open			

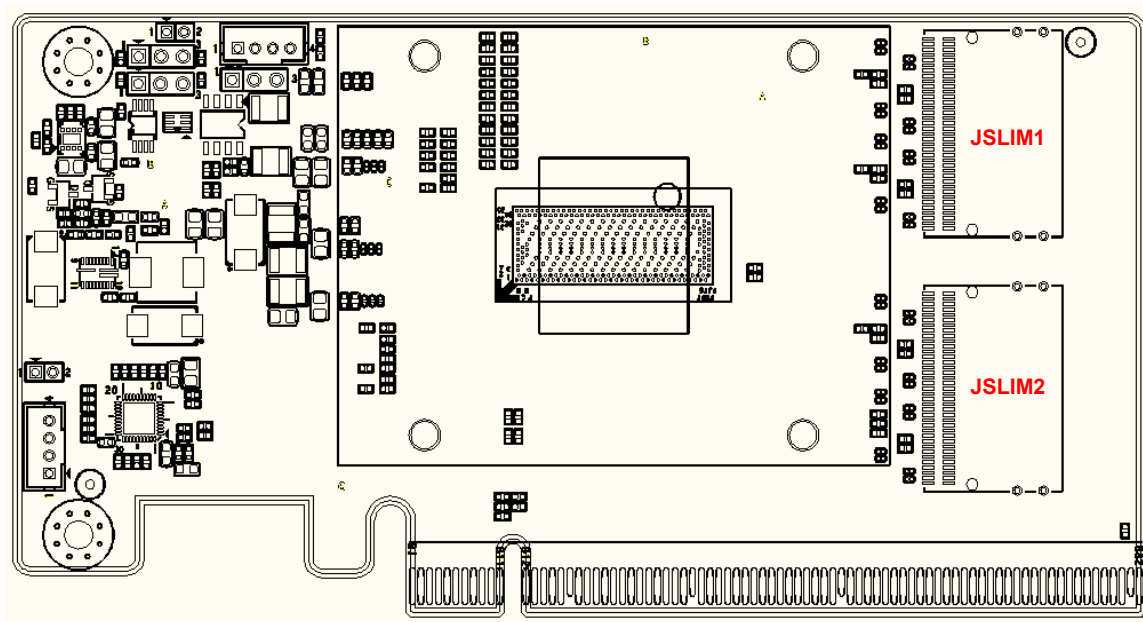
Placement



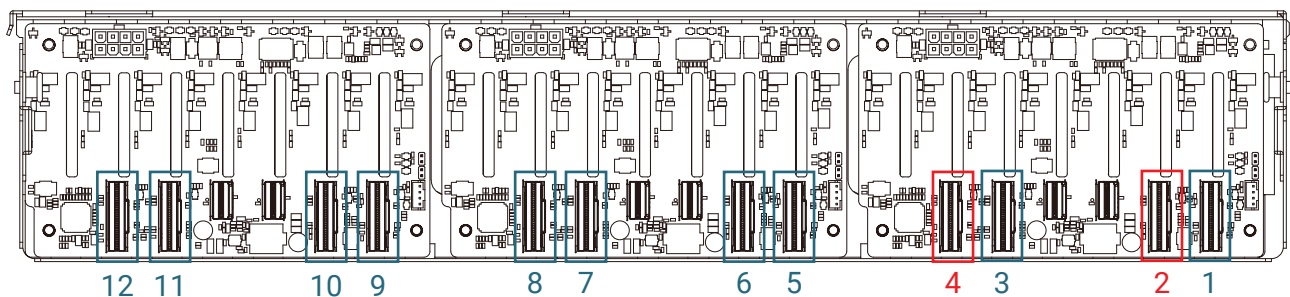
3.4.4 Cable connection

Retimer Card(two x4x4) to tri-mode backplane.

Retimer Card	BP
JSLIM1	4
JSLIM2	2



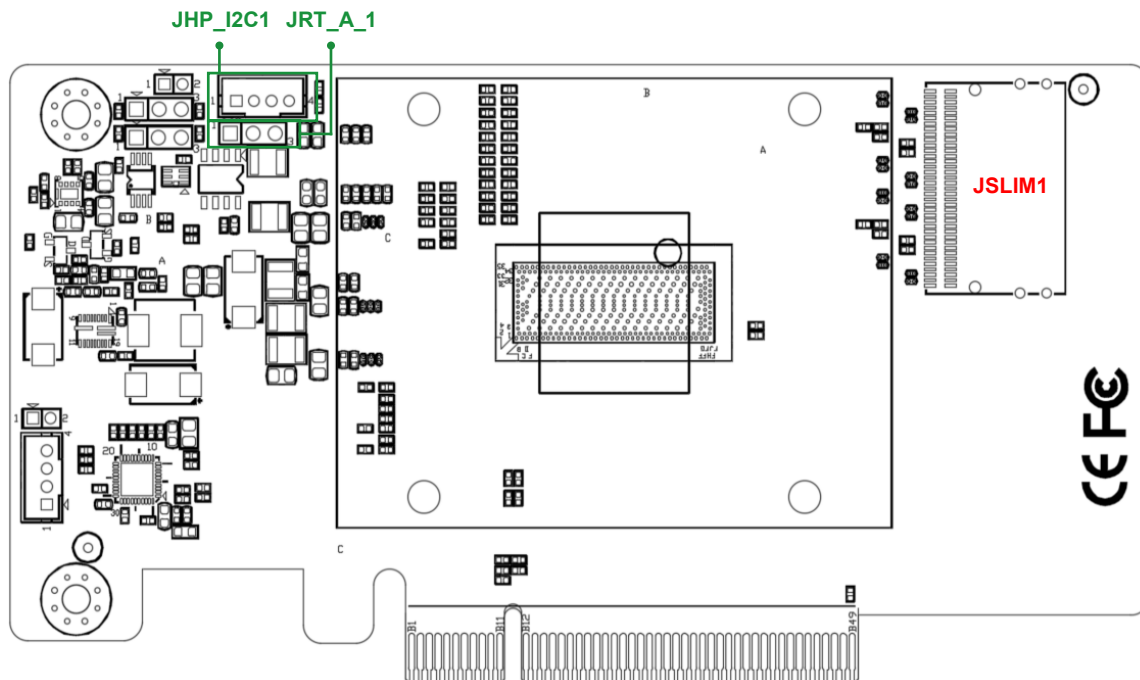
Retimer Card



Backplane

3.5 Retimer Card (one x4x4)

3.5.1 Placement



Internal connectors

Function	Description	Comments
Retimer DownStream PCIe 4 (JSLIM1)	SFF-8654 right angle	PCIe Gen4 x4x4
Hot Plug I2C(JHP-I2C1)	1x4pin box-header	External Hot-plug I2C box header
Retimer / EEPROM debug port (JRT_A_1)	1x3pin header	I2C

3.5.2 Connector

External Hot plug I2C (JHP_I2C1)



1	HP_I2C_SDA
2	GND
3	HP_I2C_SCL
4	HP_INT_R#

External Retimer EEPROM debug port (JRT_A_1)

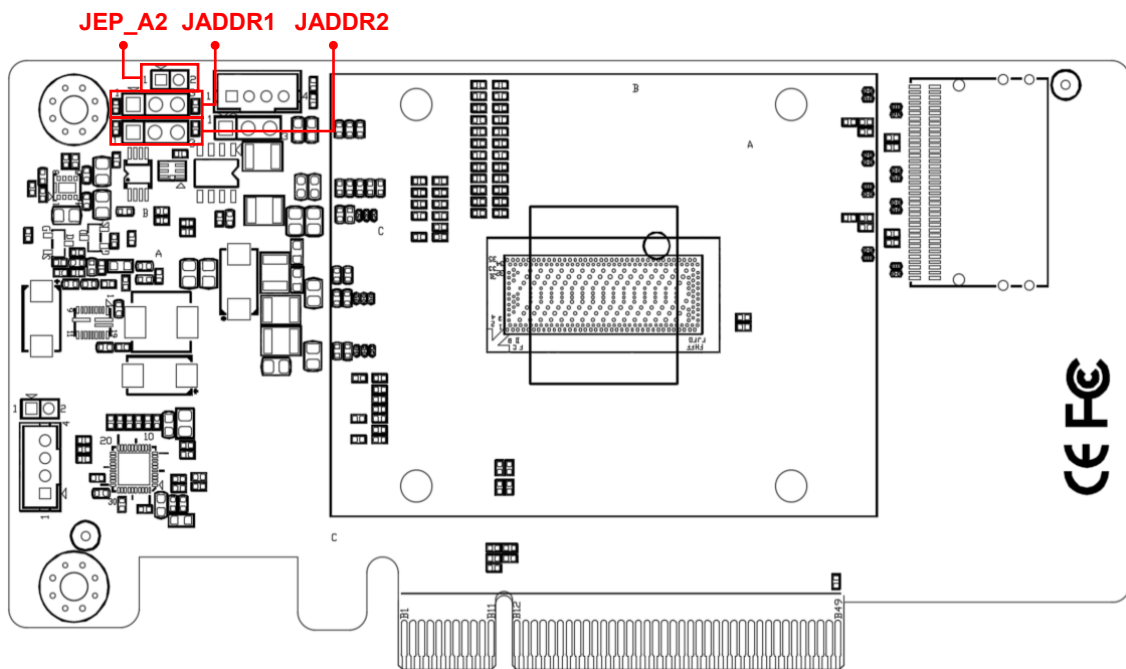


1	RTMR_EE_SDA
2	GND
3	RTMR_EE_SCL

3.5.3 Jumper

EEPROM Address Jumper Setting	JEP_A2	Retimer Address Jumper Setting	JADDR1	JADDR2
0xA0	1-2	0x20	2-3	2-3
0xA8	Open			
0xA2	1-2	0x24	Open	2-3
0xAA	Open			
0xA2	1-2	0x26	1-2	2-3
0xAA	Open			
0xA4	1-2	0x38	2-3	1-2
0xAC	Open			
0xA6	1-2	0x3C	Open	1-2
0xAE	Open			
0xA6	1-2	0x3E	1-2	1-2
0xAE	Open			

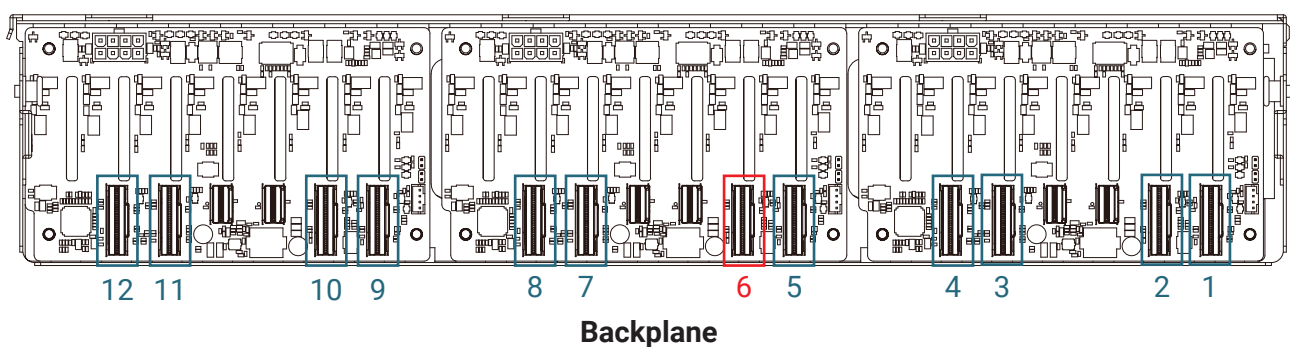
Placement



3.5.4 Cable connection

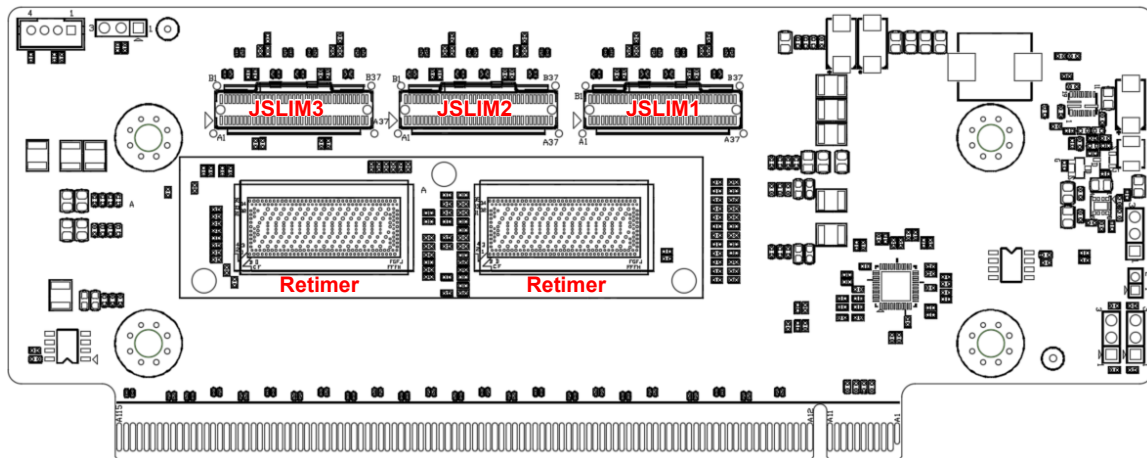
Retimer Card(one x4x4) to tri-mode backplane.

Retimer Card	BP
JSLIM1	6



3.6 Retimer Card (three x4x4)

3.6.1 Placement



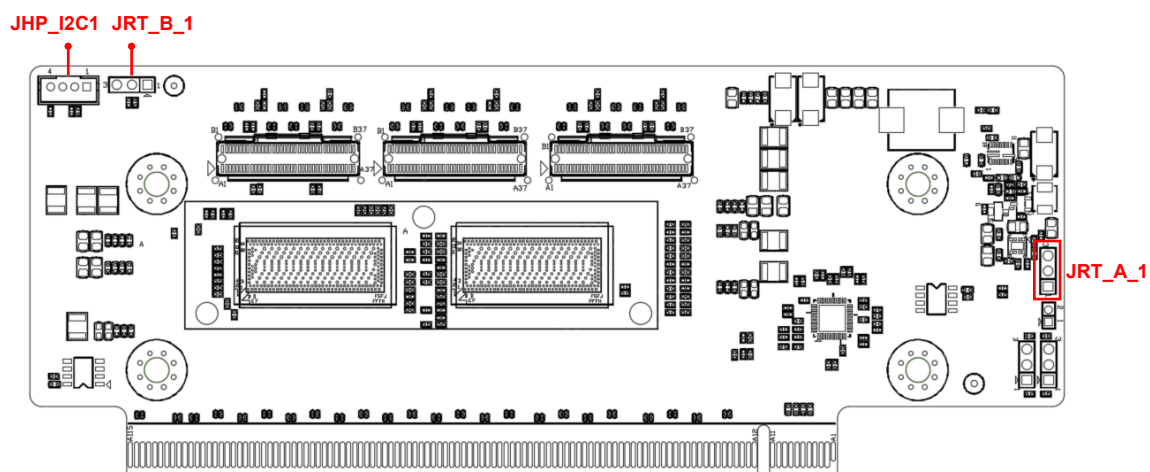
External connectors

Function	Description	Comments
Retimer DownStream PCIe 4 (JSLIM1,JSLIM2,JSLIM3)	SFF-8654 Vertical	PCIe Gen4 x4x4 / per CONN

Internal connectors

Function	Description	Comments
Hot Plug I2C (JHP-I2C1)	1x4pin box-header	External Hot-plug I2C box header
Retimer / EEPROM debug port (JRT_A_1,JRT_B_1)	1x3pin header	I2C

3.6.2 Connector



External Hot plug I2C (JHP_I2C1)



1	HP_I2C_SDA
2	GND
3	HP_I2C_SCL
4	HP_INT_R#

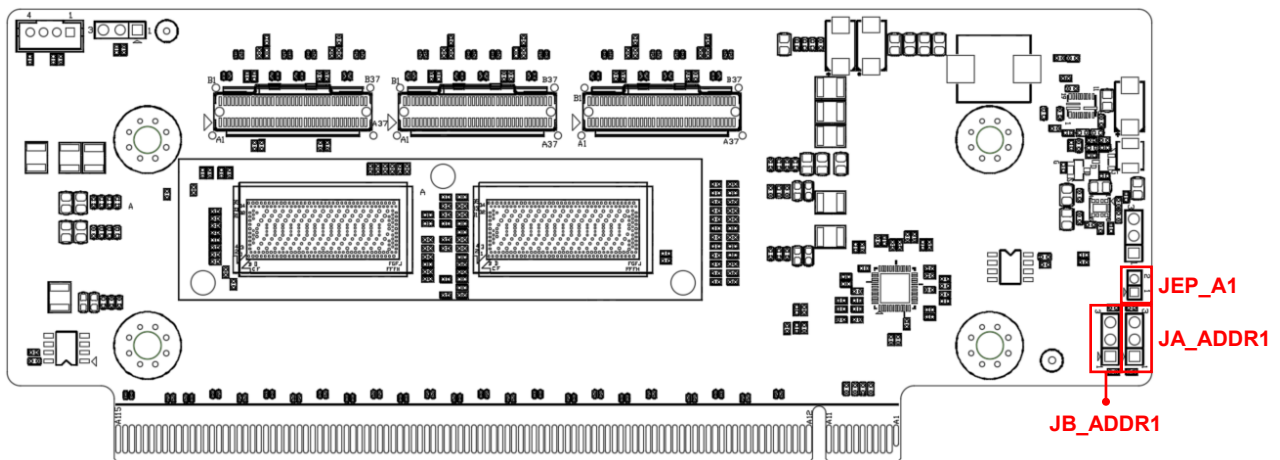
Retimer I2C debug port (JRT_A_1 , JRT_B_1)



1	Retimer_SCL
2	GND
3	Retimer_SDA

3.6.3 Jumper

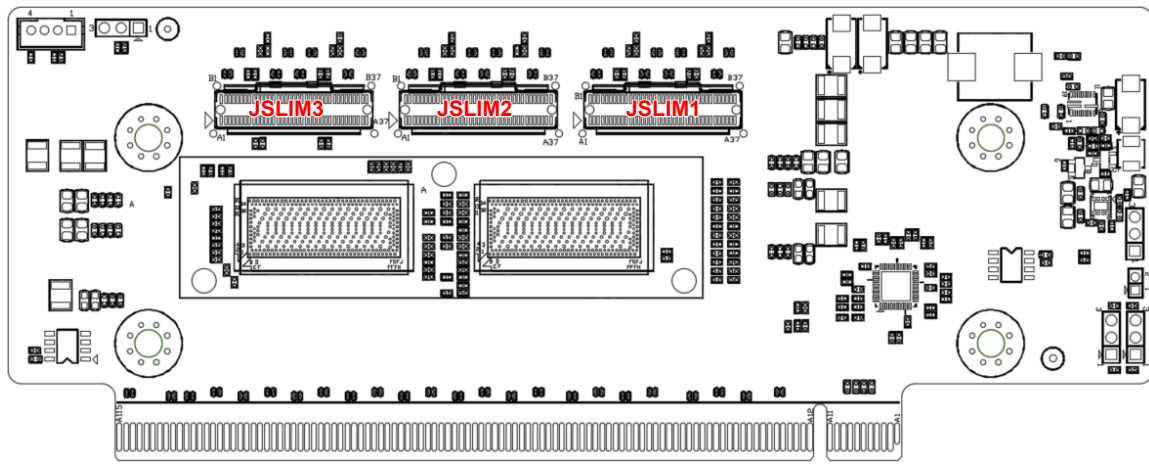
EEPROM Address Jumper Setting	JEP_A2	Retimer Address Jumper Setting		JADDR1	JADDR2
Address	2 Pin	JSLIM1/JSLIM2	JSLM3	3 Pin	3 Pin
0xA0	1-2 Short	0x20	0x38	2-3 Short	2-3 Short
0xA8	Pin Open				
0xA4	1-2 Short	0x20	0x3C	2-3 Short	Pin Open
0xAC	Pin Open	0x20	0x3E	2-3 Short	1-2 Short
0xA2	1-2 Short	0x24	0x38	Pin Open	2-3 Short
0xAA	Pin Open				
0xA6	1-2 Short	0x24	0x3C	Pin Open	Pin Open
0xAE	Pin Open	0x24	0x3E	Pin Open	1-2 Short
0xA2	1-2 Short	0x26	0x38	1-2 Short	2-3 Short
0xAA	Pin Open				
0xA6	1-2 Short	0x26	0x3C	1-2 Short	Pin Open
0xAE	Pin Open	0x26	0x3E	1-2 Short	1-2 Short



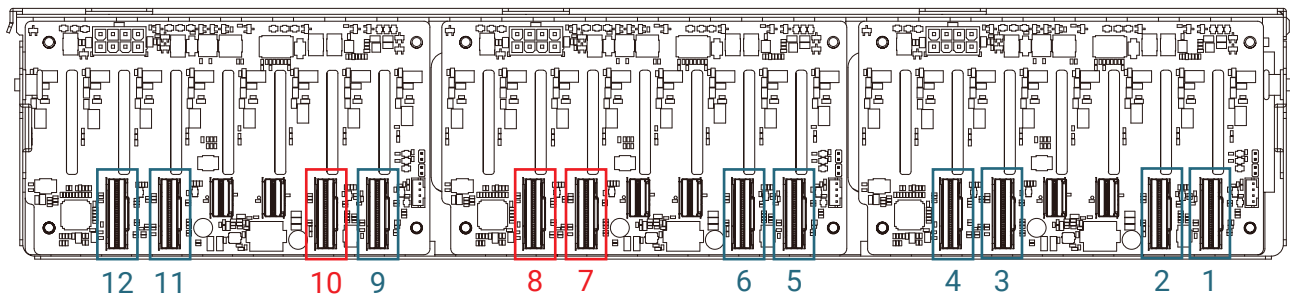
3.6.4 Cable connection

Retimer Card(three x4x4) to tri-mode backplane.

Retimer Card	BP
JSLIM1	8
JSLIM2	7
JSLIM3	10



Retimer Card



Backplane

Chapter 4. BIOS Configuration Settings

This chapter demonstrates how to configure the UEFI BIOS settings in your system device. You can enter the BIOS screen during system startup.

To enter BIOS configuration settings,

- Press **F2** or **Delete** key during the Power-On-Self-Test (POST)

To enter BIOS after POST, you have to restart the system by using one of the three methods:

- Press **Ctrl + Alt + Delete**.
- Press the reset button on the system chassis.
- Turn the system off and on.



NOTE

The following pages provide the details of BIOS menu. Please be noted that the BIOS menu are continually changing due to the BIOS updating. The BIOS menu provided are the most updated ones when this manual is written.

4.1 Navigation Keys

The navigation keys are listed below.

Function Key	Description
< ↑ > < ← > < → > < ↓ >	Select item.
< Tab >	Next function
< Enter >	Select and enter sub-screen.
< + > < - >	Modify selected option.
< PGUP >	Previous page
< PGDN >	Next page.
< HOME >	Top screen.
< F1 >	General help.
< F7 >	Discard Change and Exit.
< F9 >	Load Optimal Default for all values.
< F10 >	Save changes and exit.
< F12 >	Print Screen.
< Esc >	Exit the current menu screen.

4.2 BIOS Setup

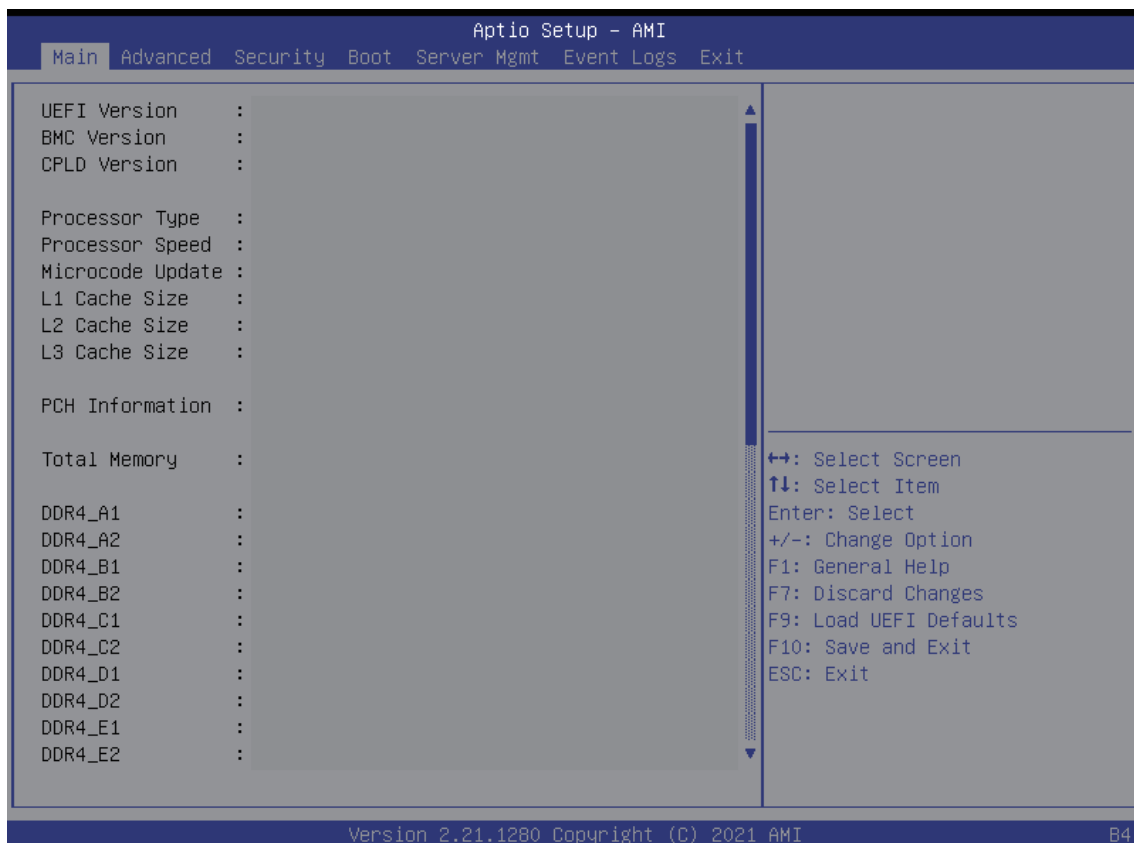
4.2.1 Menu

Press **←** and **→** to select the options of the menu bar.

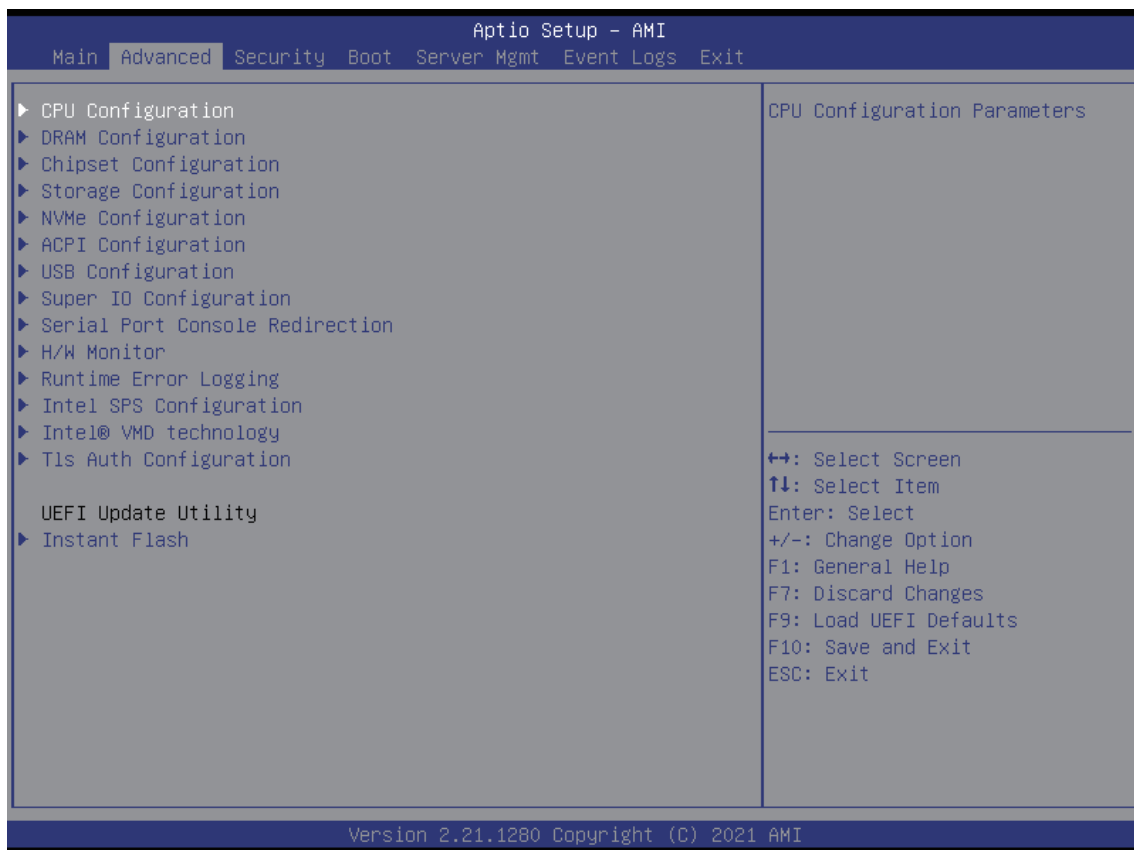
Press **Enter** to access the option screen.

Menu	Description
Main	Displays basic system information and date & time.
Advanced	Allows configuration of advanced system settings.
Security	Sets passwords and security functions.
Boot	Sets boot options, such as Quick Boot or USB Boot.
Server Management	Configure BMC parameter.
Event Logs	Configure Smbios event logs.

4.3 Main



4.4 Advanced



4.4.1 CPU Configuration

CPU Configuration	
Intel(R) Speed Step Technology	Intel(R) SpeedStep technology allows processors to switch between multiple frequencies and voltage points for better power saving and heat dissipation. CPU turbo ratio can be fixed when Intel(R) SpeedStep Technology set Disabled and Intel(R) Turbo Boost Technology set Enabled.
Intel(R) Turbo Boost Technology	Intel(R) Turbo Boost Technology enables the processor to run above its base operating frequency when the operating system requests the highest performance state.
Intel(R) Hyper Threading Technology	Intel(R) Hyper Threading Technology allows multiple threads to run on each core, so that the overall performance on threaded software is improved.
Long Duration Maintained	Configure the period of time until the CPU ratio is lowered when the Long Duration Power Limit is exceeded.
Short Duration Power Limit	Configure Package Power Limit 2 in watts. When the limit is exceeded, the CPU ratio will be lowered immediately. A lower limit can protect the CPU and save power, while a higher limit may improve performance.
Long Duration Power Limit	Configure Package Power Limit 1 in watts. When the limit is exceeded, the CPU ratio will be lowered after a period of time. A lower limit can protect the CPU and save power, while a higher limit may improve performance.
Active Processor 1/2 Cores	Select the number of cores to enable in each processor package.
Enable Intel(R) TXT Support	Enables Intel(R) Trusted Execution Technology Configuration.
Intel(R) Virtualization Technology	Intel(R) Virtualization Technology allows a platform to run multiple operating systems and applications in independent partitions, so that one computer system can function as multiple virtual systems.
Enable SMX	Use this item to enable Safer Mode Extensions.
DCU Streamer Prefetcher	DCU streamer prefetcher is an L1 data cache prefetcher (MSR 1A4h [2]).

Hardware Prefetcher	Automatically prefetch data and code for the processor. Enable for better performance.
Adjacent Cache Line Prefetcher	Automatically prefetch the subsequent cache line while retrieving the currently requested cache line. Enable for better performance.
Package C State Support	Enable CPU, PCIe, Memory, Graphics C State Support for power saving.
CPU C6 State Support	Enable C6 deep sleep state for lower power consumption.
Enhanced Halt State(C1E)	Enable Enhanced Halt State (C1E) for lower power consumption.
Hardware P-States	Disable: Hardware chooses a P-state based on OS Request (Legacy P-States) Native Mode: Hardware chooses a P-state based on OS guidance Out of Band Mode: Hardware autonomously chooses a P-state (no OS guidance)
AES-NI	Use this item to enable or disable AES-NI support.
CPU Thermal Throttling	Enable CPU internal thermal control mechanisms to keep the CPU from overheating.
SNC (Sub NUMA)	SNC disable will support 1-cluster (XPT/KTI Prefetch enable) 4-IMC way interleave. SNC2 Enable supports 2-clusters SNC and 2-way IMC interleave. SNC4 Enable supports 4-clusters SNC and 1-way IMC interleave. Enable SNC2 or SNC4 will gray out iMC_ Interleave knob and Uma Based Clustering knob.

4.4.2 DRAM Configuration

DRAM Configuration	
Enforce POR	Enable to enforce POR restrictions for DDR4 frequency and voltage programming.
DRAM Frequency	If [Auto] is selected, the motherboard will detect the memory module(s) inserted and assign the appropriate frequency automatically.
Numa	Use this item to enable or disable Non Uniform Memory Access (NUMA).
IMC Interleaving	Select to configure IMC Interleaving settings.
Mirror Mode	Mirror Mode will set entire 1LM/2LM memory in system to be mirrored, consequently reducing the memory capacity by half. Mirror Enable will disable XPT Prefetch.
Memory Rank Sparing	Enable or disable Memory Rank Sparing.
ADDDC Sparing	Enable or disable Memory Rank Sparing.
Multi Rank Sparing	Set Multi Rank Sparing number. Default and the maximum is 2 ranks per channel.
Patrol Scrub	Patrol Scrub is a background activity initiated by the processor to seek out and fix memory errors. The default value is [Enabled].
Data Scrambling for DDR4	Enable: Enables data scrambling for DDR4. Disable: Disables this feature. Auto: Sets it to the MRC default setting; current default is Enable.

4.4.3 Chipset Configuration

Chipset Configuration	
MMCFG Base	Use this item to select MMCFG Base.
MMIO High Base	Use this item to select MMIO High Base.
MMIO High Granularity Size	Use this item to select MMIO Granularity Size.
Above 4G Decoding	Enable or disable 64bit capable Devices to be decoded in Above 4G Address Space (only if the system supports 64 bit PCI decoding).
Primary Graphics Adapter	If PCI Express graphics card is installed on the motherboard, you may use this option to select PCI Express or Onboard VGA as the primary graphics adapter.
Onboard VGA	Use this to enable or disable the Onboard VGA function. The default value is [Auto].
VT-d	Intel(R) Virtualization Technology for Directed I/O helps your virtual machine monitor better utilize hardware by improving application compatibility and reliability and providing additional levels of manageability, security, isolation and I/O performance.
PCIe Hot Plug	Use this item to enable or disable PCIe Hot Plug globally.

OCP3 Link Width	This allows you to select OCP3 Link Width. The default value is [x16].
OCP3 Link Speed	This allows you to select OCP3 Link Speed. The default value is [Auto].
OCP3 ASPM Support	This option enables or disables the ASPM support for all CPU downstream devices.
PCIE1/2/3 Link Width	This allows you to select PCIE1 Link Width. The default value is [x16x8].
PCIE1/2/3 Link Speed	This allows you to select PCIE1 Link Speed. The default value is [Auto].
PCIE1/2/3 ASPM Support	This option enables or disables the ASPM support for all CPU downstream devices.
SLIMELIN 1-1/1-2/ 2-1/2-2/3-1/3-2/ 4-1/4-2/5-1/5-2/6-1/ 6-2 Link Speed	This allows you to select SLIMLINE Link Speed. The default value is [Auto].
SLIMELIN 1-1/1-2/ 2-1/2-2/3-1/3-2/ 4-1/4-2/5-1/5-2/6-1/ ASPM Support	This option enables or disables the ASPM support for all CPU downstream devices.
SR-IOV Support	If system has SR-IOV capable PCIe Devices, this option Enables or Disables Single Root IO Virtualization Support.
Restore AC Power Loss	Select the power state after a power failure. If [Power Off] is selected, the power will remain off when the power recovers. If [Power On] is selected, the system will start to boot up.
Restore AC Power Current State	This allows you to restore AC Power Current State.

4.4.4 Storage Configuration

Storage Configuration	
SATA Controller	Use this item to enable or disable SATA Controllers.
SATA Mode Selection	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive. Press <Ctrl+I> to enter RAID ROM during UEFI POST process.
SATA ALPM	Use this item to enable or disable Aggressive Link Power Management.
sSATA Controller	Use this item to enable or disable SATA Controllers.
sSATA/M.2_SATA Mode Selection	Identify the SATA/M.2_SATA port is connected to Solid State Drive or Hard Disk Drive. Press <Ctrl+I> to enter RAID ROM during UEFI POST process.
sSATA ALPM	Use this item to enable or disable Aggressive Link Power Management.

4.4.5 NVMe Configuration

NVMe Configuration	
The NVMe Configuration displays the NVMe controller and Drive information.	

4.4.6 ACPI Configuration

ACPI Configuration	
PCIE Devices Power On	Allow the system to be waked up by a PCIE device and enable wake on LAN.
Ring-In Power On	Use this item to enable or disable Ring-In signals to turn on the system from the power-soft-off mode.
RTC Alarm Power On	Use this item to enable or disable RTC (Real Time Clock) to power on the system.

4.4.7 USB Configuration

USB Configuration	
Legacy USB Support	Use this option to enable or disable legacy support for USB devices. The default value is [Enabled].

4.4.8 Super IO Configuration

Super IO Configuration	
Serial Port 1 Configuration	Use this item to set parameters of Serial Port 1 (COM1).
Serial Port	Use this item to enable or disable the serial port.
Change Settings	Use this item to select an optimal setting for Super IO device.
SOL Configuration	Use this item to set parameters of SOL.
SOL Port	Use this item to set parameters of SOL.
Change Settings	Use this item to select an optimal setting for Super IO device.

4.4.9 Serial Port Console

Serial Port Configuration	
Console Redirection	Use this option to enable or disable Console Redirection. If this item is set to Enabled, you can select a COM Port to be used for Console Redirection.
Console Redirection Settings	Use this option to configure Console Redirection Settings and specify how your computer and the host computer to which you are connected exchange information. Both computers should have the same or compatible settings.
Terminal Type	Use this item to select the preferred terminal emulation type for out-of-band management. It is recommended to select [VT-UTF8].
Bits Per Second	Use this item to select the serial port transmission speed. The speed used in the host computer and the client computer must be the same. Long or noisy lines may require lower transmission speed. The options include [9600], [19200], [38400], [57600] and [115200].
Data Bits	Use this item to set the data transmission size. The options include [7] and [8] (Bits).
Parity	Use this item to select the parity bit. The options include [None], [Even], [Odd], [Mark] and [Space].
Stop Bits	The item indicates the end of a serial data packet. The standard setting is [1] Stop Bit. Select [2] Stop Bits for slower devices.
Flow Control	Use this item to set the flow control to prevent data loss from buffer over flow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to restart the flow. Hardware flow uses two wires to send start/stop signals. The options include [None] and [Hardware RTS/CTS].
VT-UTF8 Combo Key Support	Use this item to enable or disable the VT-UTF8 Combo Key Support for ANSI/VT100 terminals.
Recorder Mode	Use this item to enable or disable Recorder Mode to capture terminal data and send it as text messages.
Resolution 100x31	Use this item to enable or disable extended terminal resolution support.
Putty Keypad	Use this item to select Function Key and Keypad on Putty.
Legacy Console Redirection Settings	Use this option to configure Legacy Console Redirection Settings and specify how your computer and the host computer to which you are connected exchange information.
Redirection COM Port	Select a COM port to display redirection of Legacy OS and Legacy OPRM Messages.
Resolution	On Legacy OS, the Number of Rows and Columns supported redirection.
Redirect After POST	When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

4.4.10 H/W Monitor

H/W Monitor	
Watch Dog Timer	This allows you to enable or disable the Watch Dog Timer. The default value is [Auto].

4.4.11 Runtime Error Logging

Runtime Error Logging	
WHEA Support	Use this item to enable or disable Windows Hardware Error Architecture.
System Error	Use this item to enable or disable System Error feature. When it is set to [Enabled], you can configure Memory Error and PCIe Error log features.
S/W Error Injection Support	When it is set to [Enabled], S/W Error Injection is supported by unlocking MSR 0x790.
Memory Error	Memory enabling and logging setup option.
Correctable Error Threshold	Correctable Error Threshold (0 - 0x7FFF) used for sparing, tagging and leaky bucket.
PCIe Corrected Error Enable	Use this item to enable or disable PCIe Correctable errors.
PCIe Corrected Error Threshold	PCIe Correctable Error Threshold (0x01-0xFF) used for sparing, tagging and leaky bucket.
PCIe Uncorrected Error Enable	Use this item to enable or disable PCIe Uncorrectable errors.
PCIe Fatal Error Enable	Use this item to enable or disable PCIe Fatal errors.

4.4.12 Intel(R) SPS

SPS
SPS screen displays the Intel(R) SPS Configuration information, such as Operational Firmware Version and Firmware State.

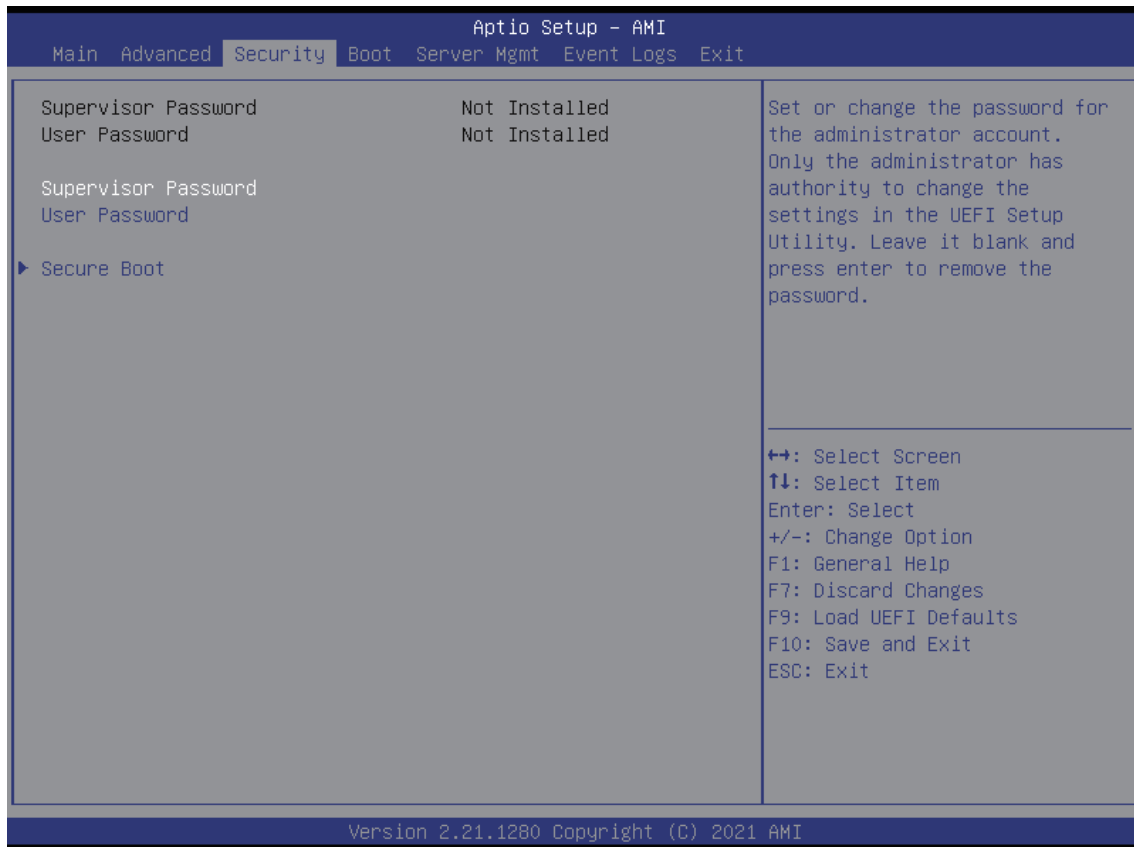
4.4.13 Intel(R) VMD technology

Intel(R) VMD technology	
Intel(R) VMD for Volume Management Device Technology	Use this item to enable or disable Intel(R) Volume Management Device Technology in this Stack. When [Enabled], users are allowed to configure the options below.
VMD port 1A/1C/2A/2B/2C/ 2D/3A/3B/3C/3D/ 4A/4B/4C/4D	Use this item to enable or disable Intel(R) Volume Management Device Technology on specific root port.
Hot Plug Capable	Use this item to enable or disable Hot Plug for PCIe Root Ports 3A-3D.

4.4.14 Tls Auth Configuration

H/W Monitor	
Server CA Configuration	Press <Enter> to configure Server CA.
Client Cert Configuration	Press <Enter> to configure Client Cert.

4.5 Security

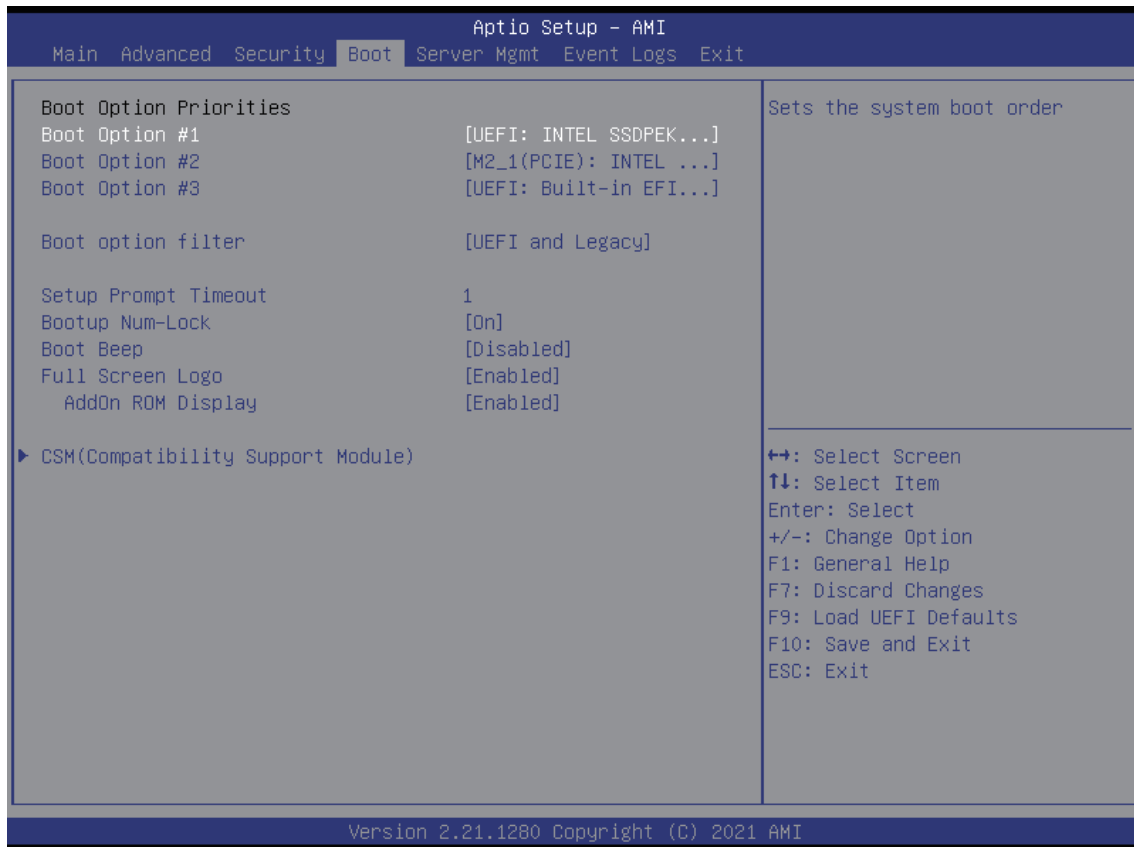


Security	
Supervisor Password	Set or change the password for the administrator account. Only the administrator has authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.
User Password	Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.
Secure Boot	Use this to enable or disable Secure Boot Control. The default value is [Disabled]. Enable to support Windows Server 2012 R2 or later versions Secure Boot.
Secure Boot Mode	Secure Boot mode selector: Standard/Custom. In Custom mode Secure Boot Variables can be configured without authentication.

4.5.1 Key Management

Key Management	
Factory Key Provision	Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.
Install Default Secure Boot Keys	Please install default secure boot keys if it's the first time you use secure boot.
Enroll Efi Image	Allow the image to run in Secure Boot mode. Enroll SHA256 hash of the binary into Authorized Signature Database (db).
Restore DB defaults	Restore DB variable to factory defaults.

4.6 Boot

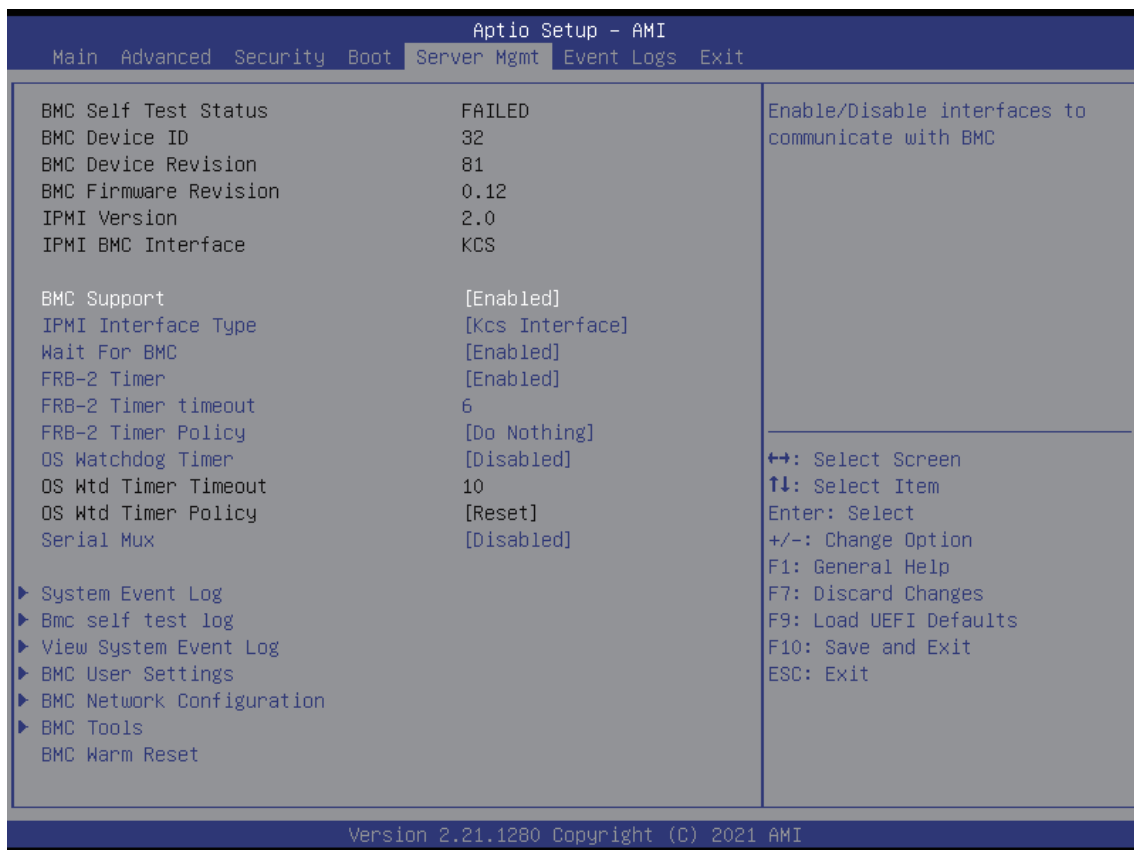


Boot	
Boot Option #1/2/3	Use this item to set the system boot order.
Boot Option Filter	This option controls Legacy/UEFI ROMs priority.
Setup Prompt Timeout	Configure the number of seconds to wait for the UEFI setup utility.
Bootup Num-Lock	If this item is set to [On], it will automatically activate the Numeric Lock function after boot up.
Boot Beep	Select whether the Boot Beep should be turned on or off when the system boots up. Please note that a buzzer is needed.
Full Screen Logo	Use this item to enable or disable OEM Logo. The default value is [Enabled].
Add On ROM Display	Use this option to adjust AddOn ROM Display. If you enable the option "Full Screen Logo" but you want to see the AddOn ROM information when the system boots, please select [Enabled]. Configuration options: [Enabled] and [Disabled]. The default value is [Enabled].

4.6.1 CSM Parameters

CSM Parameters	
CSM	Enable to launch the Compatibility Support Module. Please do not disable unless you're running a WHCK test. If you are using Windows 10 64-bit and all of your devices support UEFI, you may also disable CSM for faster boot speed.
Launch Other Storage OpROM Policy	Select UEFI only to run those that support UEFI option ROM only. Select Legacy only to run those that support legacy option ROM only. Select Do not launch to not execute both legacy and UEFI option ROM.
Launch Video OpROM Policy	Select UEFI only to run those that support UEFI option ROM only. Select Legacy only to run those that support legacy option ROM only. Select Do not launch to not execute both legacy and UEFI option ROM.
OCP3 Slot OpROM	Use this item to select slot storage and Network Option ROM policy. In Auto option, the default is Disabled with NVMe device, but it is Legacy with other devices. (This item can't select Video Option ROM policy.)
PCIE1/2/3 Slot OpROM	Use this item to select slot storage and Network Option ROM policy. In Auto option, the default is Disabled with NVMe device, but it is Legacy with other devices. (This item can't select Video Option ROM policy.)
SLIM1/2/3/4/5/6 Slot OpROM	Use this item to select slot storage and Network Option ROM policy. In Auto option, the default is Disabled with NVMe device, but it is Legacy with other devices. (This item can't select Video Option ROM policy.)
M2_1 Slot OpROM	Use this item to select slot storage and Network Option ROM policy. In Auto option, the default is Disabled with NVMe device, but it is Legacy with other devices. (This item can't select Video Option ROM policy.)

4.7 Server Management



Server Mgmt	
BMC Support	Enable/Disable interfaces to Communicate with BMC.
IPMI Interface Type	Type of Interface to communicate BMC from HOST.
Wait For BMC	Wait For BMC response for specified time out. BMC starts at the same time when BIOS starts during AC power ON. It takes around 90 seconds to initialize Host to BMC interfaces.
FRB-2 Timer	Enable or Disable FRB-2 timer(POST timer).
FRB-2 Timer Timeout	Enter value between 1 to 30 min for FRB-2 Timer Expiration.
FRB-2 Timer Policy	Configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled.
OS Watchdog Timer	If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follow the OS Boost Watchdog Timer policy.
Serial Mux	Press <Enter> to enable or disable Serial Mux configuration.

4.7.1 System Event Log

System Event Log	
SEL Components	Change this to enable or disable event logging for error/progress codes during boot.
Erase SEL	Use this to choose options for erasing SEL.
When SEL is Full	Use this to choose options for reactions to a full SEL.
Log EFI Status Codes	Use this item to disable the logging of EFI Status Codes or log only error code or only progress code or both.

4.7.2 BMC Self Test Log

BMC Self Test Log	
Erase SEL	Use this to choose options for erasing SEL.
When SEL is Full	Use this to choose options for reactions to a full SEL.

4.7.3 View System Event Log

View System Event Log	
Press <Enter> to view the System Event Log Records.	

4.7.4 BMC User Settings

BMC User Settings	
Add User	Press <Enter> to add a user.
Delete User	Press <Enter> to delete a user.
Change User Settings	Press <Enter> to change user settings.

4.7.5 BMC Network

BMC Network	
BMC Out of Band Access	Enabled/Disabled BMC Out of band Access.
Manual Setting IPMI LAN	If [No] is selected, the IP address is assigned by DHCP. If you prefer using a static IP address, toggle to [Yes] and the changes take effect after the system reboots. The default value is [No].
Configuration Address Source	Select to configure BMC network parameters statically or dynamically(by BIOS or BMC). Configuration options: [Static] and [DHCP].
IPV6 Support	Enable or Disable LAN1 IPV6 Support.
Manual Setting IPMI LAN(IPV6)	Select to configure LAN channel parameters statically or dynamically(by BIOS or BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

4.7.6 BMC

BMC Network	
Load BMC Default Settings	Use this item to load BMC default settings.
KCS control	Select the KCS interface state after POST end. If [Enabled] is selected, the BMC will remain KCS interface after POST stage. If [Disabled] is selected, the BMC will disable KCS interface after POST stage.
BMC Warm Reset	Press <Enter> to do Warm Reset BMC.

4.8 Event Logs



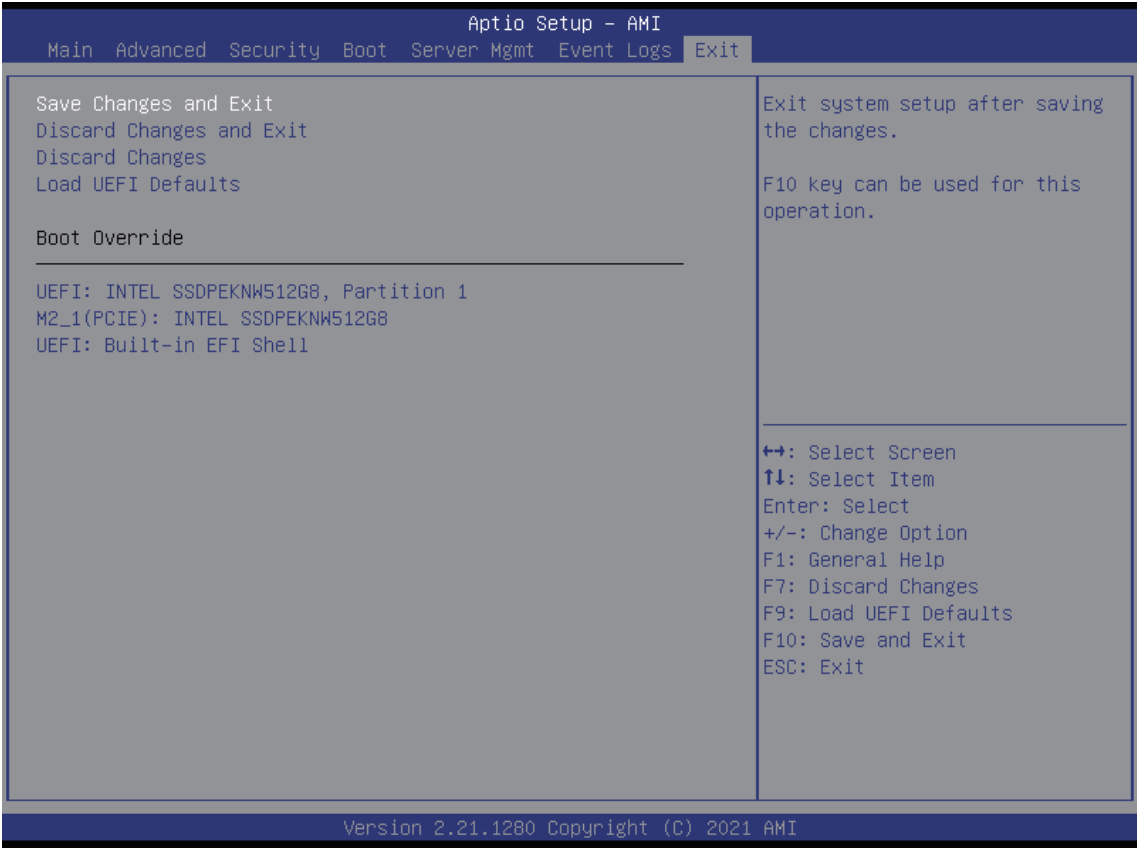
4.8.1 Change Smbios Event Log Settings

Change Smbios Event Log Settings	
Smbios Event Log	Use this item to enable or disable all features of the SMBIOS Event Logging during system boot.
Erase Event Log	The options include [No], [Yes, Next reset] and [Yes, Every reset]. If Yes is selected, all logged events will be erased.
When Log is Full	Use this item to choose options for reactions to a full Smbios Event Log. The options include [Do Nothing] and [Erase Immediately].
Log System Boot Event	Choose option to enable/disable logging of System boot event.

4.8.2 View Smbios Event Log

View Smbios Event Log
Press <Enter> to view the Smbios Event Log records.

4.9 Exit

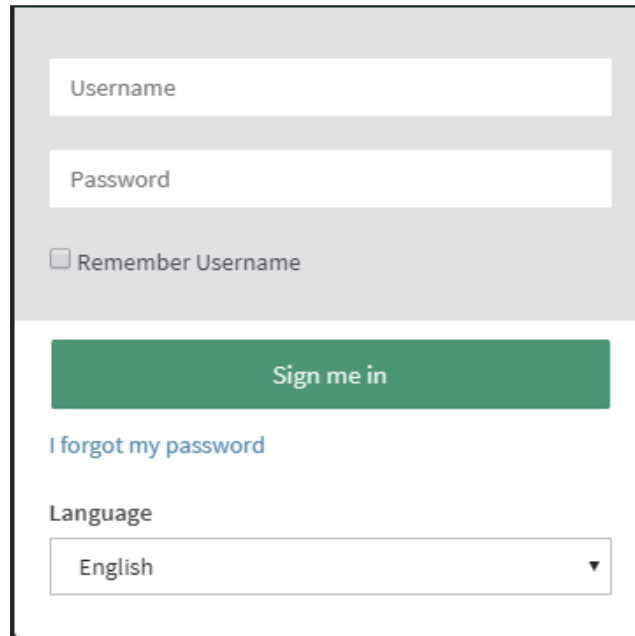


Exit	
Save Changes and Exit	When you select this option, the following message “Save configuration changes and exit setup?” will pop-out. Press <F10> key or select [Yes] to save the changes and exit the UEFI SETUP UTILITY.
Discard Changes and Exit	When you select this option, the following message “Discard changes and exit setup?” will pop-out. Press <ESC> key or select [Yes] to exit the UEFI SETUP UTILITY without saving any changes.
Discard Changes	When you select this option, the following message “Discard changes?” will pop-out. Press <F7> key or select [Yes] to discard all changes.
Load UEFI Defaults	Load UEFI default values for all the setup questions. F9 key can be used for this operation.

Chapter 5. BMC Configuration Settings

5.1 Login Information

Initial access prompts you to enter the User Name and Password. A sample screenshot of the login screen is given below.



The fields are explained as follows:

Username: Enter your username in this field.

Password: Enter your password in this field.

Remember Username: Check this option to remember your login Username. If you select this option, the browser will save your credentials internally in its memory, and when you open that site the next time, it will auto-fill Username for you.

Sign me in: After entering the required credentials, click the [Sign me in](#) to login.

I Forgot my Password: If you forget your password, you can generate a new password using this link.

Language: Select the language of Web GUI, you can choose English, Traditional Chinese or Simplified Chinese.



NOTE

Default username and password

- Username: admin
- Password: 123456789

Password Change

When the BMC is updated, the default username and password will be changed to

- Username: admin
- Password: password

Enter to change the user's password.

Chassis ID

Before updating the BMC, enter the command below to login with the correct version.

```
ipmitool -I lanplus -H <ip> -U <user> -P <password>raw 0x3a 0xaa 0x53 0x42 0x32 0x30 0x32 0x2d 0x41 0x36
```

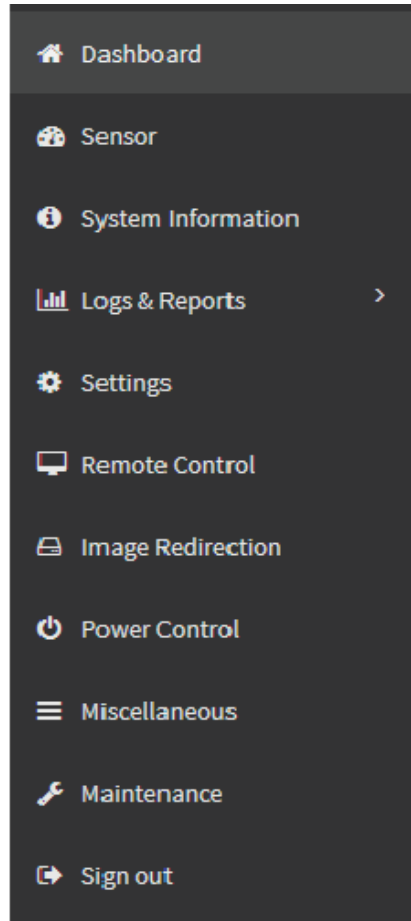
5.2 Web GUI

5.2.1 Menu Bar

The menu bar displays the following.

Firmware Information will be displayed with the latest version, date and time details.

- Dashboard
- Sensor
- System Information
- Logs & Reports
- Settings
- Remote Control
- Image Redirection
- Power Control
- Miscellaneous
- Maintenance
- Sign out

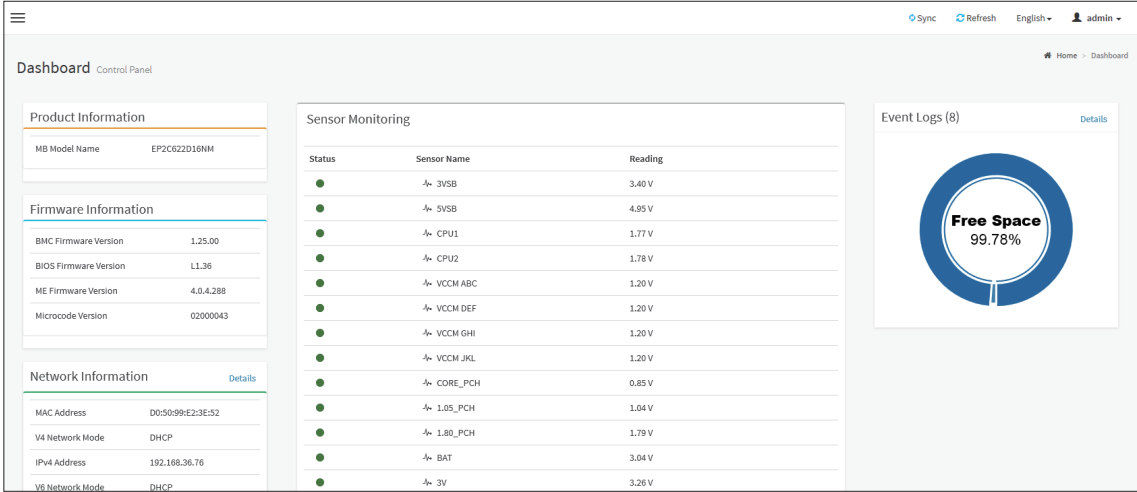
**NOTE**

For further details about the BMC, please refer to SB202-A6 BMC Manual for reference.
AIC® website link: <https://www.aicpc.com/en/productdetail/51328>.

5.2.2 Dashboard

The Dashboard page gives the overall information about the status of a device.

To open the Dashboard page, click [Dashboard](#) from the menu bar. A sample screenshot of the Dashboard page is shown below.



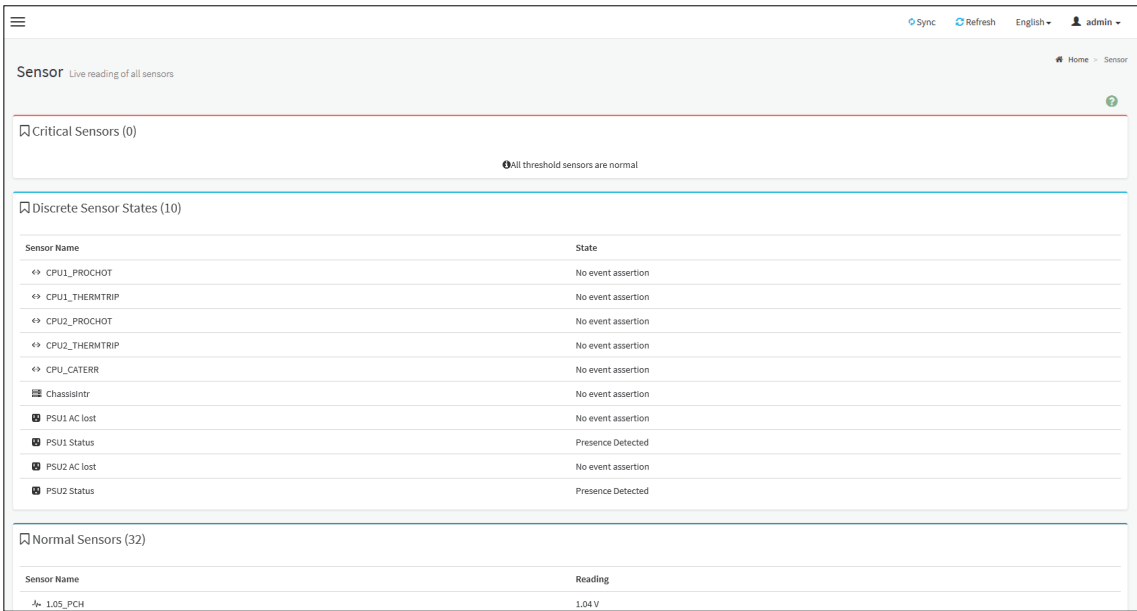
A brief description of the Dashboard page is given below.

- Firmware Information
- Network Information
- Sensor Monitoring
- Event Logs

5.2.3 Sensor

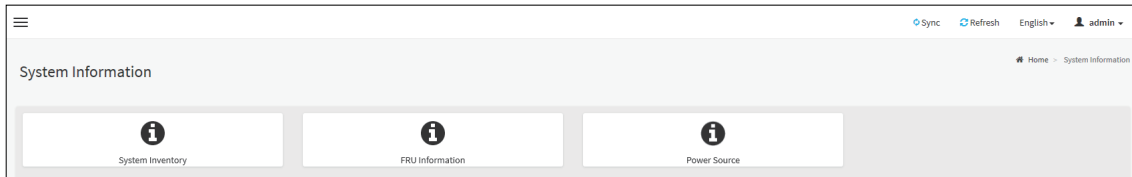
The Sensor Reading page displays all the sensor related information.

To open the Sensor Reading page, click [Sensor](#) from the menu. Click on any sensor to show more information about that particular sensor, including thresholds and a graphical representation of all associated events. A screenshot of Sensor Reading page is given below.



5.2.4 System Information

This group of pages allows you to view system information.



A brief description of the system Information page is given below.

- System Inventory
- FRU Information
- Power Source

5.2.5 Logs & Reports

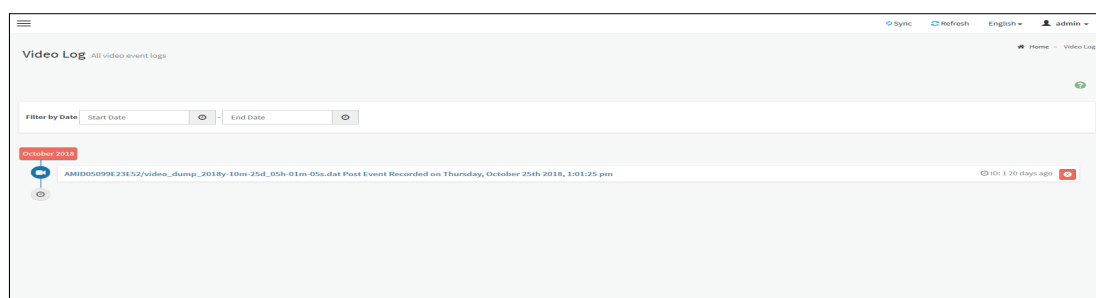
IPMI Event Log

This page displays the list of event logs occurred by the different sensors on this device. Double click on a record to see the details of that entry. You can use the sensor type or sensor name filter options to view those specific events or you can also sort the list of entries by clicking on any of the column headers.

Event ID	Time Stamp	Sensor Name	Sensor Type	Description
14	10/25/2018, 12:58:14	SPS / ME	Boot Up	NIM OEM Record - Asserted
13	10/25/2018, 12:58:13	SPS / ME	Microcontroller / Coprocessor	transition to Running - Asserted
12	10/25/2018, 12:58:07	CPU_CATERR	Processor	State Asserted - Deasserted
11	10/25/2018, 12:57:28	CPU_CATERR	Processor	State Asserted - Asserted
10	10/25/2018, 10:21:23	CPU1_FAN1	Fan	Lower Non-critical - going low - Deasserted
9	10/25/2018, 10:21:21	CPU1_FAN1	Fan	Lower Non-critical - going low - Asserted
8	10/25/2018, 10:08:09	BIOS	System Event	Timestamp Clock Synchron - Asserted
7	10/25/2018, 10:08:08	Unknown	System Event	Timestamp Clock Synchron - Asserted
6	11/14/2018, 17:56:58	Unknown	System Event	Timestamp Clock Synchron - Asserted
5	11/14/2018, 17:56:58	BIOS	System Event	Timestamp Clock Synchron - Asserted
4	Pre-init Timestamp	PSU2 Status	Power Supply	Presence detected - Asserted
3	Pre-init Timestamp	PSU1 Status	Power Supply	Presence detected - Asserted
2	Pre-init Timestamp	PSU1 VIN	Voltage	Upper Critical - going high - Deasserted
1	Pre-init Timestamp	PSU1 VIN	Voltage	Upper Critical - going high - Asserted

Video Log

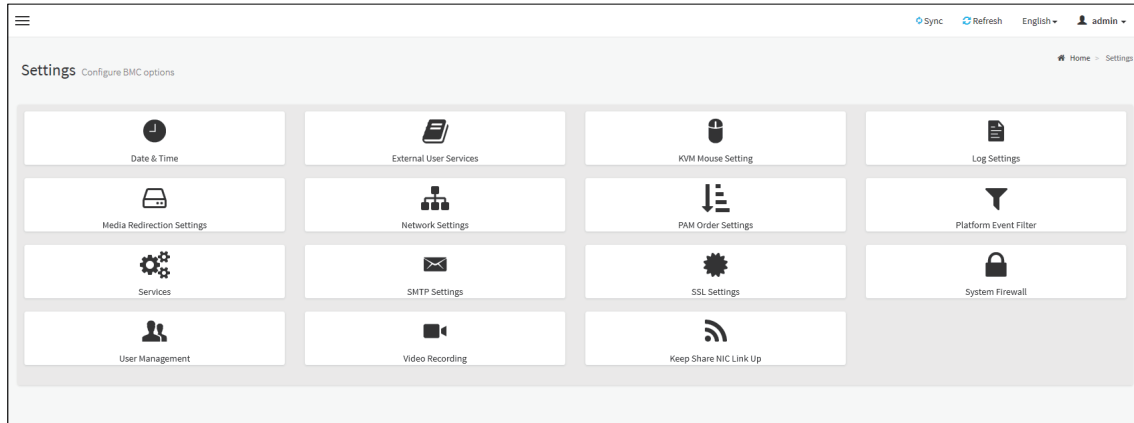
This page displays the list of video logs occurred by the different events on this device.



Filter By Date: Filtering can be done by selecting Start Date and End Date.

5.2.6 Settings

This group of pages allows you to access various configuration settings. A screenshot of Configuration Group menu is shown below.



- Data & Time
- External User services
- KVM Mouse Settings
- Log Settings
- Media Redirection
- Network Settings
- PAM Order Settings
- Platform Event Filter
- Services
- SMTP Settings
- SSL Settings
- System Firewall
- User Management
- Video Recording
- Keep Share NIC Link Up

5.2.7 Remote Control

This page is used to launch the remote console redirection.



Launch KVM: Click the button to open remote control KVM page.

Launch Java KVM: Click the button to open Java KVM application.

5.2.8 Image Redirection

This page is used to configure the images into BMC for redirection.

Remote Media

This page is used to configure the remote images into BMC for redirection.

Media Type	Media Instance	Image Name	Redirection Status	Connected Server Session	
CD/DVD	0	rhel-server-6	~	N/A	▶ ▲
CD/DVD	1	rhel-server-6	~	N/A	▶ ▲
CD/DVD	2	rhel-server-6	~	N/A	▶ ▲
CD/DVD	3	rhel-server-6	~	N/A	▶ ▲
Floppy	0		~	N/A	▶ ▲
Floppy	1		~	N/A	▶ ▲
Floppy	2		~	N/A	▶ ▲
Floppy	3		~	N/A	▶ ▲
Hard disk	0		~	N/A	▶ ▲
Hard disk	1		~	N/A	▶ ▲
Hard disk	2		~	N/A	▶ ▲
Hard disk	3		~	N/A	▶ ▲

Media Type: Displays type of Media such as CD/DVD, Floppy and Hard-disk.

Media Instance: Displays total media instance count.

Image Name: Displays the default recovery image name on the server.

Status: Displays the status of the media.

Session Index: Displays Media Server Session Index.

Start/Stop Redirection: To start or stop media redirection.

Pause: To pause the media redirection.

Refresh Image List: To get latest Image lists from the Remote Storage.

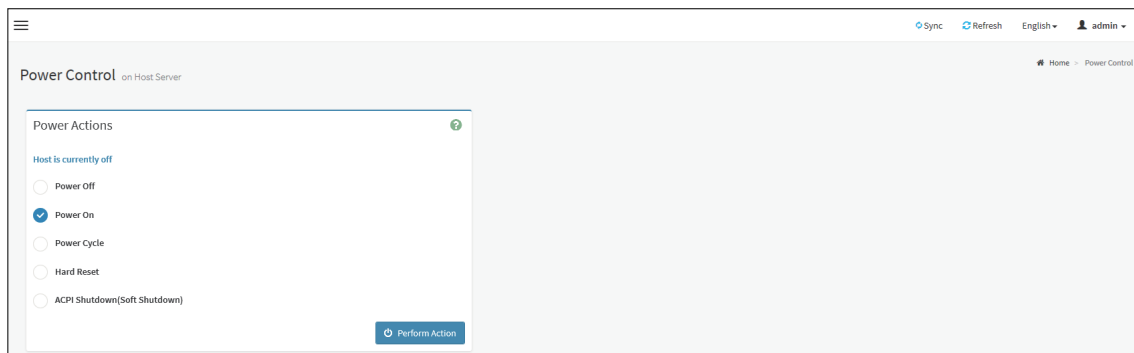


NOTE

To configure the image, you need to enable Remote Media support first.

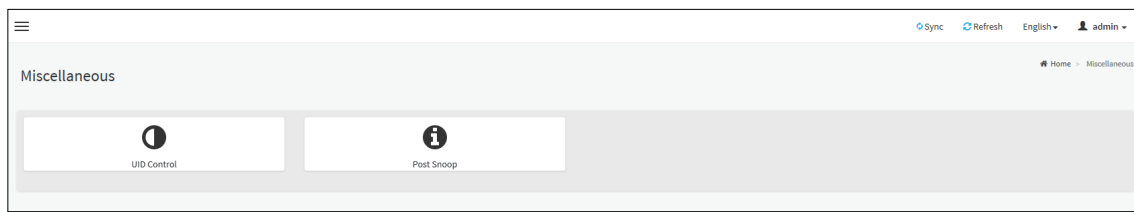
5.2.9 Power Control

This page is used to view and control the power of the server.



5.2.10 Miscellaneous

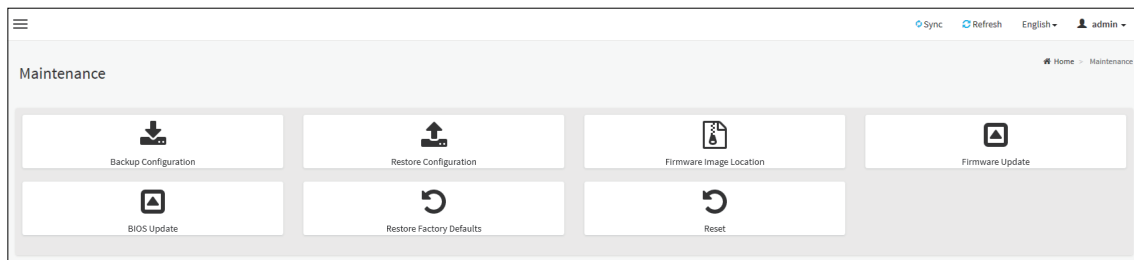
This page is used to configure miscellaneous settings.



- UID Control
- Post Snoop

5.2.11 Maintenance

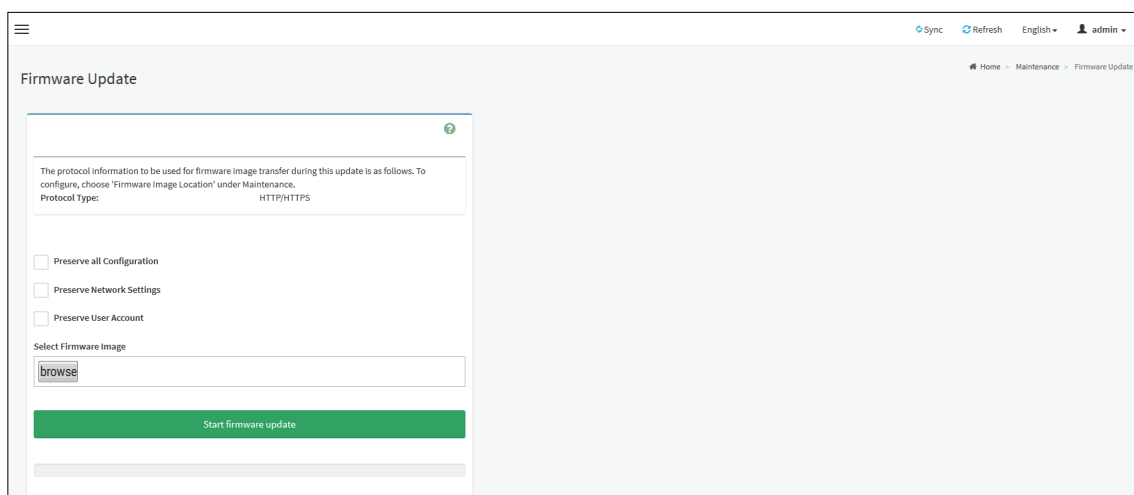
This page is used to do maintenance tasks on the device.



- Backup Configuration
- Restore Configuration
- Firmware Image Location
- Firmware Update
- BIOS Update
- Restore Factory Defaults
- Reset
- Sign Out

5.2.11.1 Firmware Update

This page is used to update BMC firmware.



Preserve all Configuration: To preserve all configuration.

Preserve Network Settings: To preserve network settings.

Preserve User Account: To preserve user accounts.

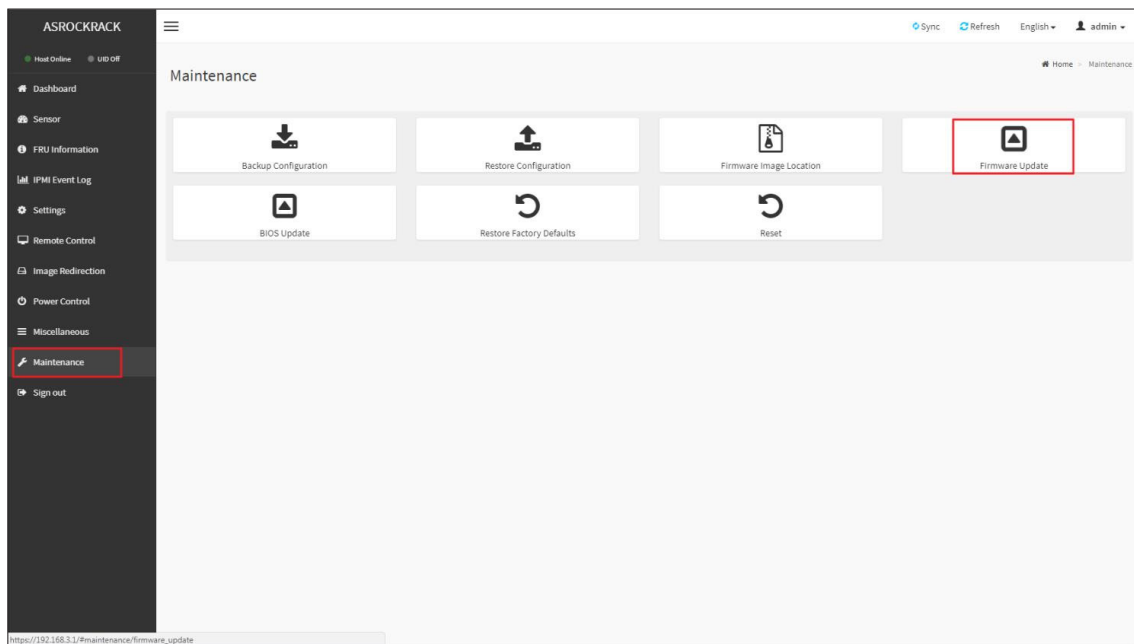
Select Firmware Image: To Select the firmware image to be uploaded.

Start Firmware Update: To Start the firmware update.

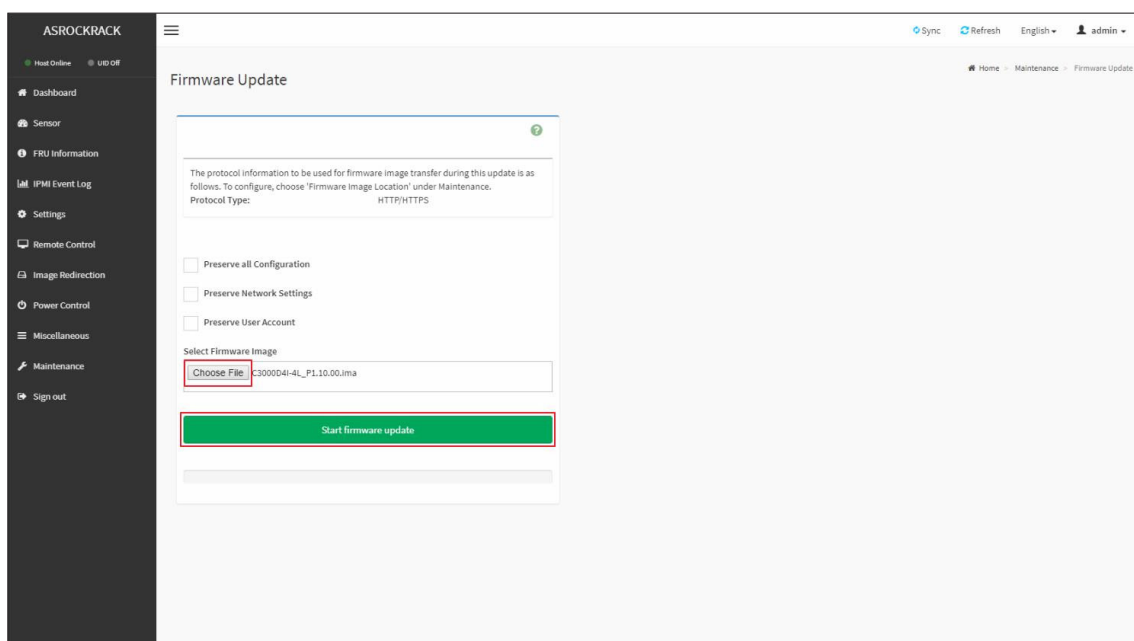
- **Steps to update BMC firmware via IPMI webUI**
(Supports server motherboard built with BMC AST2500 controller)

BMC Firmware Update

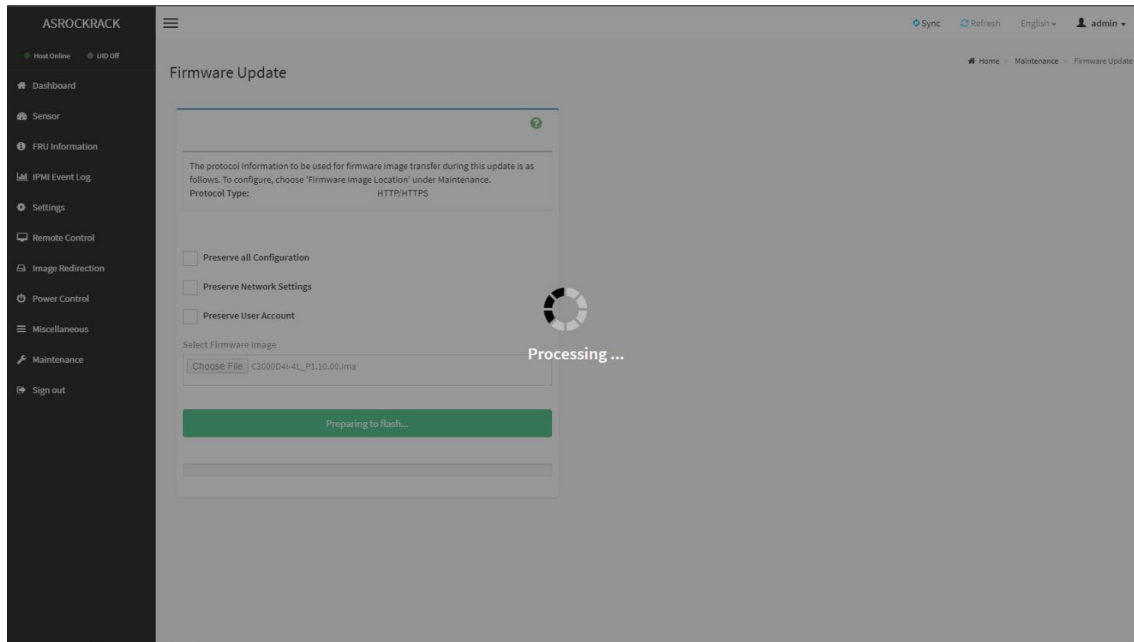
[Maintenance] -> [Firmware Update]



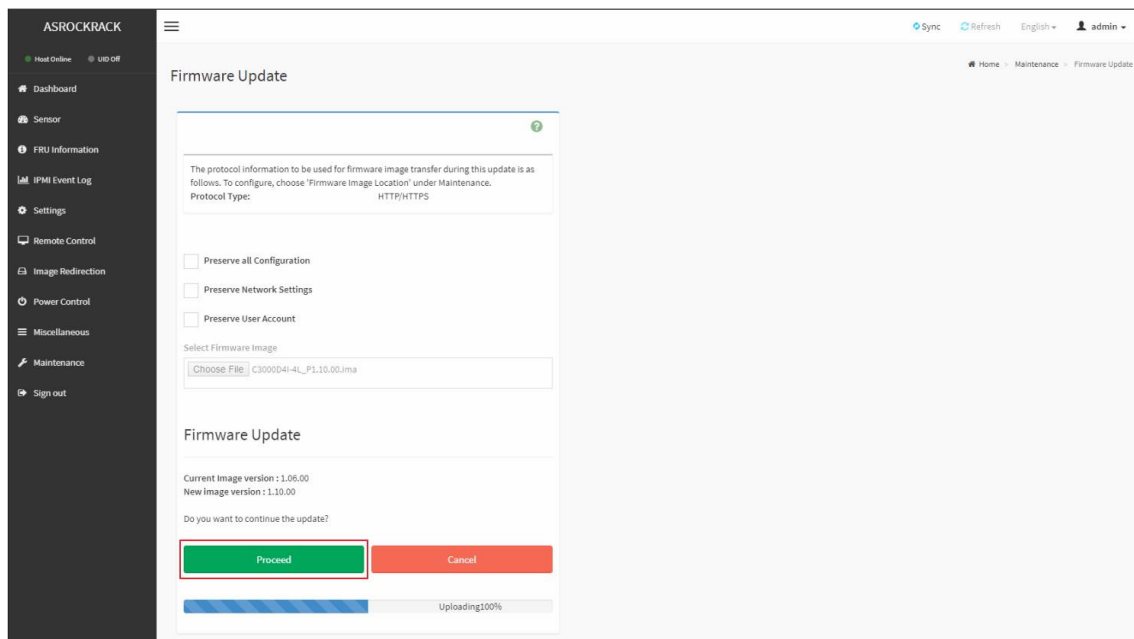
This wizard will take you through the process of firmware upgrades. Click the **“Choose File”** to select the BMC FW file. Then click **“Start firmware update”** for the upgrade process.



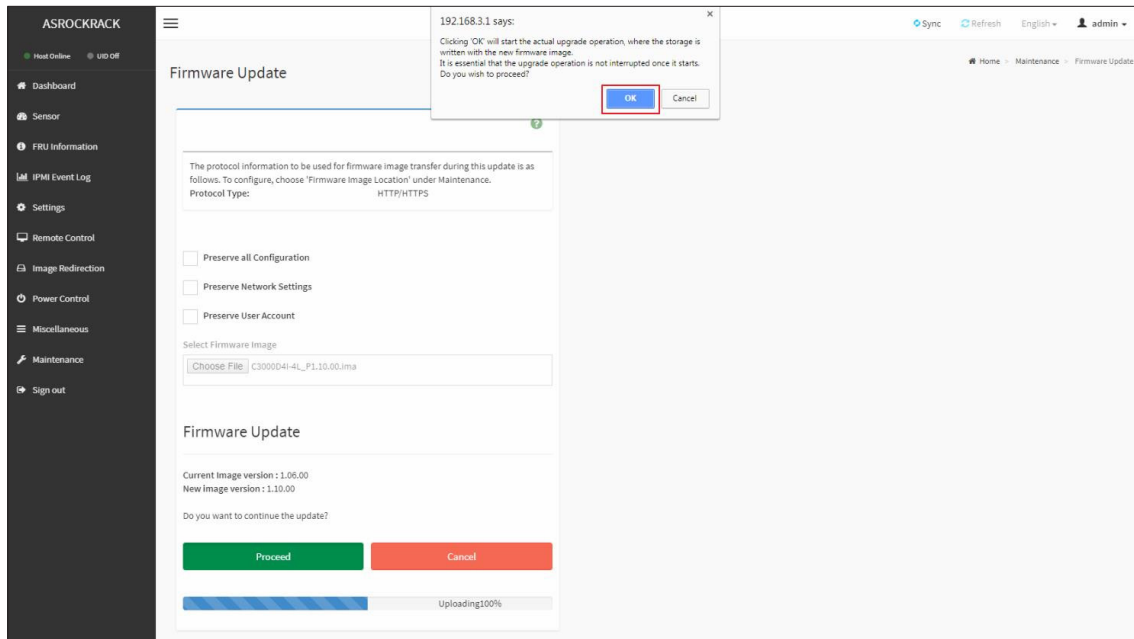
FW verification processing, please wait a few second.



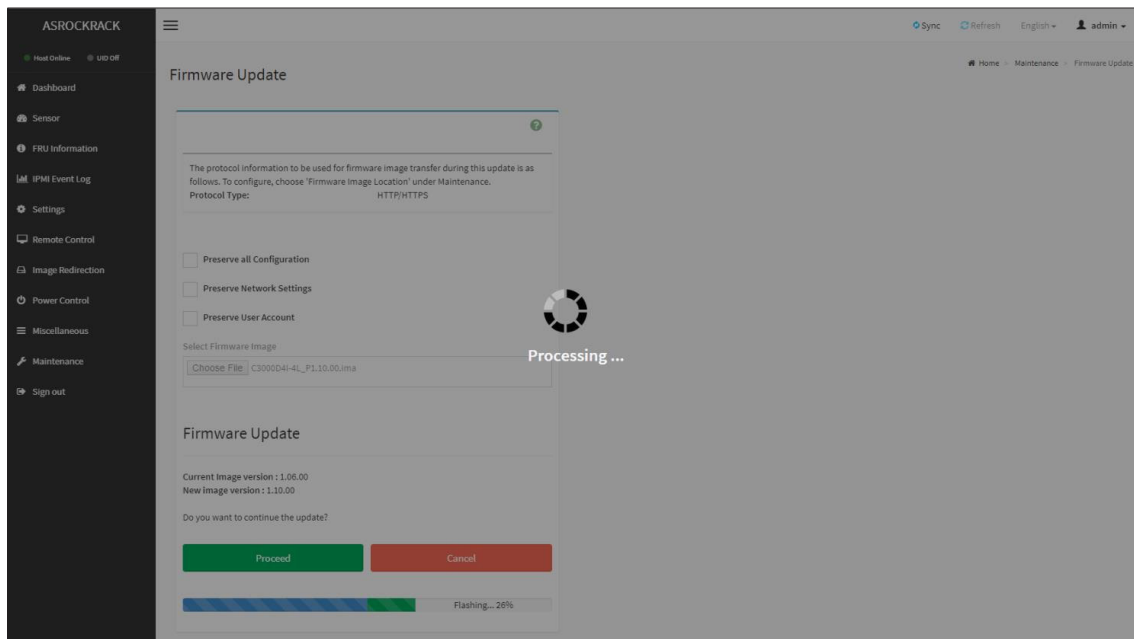
Click “Proceed” to continue the BMC FW upgrade process.



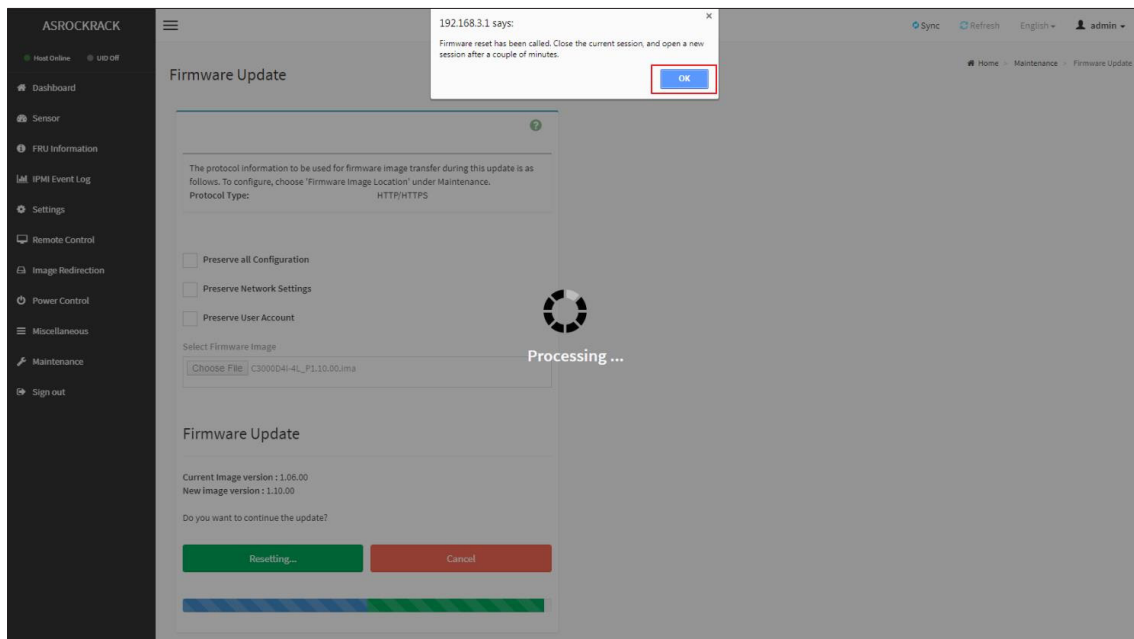
Click “OK” to start the actual BMC FW upgrade operation.



FW upgrade is processing, please wait a few second.



BMC FW upgrade process finished. Click “OK” and reboot the system.

**NOTE**

Clear the browser cookies after the BMC FW upgrade process finished.

5.2.11.2 BIOS Firmware Update

This page is used to update BIOS firmware.

The screenshot shows the 'BIOS Update' page in the BMC web interface. At the top, there are links for 'Sync', 'Refresh', 'English', and 'admin'. Below the header, the page title 'BIOS Update' is displayed. The main content area contains a message: 'Upgrade BIOS of the host. Please select BIOS image and press 'Start BIOS update' to start the BIOS update procedure.' Below this, there are two sections: 'Configuration' with a checkbox for 'Preserve BIOS configuration' (unchecked), and 'Option' with a checked checkbox for 'Immediately shutdown server to flash BIOS'. Under the 'Option' section, there is a 'Select BIOS Image' section with a file selection button labeled '瀏覽...' and a text box showing '未選擇檔案'. At the bottom, there is a large green button labeled 'Start BIOS update'.

Preserve BIOS configuration: To preserve BIOS configuration.

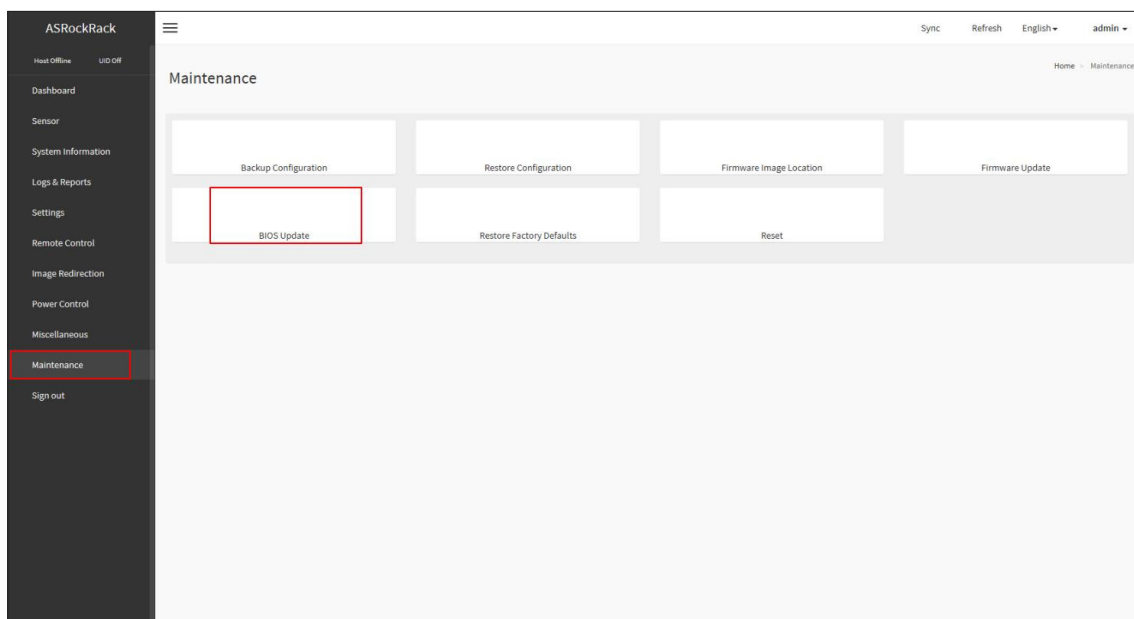
Immediately shutdown server to flash BIOS: To shutdown server immediately to flash BIOS.

Start Firmware Update: To Start the BIOS update.

- **Steps to update BIOS firmware via IPMI webUI**
(Supports server motherboard built with BMC AST2500 controller)

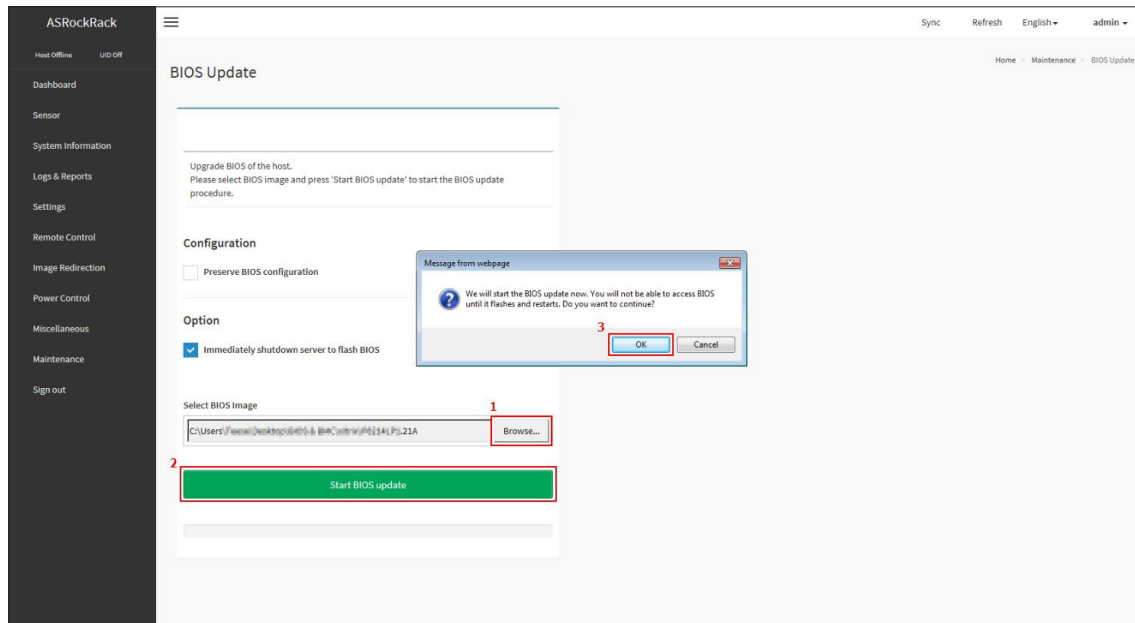
BIOS Firmware Update

[Maintenance] -> [BIOS Update]

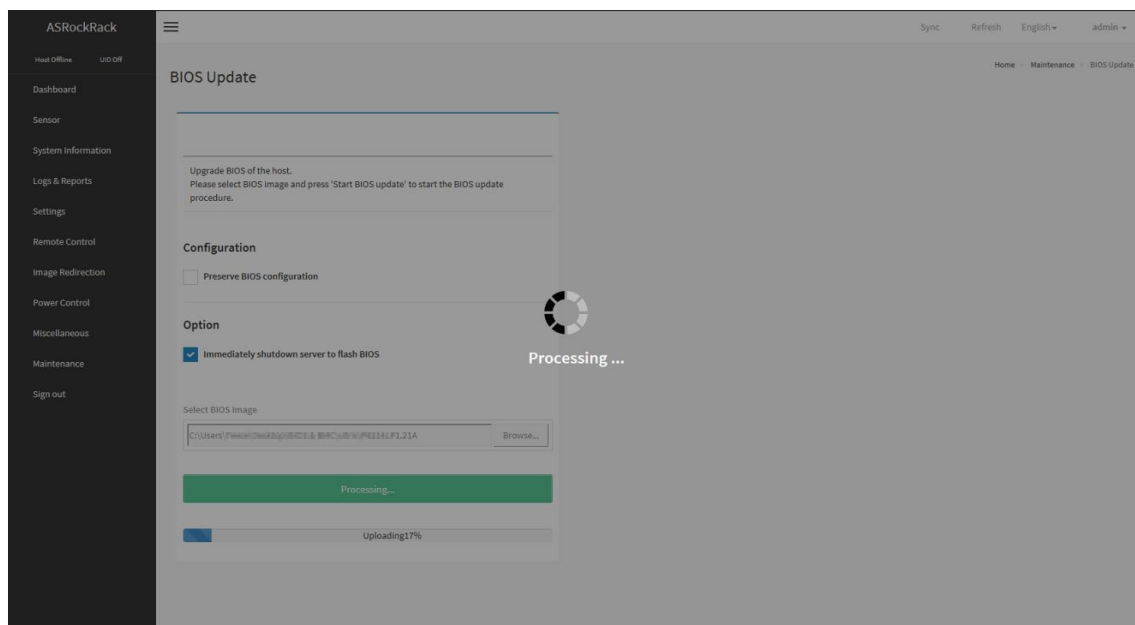


This wizard will take you through the process of firmware upgrades.

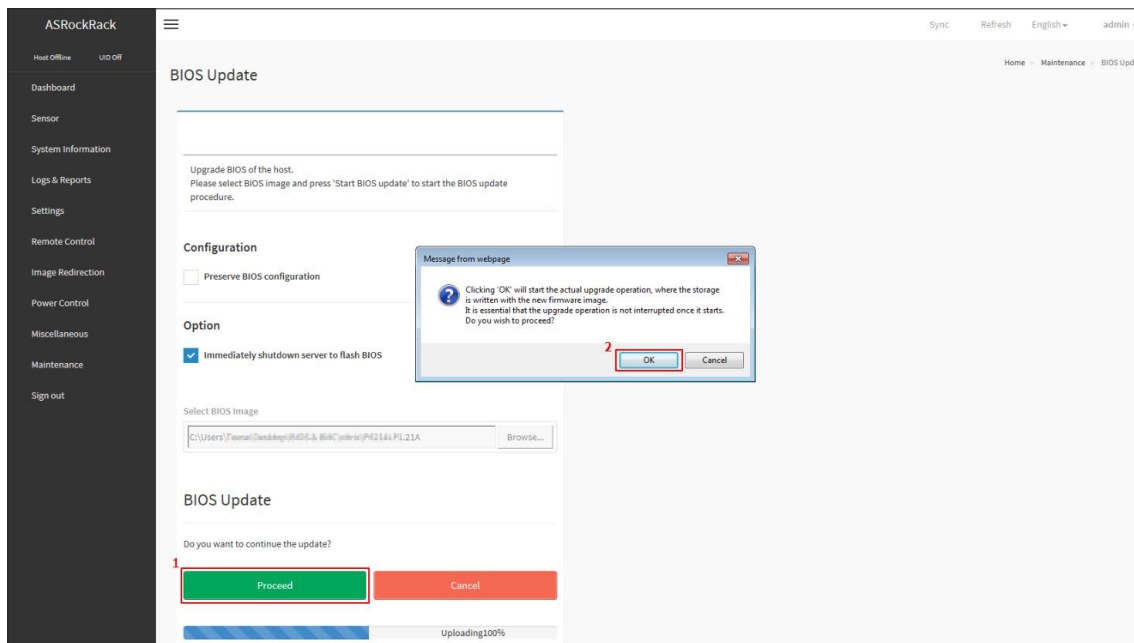
Click the **“Browse...”** to select the BIOS FW file. Then click **“Start BIOS update”** for the upgrade process. And click **“OK”** if confirmed to continue the process.



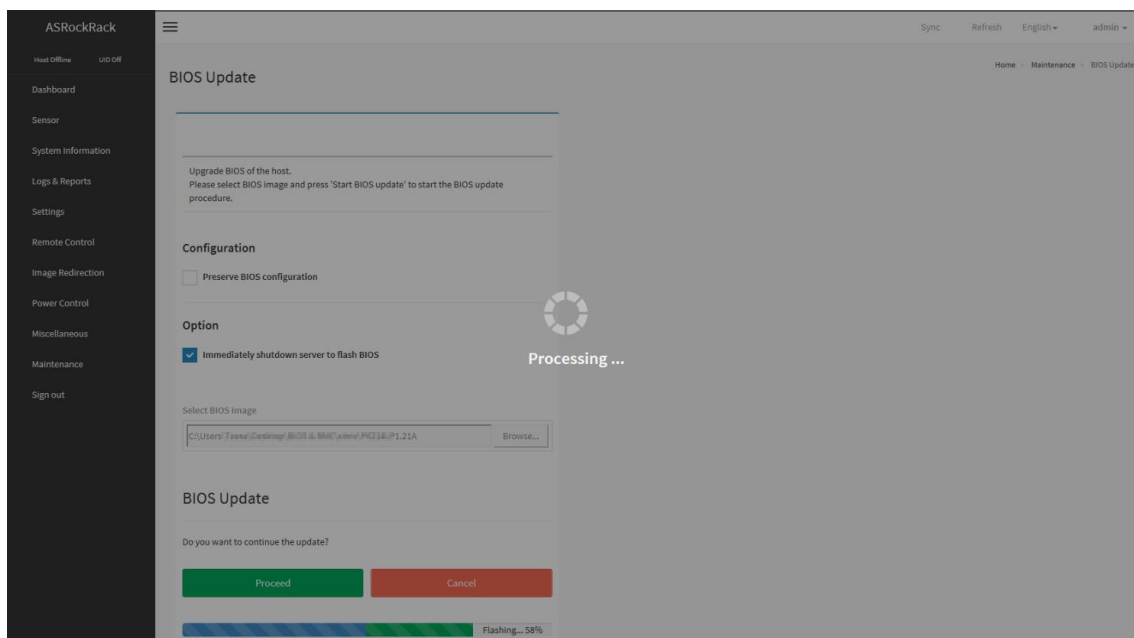
FW verification processing, please wait a few second.



Click **“Proceed”** to continue the BIOS FW upgrade process. Then click **“OK”** to start the actual BIOS FW upgrade operation.



FW upgrade is processing, please wait a few second.



BIOS FW upgrade process finished. Click **“OK”** to reload the webpage.



NOTE

Clear the browser cookies after the BIOS FW upgrade process finished.

Chapter 6. Technical Support



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